

TECHNICAL BULLETIN

[Issue No.] T99-0050-N

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[Title] Production discontinuation of MELSEC-A/QnA (large type) series

[Date of Issue] February 2005 (Ver. N: January 2013)

[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Thank you for your continued support of Mitsubishi programmable logic controllers, MELSEC-A/QnA series. MELSEC-A and QnA large type series have been used in many production sites with rise of Japanese production business for about 20 years since they were released in 1985.

However, unfortunately, we are now unable to continue the production of them and have decided to discontinue them. This technical bulletin is to provide the information regarding this production discontinuation.

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[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

1. Models to be discontinued

Production will be discontinued for the large type (large/medium scale-compatible) CPU modules (AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU and A273UHCPU), power supply module (partial), base unit, I/O module, special function module, network module and relevant products of the A/QnA series, and the products relevant to a made-to-order production based on the large-sized A series products.

For the details of programmable controller products to be discontinued, refer to page10 or later. Regarding the details of the motion controller A273UHCPU and other relevant models to be discontinued, refer to "Sales and Service" (Issued in May 2005) for the motion controller.

2. Production discontinuation

- Transition to made-to-order : October 1, 2005
- Order deadline : Through August 2006
- Final production : Through September 2006

Point
(1) The production of the small type (small/medium scale-compatible) AnS/Q2AS series modules will be discontinued at the end of September 2014. For details, please refer to the technical bulletin "Production discontinuation of MELSEC-AnS/QnAS (small type) series and MELSEC-I/OLINK" (FA-A-0142).
(2) The production of the large type MELSECNET/10 network module will be discontinued at the end of September 2014. For details, please refer to the technical bulletin "Production discontinuation of MELSEC-A/QnA (large type) series MELSECNET/10 network modules" (FA-A-0141).

3. Reasons for discontinuing production

Main electronic components of programmable controllers, i.e., semiconductor components (micro computer, memory, ASIC, etc.) are now absolutely difficult to obtain, as they are produced based on the stricter process rules and the contributions to environmental conservation, such as lead-free, compliance to RoHS directives, are required. We have been producing A/QnA series products by securing the stock of these obsolete components. However, the stock is about to run out, and we have extreme difficulty to maintain the production system and product quality.

4. Repair acceptance

- Repair acceptance : Through September 2013 (for 7 years after production discontinuation)

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5. Spare parts

As in Section 2, we will discontinue production, but for the modules listed in Section 13, we will continue production for 2 years after discontinuation on a made-to-order basis for spare parts. (Final order deadline will be through August 2008)

For products we will continue production on made-to-order basis for spare parts usage is as following:

- Start of order acceptance : September 1, 2006
- Order acceptance : Through August 2008
- Final production : Through September 2008

As for repair for spare parts that will continue production, we will accept repair till the end of September 2015.

	2005	06	07	08	09	2010	11	12	13	14	15	16
Discontinuation Schedule	<div style="display: flex; justify-content: space-between; align-items: center;"> <div style="width: 40%; text-align: left;"> <p>▲ 2006/9 Production Discontinuation</p> </div> <div style="width: 20%; text-align: center;"> <p>← Repair Acceptance (7years) →</p> </div> <div style="width: 40%; text-align: right;"> <p>▲ 2013/9 Finish Repair Acceptance</p> </div> </div>											
Subsidiary Product Schedule	<div style="display: flex; justify-content: space-between; align-items: center;"> <div style="width: 30%; text-align: left;"> <p>▲ 2006/9 Start accepting orders</p> </div> <div style="width: 40%; text-align: center;"> <p>← Repair Acceptance (7years) →</p> </div> <div style="width: 30%; text-align: right;"> <p>▲ 2015/9 Complete Repair Acceptance</p> </div> </div>											

6. Continuous production of power supply modules/battery

As for power supplies, we will continue production of the 3 models, A61PN, A63P, and A61RP.

If using other power supplies than the above, please purchase spare parts, or consider transition to the above 3 models.

For batteries, we will continue production of the A6BAT. (Production of the A61P will be discontinued in September 2008. For details, refer to the technical bulletin "Production discontinuation of A/QnA series power supply module" (FA-A-0034).)

7. Correspondence to machine/line modifications

Mainly for usage in machine/line modifications, we will propose usage of the combination with our A-A1S module conversion adapter and A1S modules.

The A-A1S module conversion adapter has the following two models.

- A1ADP-XY: For I/O modules
- A1ADP-SP: For special function modules

For model selection of the A-A1S module conversion adapter, refer to Section 14.

(1) Applicable A-A1S module conversion adapter depends on the A1S module model.

Moreover, applicable A-A1S module conversion adapter may differ depending on the module type in I/O assignment (Input, output, special). For example, I/O assignment of the A1SI61 is "Special", and the compatible model to the A1SI61 is the A1ADP-XY.

(2) The A-A1S module conversion adapter cannot be used for some A1S modules.

Please refer to Section 10 for machine/line modifications using the A-A1S module conversion adapter.

Note that the A-A1S module conversion adapter was released in August 2006.

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[Relevant Models] AnNCP, AnACP, AnUCP, QnACP, Q4ARCP, A2C(J)CP, other models

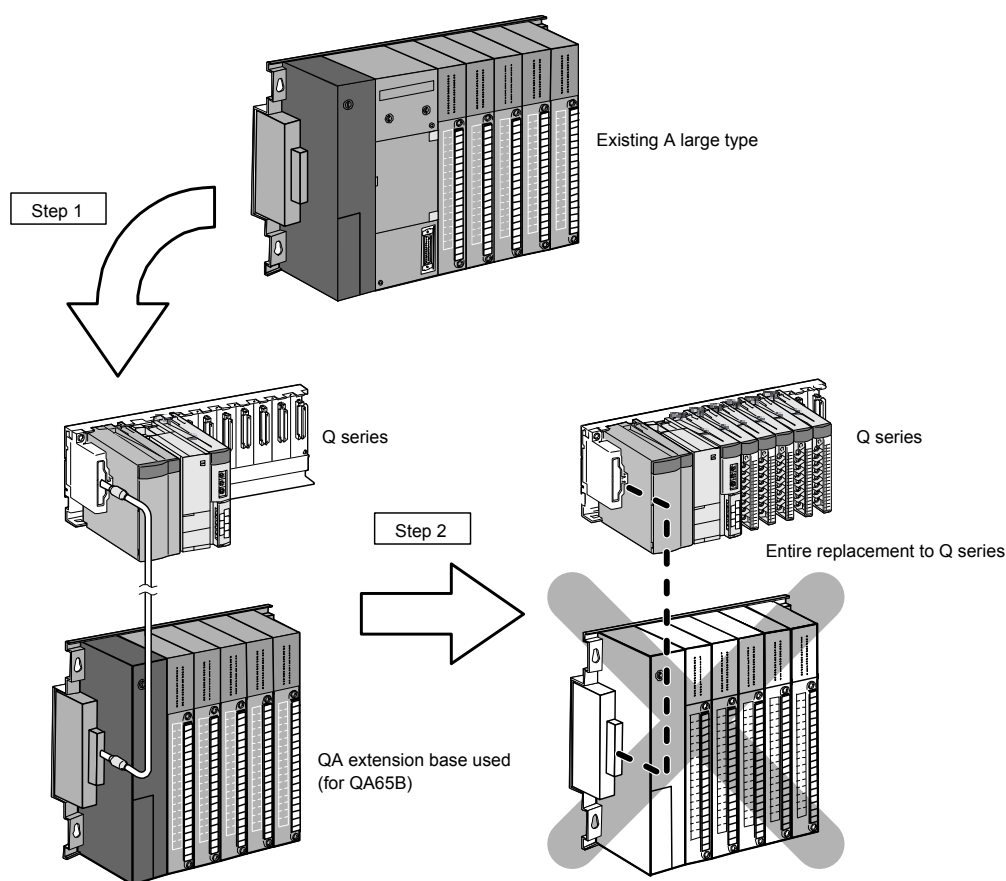
8. Gradual transition from A series to Q series (Q mode)

We propose gradual transition from A series to Q series (Q mode) by using the QA6□B type extension base unit. (The QA6□B type extension base units are compatible with High-performance CPUs and Universal model QCPUs (with a serial number (first five digits) of "13102" or later) only. Basic model QCPUs, Process CPUs, and Redundant CPUs are not compatible.)

By mounting the modules on the A (large type) base unit in the existing system on the QA6□B type extension base unit, the existing A (large type) series modules can be utilized as is in the new Q series system, which is controlled by a new Q series CPU module (Q mode). Then, gradually change the A (large type) series modules to the Q series (Q mode) modules to configure a complete Q series (Q mode) system.

For modules that can be mounted to the QA6□B type extension base unit, please refer to the QA65B/QA68B Extension Base Unit Users Manual (IB-0800158).

Note that the 8-slot type QA68B was released in May 2006.



Assets of the power supply of existing A large type, I/O module and other module can be reused + wiring is left as it is.

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other models

9. Recommendable proposals

We recommend the following solutions for A/QnA (large type) series production discontinuation.

(1) Purchase of spare parts for necessary models before order deadline described in Section 2 and 5.

(2) Replacement with Q series (Q mode) or L series

Please consider transition to the Q series (Q mode) or L series, which is the latest MELSEC series.

However, if modules or functions that cannot be replaced by the Q series (Q mode) or L series are used in the existing system, please purchase a sufficient amount of spare parts.

As for alternative models, please refer to Section 12.

In the alternative model list, we have introduced models that have small restrictions when transition from the A/QnA (large type) series. There are cases in customers systems that there are models that can be selected if to reduce specifications, so please confirm your existing system specification and select the models.

(3) Replacement with Q series (Q mode)

Please consider transition to the Q series if the following conditions are met:

- Existing communication cables are used or the system is gradually replaced when a system including a MELSECNET(II) data link system is replaced.
- Only a CPU module is replaced and existing A/QnA series modules are continued to be used.
- The existing wiring of terminal block type modules is used by using a conversion adapter.
- Modules that are difficult to be replaced with those for L series, such as the intelligent communication module "AD51H-S3", are included.

(4) Replacement with L series

Please consider transition to the Q series if the following conditions are met:

- All the existing A/QnA series modules can be replaced with the L series modules.
- The modules in the system are replaced all at once, not gradually.
- Wiring of the existing A/QnA series modules are not utilized.
- Modules are used in a CC-Link system or stand-alone.

Reference Material

When considering transition to Q series (Q mode), please refer to the following materials:

- Transition from MELSEC-A/QnA (Large Type) Series to Q Series Handbook (Fundamentals) : L(NA)08043ENG
- Transition from MELSEC-A/QnA (Large Type) Series to Q Series Handbook (Intelligent function modules) : L(NA)08046ENG
- Transition from MELSEC-A/QnA (Large Type) Series to Q Series Handbook (Network Modules) : L(NA)08048ENG
- Transition from MELSEC-A/QnA (Large Type) Series to Q Series Handbook (Communications) : L(NA)08050ENG

For useful renewal tools not listed in this technical bulletin, refer to the following material:

- MELSEC-A/QnA Series Transition Guide : L(NA)08077E

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other models

10. Basic concept for machine/line modifications and spare parts

In conjunction with the A/QnA (large type) series product discontinuation, we propose the following for machine/line modifications and spare parts for failure.

10.1 Module failures

We propose possession of a sufficient amount of spare parts for maintenance and exchange for module failures.

In order to have time for our customers to prepare for spare parts, we will continue providing the modules listed on the spare parts list in Section 13, for 2 years after product discontinuation.

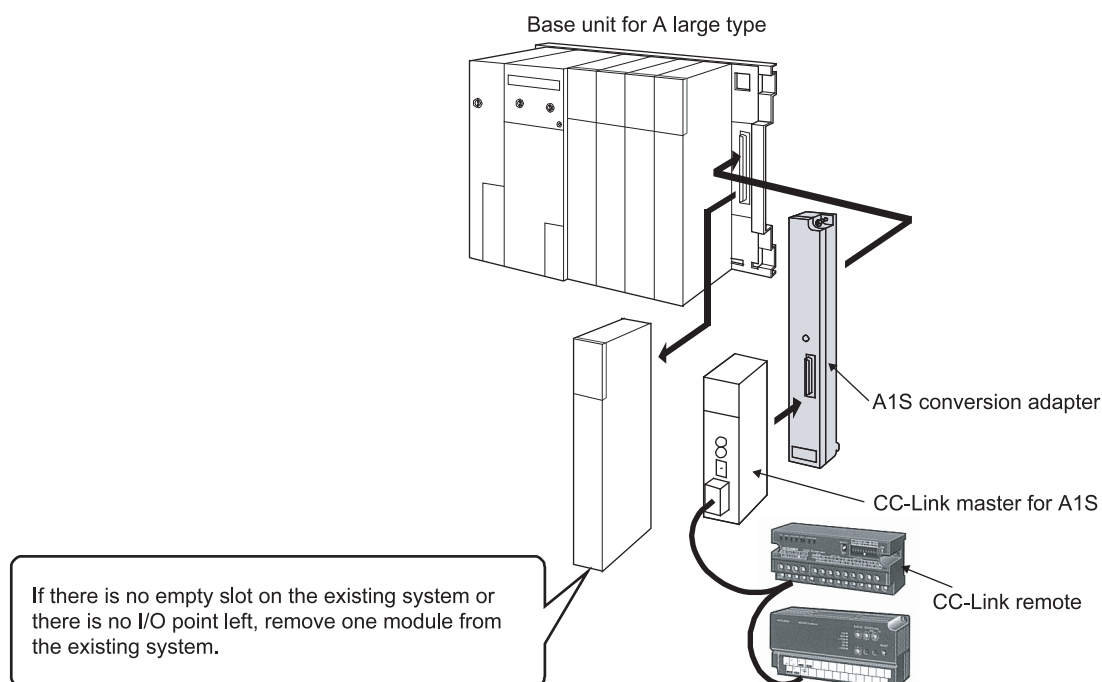
Production of the power supply modules, A61PN, A63P, and A61RP will continue even after October 2006. Please consider them as alternative modules.

10.2 Machine/line modifications

Please select the module that has the function for the machinery/line modifications necessary from the CC-Link product range. The module can be added by mounting the Q series CC-Link system master/local module to an empty slot on the Q series base unit and establishing a CC-Link system.

The module can also be added by mounting the CC-Link system master/local module to an empty slot on the A large type base unit using the A-A1S module conversion adapter and establishing a CC-Link system.

If there is no empty slot on the existing system or there is no I/O point left, remove one module from the existing system so that the CC-Link system master/local module can be mounted. Please add a CC-Link remote module to replace the functions of the removed module.



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other models

10.3 Spare parts storage

- (1) The general specifications of programmable controllers are as follows. Please do not store spare parts under a high temperature or high humidity condition, even within the range guaranteed by the specifications.

Storage ambient temperature	-20 to 75°C
Storage ambient humidity	10 to 90%, no condensation

- (2) Store in a place avoiding direct sunlight.
- (3) Store under a condition with no dust or corrosive gas.
- (4) The battery capacity of a A6BAT battery or a lithium-coin battery (commercially available) for memory card will be decreased by its self-discharging even when it is not used. Replace it with new one in 5 years as a guideline.
- (5) For the power supply module, power supply built-in type CPU module, or analog module that uses the aluminum electrolytic capacitor, which is shown in the following table, secure the spare parts since the basic function will be influenced by life deterioration.

In addition, when leaving products other than products marked with (*) un-energized for a long time, take following measures since characteristics of the aluminum electrolytic capacitor will be deteriorated.

Product	Model
CPU module (Power supply built-in type)	A1NCPU, A1NCPUP21, A1NCPUR21, A1NCPUP21-S3, A2CCPU, A2CCPUP21, A2CCPUR21, A2CCPUC24, A2CCPUC24-PRF, A2CJCPU-S3 (*)
Power supply module	A61P, A61PN, A61PEU, A61P-UL, A62P, A62PEU, A63P (*), A68P, A61RP, A67RP, A2CJ66P
Analog module	A62DA (*), A62DA-S1 (*)

[Countermeasures for preventing aluminum electrolytic capacitor characteristics deterioration]

For the power supply module or power supply built-in type CPU module which uses the aluminum electrolytic capacitor and whose rated voltage specifications is 100VAC/DC, characteristics will be deteriorated when it is left un-energized for a long time. Therefore, rotate the product at regular inspection (once in one or two years).

Or, activate the product once in two or three years, increasing voltage gradually from 0V to the rated voltage over 10 minutes or more and maintaining the voltage for a few hours.

[Reference]

When leaving the aluminum electrolytic capacitor un-energized, it will be deteriorated at approximately 1/4 speed of the case when it is energized, even at normal temperature. For example, when storing it for 10 years, life will be shortened for 2.5 years.

Since environment with high temperature and high humidity further accelerates the deterioration, store the spare parts avoiding such an environment.

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[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

11. Precautions for replacing the series

This section describes precautions for replacing the currently-used series with Q series (Q mode) or L series.

(1) CPU module

Select a new CPU module considering the required program capacity, I/O points and device points.

(2) Power supply module

Select a new power supply module by considering the amount of current consumed by each module to be mounted.

■ Pay enough attention when using an extension base unit type that doesn't need power.

■ In the Q series when selecting the capacity of the power supply module, it is necessary to take consideration of the base unit current consumed as well, so please pay attention.

(3) Base unit

Select a new base unit based on the number of slots and power supply module to be used.

Paying full attention to the followings.

■ Modify the size of holes used to fix programmable controllers to a control panel, as the hole size differs between the series.

■ In the L series, install a base unit to a DIN rail to fix it to a control panel.

■ In the L series, note that the number of extension blocks to be configured, the number of connectable modules, and the maximum configuration of the entire system differ depending on the CPU module.

(4) I/O module

Select the model that satisfies the following specifications: number of I/O points, I/O current/voltage. Replace with it while paying full attention to the followings.

■ Change the wiring referring to the manual, as the terminal block/connector shape, signal layout, and common type differ between the series.

(5) Special function module (Intelligent function module)

Select the model that satisfies the performance specifications.

Replace with it while paying full attention to the followings.

■ Modify the X/Y device Nos. if the new model is different from the old one in the number of occupied I/O points.

■ Change the wiring referring to the manual, as the terminal block/connector shape and signal layout differ between the series.

(6) Network module

Replace with an alternative model while paying full attention to the followings.

■ Cables may have to be modified. Confirm the specifications provided in the manual.

■ If there is no alternative model, transition to other network system is recommended.

■ For CC-Link and Ethernet network, use the network parameters to modify the initial settings made by sequence program.

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[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

(7) Programming

When the type of a programmable controller is changed using GX Developer (included in GX Works2 as well), the programs and parameters are automatically converted for the new CPU module. Note the followings.

- Prepare GX Developer with a version that supports the new CPU module and cables for connecting GX Developer and the programmable controller. For applicable cables, refer to the manual for GX Developer.
- Some of network parameters are deleted, as they cannot be converted.
Set the parameters after conversion.

Network parameter	A to Q (Q mode)/L	QnA to Q (Q mode)/L
MELSECNET(II)	MELSECNET(II) parameters are deleted.	
MELSECNET/10(H)	MELSECNET/10 parameters are converted for MELSECNET/10 mode. Note that MELSECNET/10 parameters shall be deleted for L series.	
MELSECNET/MINI	MELSECNET/MINI parameters are deleted.	
CC-Link	—	If 5 or more modules have been set in the CC-Link parameter, 5 th or later ones are deleted.
Ethernet	—	"Use the KeepAlive" is set on "Ethernet operations". Note that Ethernet parameters shall be deleted for L series.

- The instructions or devices that cannot be used without modification are converted into SM1255 or SD1255 (*1). Search SM1255 or SD1255 and modify the program after changing a programmable controller type.
*1: For Basic model QCPU, they are converted into SM999 or SD999.
- If the new CPU module does not have the sufficient program capacity, some parts of program are deleted during conversion. (END instruction is added.)
Check that no part of program is missing after changing a programmable controller type.
- Buffer memory contents and address assignments of special function modules (intelligent function modules) and network modules differ between the series. Modify the program that writes to/reads from buffer memory, if necessary.
[Reference] GX Works2 can be used in the Q series (Q mode) and L series. Use of GX Works2 simplifies programs for intelligent function modules and network modules.
- Roles of the accumulator (A), index register (V, Z), and file register (R) differ between the series. Therefore, modify programs if necessary.
- Micro computer programs cannot be created.
- Each of main program, sub-program and SFC program is converted into one program file.
When sub-program or SFC program is used, be sure to enable the multiple programs in the PLC parameter (program settings) after conversion. Then, modify the part that starts up programs.

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other models

12. Alternative models

12.1 Transition from A series to Q series (Q mode)

Production discontinuation		Transition to Q series (Q mode)	
Product	Model	Model	Remarks (restrictions)
CPU module	A1NCPU	Q02CPU	1) I/O control: Refresh/Direct switching → Refresh only 2) Processing speed (LD instruction): For refresh 1.0μs → 0.079μs 3) PC MIX value: 0.2 → 4.4 4) I/O points: 256 points → 4096 points 5) Program capacity: 6k steps → 28k steps 6) File register points: 0 point → 1017k points 7) Extension stage: 1 stage → 7 stage 8) Applicable memory: 4KRAM/4KROM/4KEROM → built-in RAM/built-in flash ROM/memory card Q2MEM-** 9) Micro computer program: Available → Not available
	A1NCPUP21	Q02CPU QJ71LP21-25	1) I/O control: Refresh/Direct switching → Refresh only 2) Processing speed (LD instruction): For refresh 1.0μs → 0.079μs 3) PC MIX value: 0.2 → 4.4 4) I/O points: 256 points → 4096 points 5) Program capacity: 6k steps → 28k steps 6) File register points: 0 point → 1017k points 7) Extension stage: 1 stage → 7 stage 8) Applicable memory: 4KRAM/4KROM/4KEROM → built-in RAM/built-in flash ROM/memory card Q2MEM-** 9) Micro computer program: Available → Not available
	A1NCPUR21	Q02CPU QJ71BR11	1) I/O control: Refresh/Direct switching → Refresh only 2) Processing speed (LD instruction): For refresh 1.0μs → 0.079μs 3) PC MIX value: 0.2 → 4.4 4) I/O points: 256 points → 4096 points 5) Program capacity: 6k steps → 28k steps 6) File register points: 0 point → 1017k points 7) Extension stage: 1 stage → 7 stage 8) Applicable memory: 4KRAM/4KROM/4KEROM → built-in RAM/built-in flash ROM/memory card Q2MEM-** 9) Micro computer program: Available → Not available
	A2NCPU	Q02CPU	1) I/O control: Refresh/Direct switching → Refresh only 2) Processing speed (LD instruction): For refresh 1.0μs → 0.079μs 3) PC MIX value: 0.2 → 4.4 4) I/O points: 512 points → 4096 points 5) Program capacity: 14k steps → 28k steps 6) File register points: 4k points → 1017k points 7) Extension stage: 3 stages → 7 stages 8) Applicable memory: Depending on the memory cassette → built-in RAM/built-in flash ROM/memory card Q2MEM-** 9) Micro computer program: Available → Not available

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other models

Production discontinuation		Transition to Q series (Q mode)	
Product	Model	Model	Remarks (restrictions)
CPU module	A2NCPUP21	Q02CPU QJ71LP21-25	1) I/O control: Refresh/Direct switching → Refresh only 2) Processing speed (LD instruction): For refresh 1.0 μ s → 0.079 μ s 3) PC MIX value: 0.2 → 4.4 4) I/O points: 512 points → 4096 points 5) Program capacity: 14k steps → 28k steps 6) File register points: 4k points → 1017k points 7) Extension stage: 3 stages → 7 stages 8) Applicable memory: Depending on the memory cassette → built-in RAM/built-in flash ROM/memory card Q2MEM-** 9) Micro computer program: Available → Not available
	A2NCPUR21	Q02CPU QJ71BR11	1) I/O control: Refresh/Direct switching → Refresh only 2) Processing speed (LD instruction): For refresh 1.0 μ s → 0.079 μ s 3) PC MIX value: 0.2 → 4.4 4) I/O points: 512 points → 4096 points 5) Program capacity: 14k steps → 28k steps 6) File register points: 4k points → 1017k points 7) Extension stage: 3 stages → 7 stages 8) Applicable memory: Depending on the memory cassette → built-in RAM/built-in flash ROM/memory card Q2MEM-** 9) Micro computer program: Available → Not available
	A2NCPUS1	Q02CPU	1) I/O control: Refresh/Direct switching → Refresh only 2) Processing speed (LD instruction): For refresh 1.0 μ s → 0.079 μ s 3) PC MIX value: 0.2 → 4.4 4) I/O points: 1024 points → 4096 points 5) Program capacity: 14k steps → 28k steps 6) File register points: 4k points → 1017k points 7) Extension stage: 7 stages → 7 stages 8) Applicable memory: Depending on the memory cassette → built-in RAM/built-in flash ROM/memory card Q2MEM-** 9) Micro computer program: Available → Not available
	A2NCPUP21-S1	Q02CPU QJ71LP21-25	1) I/O control: Refresh/Direct switching → Refresh only 2) Processing speed (LD instruction): For refresh 1.0 μ s → 0.079 μ s 3) PC MIX value: 0.2 → 4.4 4) I/O points: 1024 points → 4096 points 5) Program capacity: 14k steps → 28k steps 6) File register points: 4k points → 1017k points 7) Extension stage: 7 stages → 7 stages 8) Applicable memory: Depending on the memory cassette → built-in RAM/built-in flash ROM/memory card Q2MEM-** 9) Micro computer program: Available → Not available
	A2NCPUR21-S1	Q02CPU QJ71BR11	1) I/O control: Refresh/Direct switching → Refresh only 2) Processing speed (LD instruction): For refresh 1.0 μ s → 0.079 μ s 3) PC MIX value: 0.2 → 4.4 4) I/O points: 1024 points → 4096 points 5) Program capacity: 14k steps → 28k steps 6) File register points: 4k points → 1017k points 7) Extension stage: 7 stages → 7 stages 8) Applicable memory: Depending on the memory cassette → built-in RAM/built-in flash ROM/memory card Q2MEM-** 9) Micro computer program: Available → Not available

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other models

Production discontinuation		Transition to Q series (Q mode)	
Product	Model	Model	Remarks (restrictions)
CPU module	A3NCPU	Q06HCPU	1) I/O control: Refresh/Direct switching → Refresh only 2) Processing speed (LD instruction): For refresh 1.0μs → 0.034μs 3) PC MIX value: 0.2 → 10.3 4) I/O points: 2048 points → 4096 points 5) Program capacity: 30k × 2 steps → 60k steps 6) File register points: 8k points → 1017k points 7) Extension stage: 7 stages → 7 stages 8) Applicable memory: Depending on the memory cassette → built-in RAM/built-in flash ROM/memory card Q2MEM-** 9) Micro computer program: Available → Not available
	A3NCPUP21	Q06HCPU QJ71LP21-25	1) I/O control: Refresh/Direct switching → Refresh only 2) Processing speed (LD instruction): For refresh 1.0μs → 0.034μs 3) PC MIX value: 0.2 → 10.3 4) I/O points: 2048 points → 4096 points 5) Program capacity: 30k × 2 steps → 60k steps 6) File register points: 8k points → 1017k points 7) Extension stage: 7 stages → 7 stages 8) Applicable memory: Depending on the memory cassette → built-in RAM/built-in flash ROM/memory card Q2MEM-** 9) Micro computer program: Available → Not available
	A3NCPUR21	Q06HCPU QJ71BR11	1) I/O control: Refresh/Direct switching → Refresh only 2) Processing speed (LD instruction): For refresh 1.0μs → 0.034μs 3) PC MIX value: 0.2 → 10.3 4) I/O points: 2048 points → 4096 points 5) Program capacity: 30k × 2 steps → 60k steps 6) File register points: 8k points → 1017k points 7) Extension stage: 7 stages → 7 stages 8) Applicable memory: Depending on the memory cassette → built-in RAM/built-in flash ROM/memory card Q2MEM-** 9) Micro computer program: Available → Not available
	A2ACPU	Q02CPU	1) I/O control: Refresh/Direct switching → Refresh only 2) Processing speed (LD instruction): 0.2μs → 0.079μs 3) PC MIX value: 0.9 → 4.4 4) I/O points: 512 points → 4096 points 5) Program capacity: 14k steps → 28k steps 6) File register points: 8k points → 1017k points 7) Extension stage: 3 stages → 7 stages 8) Applicable memory: Depending on the memory cassette → built-in RAM/built-in flash ROM/memory card Q2MEM-**
	A2ACPUP21	Q02CPU QJ71LP21-25	1) I/O control: Refresh/Direct switching → Refresh only 2) Processing speed (LD instruction): 0.2μs → 0.079μs 3) PC MIX value: 0.9 → 4.4 4) I/O points: 512 points → 4096 points 5) Program capacity: 14k steps → 28k steps 6) File register points: 8k points → 1017k points 7) Extension stage: 3 stages → 7 stages 8) Applicable memory: Depending on the memory cassette → built-in RAM/built-in flash ROM/memory card Q2MEM-**

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[Title] Production discontinuation of MELSEC-A/QnA (large type) series

[Date of Issue] February 2005 (Ver. N: January 2013)

[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to Q series (Q mode)	
Product	Model	Model	Remarks (restrictions)
CPU module	A2ACPUR21	Q02CPU QJ71BR11	1) I/O control: Refresh/Direct switching → Refresh only 2) Processing speed (LD instruction): 0.2μs → 0.079μs 3) PC MIX value: 0.9 → 4.4 4) I/O points: 512 points → 4096 points 5) Program capacity: 14k steps → 28k steps 6) File register points: 8k points → 1017k points 7) Extension stage: 3 stages → 7 stages 8) Applicable memory: Depending on the memory cassette → built-in RAM/built-in flash ROM/memory card Q2MEM.**
	A2ACPU-S1	Q02CPU	1) I/O control: Refresh only 2) Processing speed (LD instruction): 0.2μs → 0.079μs 3) PC MIX value: 0.9 → 4.4 4) I/O points: 1024 points → 4096 points 5) Program capacity: 14k steps → 28k steps 6) File register points: 8k points → 1017k points 7) Extension stage: 7 stages → 7 stages 8) Applicable memory: Depending on the memory cassette → built-in RAM/built-in flash ROM/memory card Q2MEM.**
	A2ACPUP21-S1	Q02CPU QJ71LP21-25	1) I/O control: Refresh only 2) Processing speed (LD instruction): 0.2μs → 0.079μs 3) PC MIX value: 0.9 → 4.4 4) I/O points: 1024 points → 4096 points 5) Program capacity: 14k steps → 28k steps 6) File register points: 8k points → 1017k points 7) Extension stage: 7 stages → 7 stages 8) Applicable memory: Depending on the memory cassette → built-in RAM/built-in flash ROM/memory card Q2MEM.**
	A2ACPUR21-S1	Q02CPU QJ71BR11	1) I/O control: Refresh only 2) Processing speed (LD instruction): 0.2μs → 0.079μs 3) PC MIX value: 0.9 → 4.4 4) I/O points: 1024 points → 4096 points 5) Program capacity: 14k steps → 28k steps 6) File register points: 8k points → 1017k points 7) Extension stage: 7 stages → 7 stages 8) Applicable memory: Depending on the memory cassette → built-in RAM/built-in flash ROM/memory card Q2MEM.**
	A3ACPU	Q06HCPU	1) I/O control: Refresh only 2) Processing speed (LD instruction): 0.15μs → 0.034μs 3) PC MIX value: 1.2 → 10.3 4) I/O points: 2048 points → 4096 points 5) Program capacity: 30k × 2 steps → 60k steps 6) File register points: 8k points → 1017k points 7) Extension stage: 7 stages → 7 stages 8) Applicable memory: Depending on the memory cassette → built-in RAM/built-in flash ROM/memory card Q2MEM.**

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[Title] Production discontinuation of MELSEC-A/QnA (large type) series

[Date of Issue] February 2005 (Ver. N: January 2013)

[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to Q series (Q mode)	
Product	Model	Model	Remarks (restrictions)
CPU module	A3ACPUP21	Q06HCPU QJ71LP21-25	1) I/O control: Refresh only 2) Processing speed (LD instruction): 0.15 μ s \rightarrow 0.034 μ s 3) PC MIX value: 1.2 \rightarrow 10.3 4) I/O points: 2048 points \rightarrow 4096 points 5) Program capacity: 30k \times 2 steps \rightarrow 60k steps 6) File register points: 8k points \rightarrow 1017k points 7) Extension stage: 7 stages \rightarrow 7 stages 8) Applicable memory: Depending on the memory cassette \rightarrow built-in RAM/built-in flash ROM/memory card Q2MEM.**
	A3ACPUR21	Q06HCPU QJ71BR11	1) I/O control: Refresh only 2) Processing speed (LD instruction): 0.15 μ s \rightarrow 0.034 μ s 3) PC MIX value: 1.2 \rightarrow 10.3 4) I/O points: 2048 points \rightarrow 4096 points 5) Program capacity: 30k \times 2 steps \rightarrow 60k steps 6) File register points: 8k points \rightarrow 1017k points 7) Extension stage: 7 stages \rightarrow 7 stages 8) Applicable memory: Depending on the memory cassette \rightarrow built-in RAM/built-in flash ROM/memory card Q2MEM.**
	A2UCPU	Q02CPU	1) I/O control: Refresh only 2) Processing speed (LD instruction): 0.2 μ s \rightarrow 0.079 μ s 3) PC MIX value: 0.9 \rightarrow 4.4 4) I/O points: 512 points \rightarrow 4096 points 5) Program capacity: 14k steps \rightarrow 28k steps 6) File register points: 8k points \rightarrow 1017k points 7) Extension stage: 3 stages \rightarrow 7 stages 8) Applicable memory: Depending on the memory cassette \rightarrow built-in RAM/built-in flash ROM/memory card Q2MEM.**
	A2UCPU-S1	Q02CPU	1) I/O control: Refresh only 2) Processing speed (LD instruction): 0.2 μ s \rightarrow 0.079 μ s 3) PC MIX value: 0.9 \rightarrow 4.4 4) I/O points: 1024 points \rightarrow 4096 points 5) Program capacity: 14k steps \rightarrow 28k steps 6) File register points: 8k points \rightarrow 1017k points 7) Extension stage: 7 stages \rightarrow 7 stages 8) Applicable memory: Depending on the memory cassette \rightarrow built-in RAM/built-in flash ROM/memory card Q2MEM.**
	A3UCPU	Q06HCPU	1) I/O control: Refresh only 2) Processing speed (LD instruction): 0.15 μ s \rightarrow 0.034 μ s 3) PC MIX value: 1.2 \rightarrow 10.3 4) I/O points: 2048 points \rightarrow 4096 points 5) Program capacity: 30k \times 2 steps \rightarrow 60k steps 6) File register points: 8k points \rightarrow 1017k points 7) Extension stage: 7 stages \rightarrow 7 stages 8) Applicable memory: Depending on the memory cassette \rightarrow built-in RAM/built-in flash ROM/memory card Q2MEM.**

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[Title] Production discontinuation of MELSEC-A/QnA (large type) series

[Date of Issue] February 2005 (Ver. N: January 2013)

[Relevant Models] AnNCPUP, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to Q series (Q mode)	
Product	Model	Model	Remarks (restrictions)
CPU module	A4UCPU	Q12HCPU	1) I/O control: Refresh only 2) Processing speed (LD instruction): 0.15 μ s \rightarrow 0.034 μ s 3) PC MIX value: 1.2 \rightarrow 10.3 4) I/O points: 4096 points \rightarrow 4096 points 5) Program capacity: 30k \times 4 steps \rightarrow 124k steps 6) File register points: 8k points \rightarrow 1017k points 7) Extension stage: 7 stages \rightarrow 7 stages 8) Applicable memory: Depending on the memory cassette \rightarrow built-in RAM/built-in flash ROM/memory card Q2MEM-**
	A1NCPUP21-S3	Q02CPU QJ71LP21G	1) I/O control: Refresh/Direct switching \rightarrow Refresh only 2) Processing speed (LD instruction): For refresh 1.0 μ s \rightarrow 0.079 μ s 3) PC MIX value: 0.2 \rightarrow 4.4 4) I/O points: 256 points \rightarrow 4096 points 5) Program capacity: 6k steps \rightarrow 28k steps 6) File register points: 0 point \rightarrow 1017k points 7) Extension stage: 1 stage \rightarrow 7 stage 8) Applicable memory: 4KRAM/4KROM/4KEROM \rightarrow built-in RAM/built-in flash ROM/memory card Q2MEM-** 9) Micro computer program: Available \rightarrow Not available
	A2NCPUP21-S3	Q02CPU QJ71LP21G	1) I/O control: Refresh/Direct switching \rightarrow Refresh only 2) Processing speed (LD instruction): For refresh 1.0 μ s \rightarrow 0.079 μ s 3) PC MIX value: 0.2 \rightarrow 4.4 4) I/O points: 512 points \rightarrow 4096 points 5) Program capacity: 14k steps \rightarrow 28k steps 6) File register points: 4k points \rightarrow 1017k points 7) Extension stage: 3 stages \rightarrow 7 stages 8) Applicable memory: Depending on the memory cassette \rightarrow built-in RAM/built-in flash ROM/memory card Q2MEM-** 9) Micro computer program: Available \rightarrow Not available
	A2NCPUP21-S4	Q02CPU QJ71LP21G	1) I/O control: Refresh/Direct switching \rightarrow Refresh only 2) Processing speed (LD instruction): For refresh 1.0 μ s \rightarrow 0.079 μ s 3) PC MIX value: 0.2 \rightarrow 4.4 4) I/O points: 1024 points \rightarrow 4096 points 5) Program capacity: 14k steps \rightarrow 28k steps 6) File register points: 4k points \rightarrow 1017k points 7) Extension stage: 7 stages \rightarrow 7 stages 8) Applicable memory: Depending on the memory cassette \rightarrow built-in RAM/built-in flash ROM/memory card Q2MEM-** 9) Micro computer program: Available \rightarrow Not available
	A3NCPUP21-S3	Q06HCPU QJ71LP21G	1) I/O control: Refresh/Direct switching \rightarrow Refresh only 2) Processing speed (LD instruction): For refresh 1.0 μ s \rightarrow 0.034 μ s 3) PC MIX value: 0.2 \rightarrow 10.3 4) I/O points: 2048 points \rightarrow 4096 points 5) Program capacity: 30k \times 2 steps \rightarrow 60k steps 6) File register points: 8k points \rightarrow 1017k points 7) Extension stage: 7 stages \rightarrow 7 stages 8) Applicable memory: Depending on the memory cassette \rightarrow built-in RAM/built-in flash ROM/memory card Q2MEM-** 9) Micro computer program: Available \rightarrow Not available

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[Title] Production discontinuation of MELSEC-A/QnA (large type) series

[Date of Issue] February 2005 (Ver. N: January 2013)

[Relevant Models] AnNCP, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to Q series (Q mode)	
Product	Model	Model	Remarks (restrictions)
CPU module	A2ACPUP21-S3	Q02CPU QJ71LP21G	1) I/O control: Refresh only 2) Processing speed (LD instruction): 0.2 μ s \rightarrow 0.079 μ s 3) PC MIX value: 0.9 \rightarrow 4.4 4) I/O points: 512 points \rightarrow 4096 points 5) Program capacity: 14k steps \rightarrow 28k steps 6) File register points: 8k points \rightarrow 1017k points 7) Extension stage: 3 stages \rightarrow 7 stages 8) Applicable memory: Depending on the memory cassette \rightarrow built-in RAM/built-in flash ROM/memory card Q2MEM-**
	A2ACPUP21-S4	Q02CPU QJ71LP21G	1) I/O control: Refresh only 2) Processing speed (LD instruction): 0.2 μ s \rightarrow 0.079 μ s 3) PC MIX value: 0.9 \rightarrow 4.4 4) I/O points: 1024 points \rightarrow 4096 points 5) Program capacity: 14k steps \rightarrow 28k steps 6) File register points: 8k points \rightarrow 1017k points 7) Extension stage: 7 stages \rightarrow 7 stages 8) Applicable memory: Depending on the memory cassette \rightarrow built-in RAM/built-in flash ROM/memory card Q2MEM-**
	A3ACPUP21-S3	Q06HCPU QJ71LP21G	1) I/O control: Refresh only 2) Processing speed (LD instruction): 0.15 μ s \rightarrow 0.034 μ s 3) PC MIX value: 1.2 \rightarrow 10.3 4) I/O points: 2048 points \rightarrow 4096 points 5) Program capacity: 30k \times 2 steps \rightarrow 60k steps 6) File register points: 8k points \rightarrow 1017k points 7) Extension stage: 7 stages \rightarrow 7 stages 8) Applicable memory: Depending on the memory cassette \rightarrow built-in RAM/built-in flash ROM/memory card Q2MEM-**
	A2CCPU	Q02CPU QJ61BT11N	1) I/O control: Refresh only 2) Processing speed (LD instruction): 1.25 μ s \rightarrow 0.079 μ s 3) PC MIX value: 0.1 \rightarrow 4.4 4) I/O points: 512 points \rightarrow 4096 points 5) Program capacity: 8k steps \rightarrow 28k steps 6) File register points: 4k points \rightarrow 1017k points 7) Remote I/O: MINI-S3 \rightarrow CC-Link 8) Applicable memory: built-in RAM/4KROM/8KROM/16KROM \rightarrow built-in RAM/built-in flash ROM/memory card Q2MEM-** 9) Micro computer program: Available \rightarrow Not available
	A2CCPUP21	Q02CPU QJ61BT11N QJ71LP21-25	1) I/O control: Refresh only 2) Processing speed (LD instruction): 1.25 μ s \rightarrow 0.079 μ s 3) PC MIX value: 0.1 \rightarrow 4.4 4) I/O points: 512 points \rightarrow 4096 points 5) Program capacity: 8k steps \rightarrow 28k steps 6) File register points: 4k points \rightarrow 1017k points 7) Remote I/O: MINI-S3 \rightarrow CC-Link 8) Applicable memory: built-in RAM/4KROM/8KROM/16KROM \rightarrow built-in RAM/built-in flash ROM/memory card Q2MEM-** 9) Micro computer program: Available \rightarrow Not available

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[Title] Production discontinuation of MELSEC-A/QnA (large type) series

[Date of Issue] February 2005 (Ver. N: January 2013)

[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to Q series (Q mode)	
Product	Model	Model	Remarks (restrictions)
CPU module	A2CCPUR21	Q02CPU QJ61BT11N QJ71BR11	1) I/O control: Refresh only 2) Processing speed (LD instruction): 1.25 μ s \rightarrow 0.079 μ s 3) PC MIX value: 0.1 \rightarrow 4.4 4) I/O points: 512 points \rightarrow 4096 points 5) Program capacity: 8k steps \rightarrow 28k steps 6) File register points: 4k points \rightarrow 1017k points 7) Remote I/O: MINI-S3 \rightarrow CC-Link 8) Applicable memory: built-in RAM/4KROM/8KROM/16KROM \rightarrow built-in RAM/built-in flash ROM/memory card Q2MEM-** 9) Micro computer program: Available \rightarrow Not available
	A2CCPUC24-PRF	Q02CPU QJ61BT11N QJ71C24N	1) I/O control: Refresh only 2) Processing speed (LD instruction): 1.25 μ s \rightarrow 0.079 μ s 3) PC MIX value: 0.1 \rightarrow 4.4 4) I/O points: 512 points \rightarrow 4096 points 5) Program capacity: 8k steps \rightarrow 28k steps 6) File register points: 4k points \rightarrow 1017k points 7) Remote I/O: MINI-S3 \rightarrow CC-Link 8) Applicable memory: built-in RAM/4KROM/8KROM/16KROM \rightarrow built-in RAM/built-in flash ROM/memory card Q2MEM-** 9) Micro computer program: Available \rightarrow Not available
	A2CCPUC24	Q02CPU QJ61BT11N QJ71C24N	1) I/O control: Refresh only 2) Processing speed (LD instruction): 1.25 μ s \rightarrow 0.079 μ s 3) PC MIX value: 0.1 \rightarrow 4.4 4) I/O points: 512 points \rightarrow 4096 points 5) Program capacity: 8k steps \rightarrow 28k steps 6) File register points: 4k points \rightarrow 1017k points 7) Remote I/O: MINI-S3 \rightarrow CC-Link 8) Applicable memory: built-in RAM/4KROM/8KROM/16KROM \rightarrow built-in RAM/built-in flash ROM/memory card Q2MEM-** 9) Micro computer program: Available \rightarrow Not available
	A2CJCPU-S3	Q02CPU QJ61BT11N	1) I/O control: Refresh only 2) Processing speed (LD instruction): 1.25 μ s \rightarrow 0.079 μ s 3) PC MIX value: 0.1 \rightarrow 4.4 4) I/O points: 512 points \rightarrow 4096 points 5) Program capacity: 8k steps \rightarrow 28k steps 6) File register points: 4k points \rightarrow 1017k points 7) Remote I/O: MINI-S3 \rightarrow CC-Link 8) Applicable memory: built-in RAM/4KROM/8KROM/16KROM \rightarrow built-in RAM/built-in flash ROM/memory card Q2MEM-** 9) Micro computer program: Available \rightarrow Not available
Main base unit	A32B	Q32SB	No restrictions
	A35B	Q35B	No restrictions
	A38B	Q38B	No restrictions
	A32B-UL	Q32SB	No restrictions
	A35B-UL	Q35B	No restrictions
	A38B-UL	Q38B	No restrictions
	A32B-E	Q32SB-E	No restrictions
	A35B-E	Q35B-E	No restrictions
	A38B-E	Q38B-E	No restrictions
	A32B-S1	Q33B	Number of I/O slots: 2 slots \rightarrow 3 slots

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[Title] Production discontinuation of MELSEC-A/QnA (large type) series

[Date of Issue] February 2005 (Ver. N: January 2013)

[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to Q series (Q mode)	
Product	Model	Model	Remarks (restrictions)
Extension base unit	A52B	Q52B	No restrictions
	A55B	Q55B	No restrictions
	A58B	Q55B	Q55B × 2 units Number of I/O slots: 8 slots → 5 slots × 2 units
	A62B	Q63B	Number of I/O slots: 2 slots → 3 slots
	A65B	Q65B	No restrictions
	A68B	Q68B	No restrictions
	A55B-UL	Q55B	No restrictions
	A58B-UL	Q55B	Q55B × 2 units Number of I/O slots: 8 slots → 5 slots × 2 units
	A65B-UL	Q65B	No restrictions
Extension cable	A68B-UL	Q68B	No restrictions
	AC06B	QC06B	No restrictions
	AC06B-UL	QC06B	No restrictions
	AC12B	QC12B	No restrictions
	AC12B-UL	QC12B	No restrictions
	AC30B	QC30B	No restrictions
	AC30B-UL	QC30B	No restrictions
	AC50B	QC50B	No restrictions
	A1SC05NB	QC05B	No restrictions
	A1SC07NB	QC06B	Parallel mounting is not allowed. Cable length: 0.7m → 0.6m
	A1SC30NB	QC30B	Parallel mounting is not allowed.
A1SC50NB	QC50B	Parallel mounting is not allowed.	
Memory cassette	A3NMCA-0	Unnecessary	Built-in RAM/Built-in flash ROM
	A3NMCA-2	Unnecessary	Built-in RAM/Built-in flash ROM
	A3NMCA-4	Unnecessary	Built-in RAM/Built-in flash ROM
	A3NMCA-8	Unnecessary	Built-in RAM/Built-in flash ROM
	A3NMCA-16	Unnecessary	Built-in RAM/Built-in flash ROM
	A3NMCA-24	Unnecessary	Built-in RAM/Built-in flash ROM
	A3NMCA-40	Unnecessary	Built-in RAM/Built-in flash ROM
	A3NMCA-56	Unnecessary	Built-in RAM/Built-in flash ROM
	A3NMCA-2-UL	Unnecessary	Built-in RAM/Built-in flash ROM
	A3NMCA-4-UL	Unnecessary	Built-in RAM/Built-in flash ROM
	A3NMCA-8-UL	Unnecessary	Built-in RAM/Built-in flash ROM
	A3NMCA-16-UL	Unnecessary	Built-in RAM/Built-in flash ROM
	A3NMCA-24-UL	Unnecessary	Built-in RAM/Built-in flash ROM
	A3NMCA-40-UL	Unnecessary	Built-in RAM/Built-in flash ROM
	A3AMCA-96	Unnecessary	Built-in RAM/Built-in flash ROM
	A4UMCA-128	Unnecessary	Built-in RAM/Built-in flash ROM
	A4UMCA-8E	Unnecessary	Built-in RAM/Built-in flash ROM
A4UMCA-32E	Unnecessary	Built-in RAM/Built-in flash ROM	
A4UMCA-128E	Unnecessary	Built-in RAM/Built-in flash ROM	
IC-RAM memory	4KRAM	Unnecessary	Built-in RAM
EP-ROM memory	4KEROM	Unnecessary	Built-in flash ROM
	32KROM	Unnecessary	Built-in flash ROM
	64KROM	Unnecessary	Built-in flash ROM

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[Title] Production discontinuation of MELSEC-A/QnA (large type) series

[Date of Issue] February 2005 (Ver. N: January 2013)

[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to Q series (Q mode)	
Product	Model	Model	Remarks (restrictions)
Input module	AX10	QX10	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Not changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed
	AX10-UL	QX10	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Not changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed
	AX11	QX10	1) External wiring: Changed 2) Number of slots: Changed (2 modules required) 3) Program Number of occupied I/O points: Not changed (32=16×2) 4) Specifications Rated input voltage: Not changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed
	AX11EU	QX10	1) External wiring: Changed 2) Number of slots: Changed (2 modules required) 3) Program Number of occupied I/O points: Not changed (32=16×2) 4) Specifications Rated input voltage: Not changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed

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[Title] Production discontinuation of MELSEC-A/QnA (large type) series

[Date of Issue] February 2005 (Ver. N: January 2013)

[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to Q series (Q mode)	
Product	Model	Model	Remarks (restrictions)
Input module	AX20	QX28	1) External wiring: Changed 2) Number of slots: Changed (2 modules required) 3) Program Number of occupied I/O points: Changed 4) Specifications Rated input voltage: Not changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed
	AX20-UL	QX28	1) External wiring: Changed 2) Number of slots: Changed (2 modules required) 3) Program Number of occupied I/O points: Changed 4) Specifications Rated input voltage: Not changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed
	AX21	QX28	1) External wiring: Changed 2) Number of slots: Changed (4 modules required) 3) Program Number of occupied I/O points: Changed 4) Specifications Rated input voltage: Changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed
	AX21EU	QX28	1) External wiring: Changed 2) Number of slots: Changed (4 modules required) 3) Program Number of occupied I/O points: Changed 4) Specifications Rated input voltage: Not changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed

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[Title] Production discontinuation of MELSEC-A/QnA (large type) series

[Date of Issue] February 2005 (Ver. N: January 2013)

[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to Q series (Q mode)	
Product	Model	Model	Remarks (restrictions)
Input module	AX31	None	Alternating with QX41 is recommended. [When applying DC input] 1) External wiring: Changed (Connector terminal block must be converted.) 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Changed (12VDC not applicable) *1 Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed *1: When 12VDC is required, use QX71. [When applying AC input] Commutate and smooth the 12/24VAC externally before inputting to QX41.
	AX31-S1	QX41	1) External wiring: Changed (Connector terminal block must be converted.) 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Not changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed
	AX40	QX40	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Changed (12VDC not applicable)*1 Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed *1: When 12VDC is required, use QX70.
	AX40-UL	QX40	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Changed (12VDC not applicable)*1 Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed *1: When 12VDC is required, use QX70.

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[Title] Production discontinuation of MELSEC-A/QnA (large type) series

[Date of Issue] February 2005 (Ver. N: January 2013)

[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to Q series (Q mode)	
Product	Model	Model	Remarks (restrictions)
Input module	AX41	QX41	1) External wiring: Changed (Connector terminal block must be converted.) 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Changed (12VDC not applicable)*1 Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed *1: When 12VDC is required, use QX71.
	AX41-S1	QX41-S1	1) External wiring: Changed (Connector terminal block must be converted.) 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Changed (12VDC not applicable) Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed
	AX41-UL	QX41	1) External wiring: Changed (Connector terminal block must be converted.) 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Changed (12VDC not applicable)*1 Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed *1: When 12VDC is required, use QX71.
	AX42	QX42	1) External wiring: Not changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Changed (12VDC not applicable)*1 Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed *1: When 12VDC is required, use QX72.

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[Title] Production discontinuation of MELSEC-A/QnA (large type) series

[Date of Issue] February 2005 (Ver. N: January 2013)

[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to Q series (Q mode)	
Product	Model	Model	Remarks (restrictions)
Input module	AX42-S1	QX42-S1	1) External wiring: Not changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Changed (12VDC not applicable) Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed
	AX50-S1	None	Alternating with QX40 is recommended. 1) External wiring: Changed Connect a 5.6kΩ (1/2W or more) resistor to the external signal wire serially. 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed
	AX60-S1	None	Alternating with QX40 is recommended. 1) External wiring: Changed Connect a 20kΩ (2W or more) resistor to the external signal wire serially. 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed
	AX70	QX70	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Changed (24VDC not applicable) Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed

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[Title] Production discontinuation of MELSEC-A/QnA (large type) series

[Date of Issue] February 2005 (Ver. N: January 2013)

[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to Q series (Q mode)	
Product	Model	Model	Remarks (restrictions)
Input module	AX70-UL	QX70	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Changed (24VDC not applicable) Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed
	AX71	QX71	1) External wiring: Changed (Connector terminal block must be converted.) 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Changed (24VDC not applicable) Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed
	AX80	QX80	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Changed (12VDC not applicable)*1 Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed *1: When 12VDC is required, use QX70.
	AX80-UL	QX80	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Changed (12VDC not applicable)*1 Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed *1: When 12VDC is required, use QX70.

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[Date of Issue] February 2005 (Ver. N: January 2013)

[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to Q series (Q mode)	
Product	Model	Model	Remarks (restrictions)
Input module	AX80E	QX82-S1	1) External wiring: Changed (Connector terminal block must be converted.) 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Changed (12VDC not applicable) *1 Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed *1: When 12VDC is required, use QX70.
	AX81	QX81	1) External wiring: Changed (Connector terminal block must be converted.) 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Changed (12VDC not applicable)*1 Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed *1: When 12VDC is required, use QX71.
	AX81B	None	Alternating with QX81 is recommended. 1) External wiring: Changed (Connector terminal block must be converted.) 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Changed 4) Specifications Rated input voltage: Not changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: The wire breakage detection function not provided
	AX81-S1	QX81	1) External wiring: Changed (Connector terminal block must be converted.) 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Changed (12VDC not applicable) *1 Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed *1: When 12VDC is required, use QX71.

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[Date of Issue] February 2005 (Ver. N: January 2013)

[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to Q series (Q mode)	
Product	Model	Model	Remarks (restrictions)
Input module	AX81-S2	None	Alternating with QX81 is recommended. 1) External wiring: Changed (Connector terminal block must be converted.) Connect a 5.6kΩ (1/2W or more) or 8.2kΩ (1W or more) resistor serially to the external signal wire at 48VDC or 60VDC, respectively. 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed
	AX81-S3	QX82-S1	1) External wiring: Changed (Connector terminal block must be converted.) 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Changed 4) Specifications Rated input voltage: Changed (12VDC not applicable) Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed
	AX82	QX82	1) External wiring: Changed (D sub → FCN connector) 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Changed (12VDC not applicable)*1 Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed *1: When 12VDC is required, use QX72.

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[Title] Production discontinuation of MELSEC-A/QnA (large type) series

[Date of Issue] February 2005 (Ver. N: January 2013)

[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to Q series (Q mode)	
Product	Model	Model	Remarks (restrictions)
Output module	AY10	QY10	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Not changed Rated output current: Not changed (However, contact life span is reduced to half.) 5) Functions: Not changed
	AY10A	QY18A	1) External wiring: Changed 2) Number of slots: Changed (2 modules required) 3) Program Number of occupied I/O points: Changed 4) Specifications Rated output voltage: Not changed Rated output current: Not changed (However, contact life span is reduced to half.) 5) Functions: Not changed
	AY10A-UL	QY18A	1) External wiring: Changed 2) Number of slots: Changed (2 modules required) 3) Program Number of occupied I/O points: Changed 4) Specifications Rated output voltage: Not changed Rated output current: Not changed (However, contact life span is reduced to half.) 5) Functions: Not changed
	AY11	QY10	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Not changed Rated output current: Not changed (However, contact life span is reduced to half.) 5) Functions: Changed (No varistor, relay not replaceable)
	AY11-UL	QY10	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Not changed Rated output current: Not changed (However, contact life span is reduced to half.) 5) Functions: Changed (No varistor)
	AY11A	QY18A	1) External wiring: Changed 2) Number of slots: Changed (2 modules required) 3) Program Number of occupied I/O points: Changed 4) Specifications Rated output voltage: Not changed Rated output current: Not changed (However, contact life span is reduced to half.) 5) Functions: Changed (No varistor)

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[Date of Issue] February 2005 (Ver. N: January 2013)

[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to Q series (Q mode)	
Product	Model	Model	Remarks (restrictions)
Output module	AY11AEU	QY18A	1) External wiring: Changed 2) Number of slots: Changed (2 modules required) 3) Program Number of occupied I/O points: Changed 4) Specifications Rated output voltage: Not changed Rated output current: Not changed (However, contact life span is reduced to half.) 5) Functions: Changed (No varistor)
	AY11E	QY10	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Not changed Rated output current: Not changed (However, contact life span is reduced to half.) 5) Functions: Changed (Not fuse, no varistor)
	AY11EEU	QY10	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Not changed Rated output current: Not changed (However, contact life span is reduced to half.) 5) Functions: Changed (Not fuse, no varistor)
	AY13	QY10	1) External wiring: Changed 2) Number of slots: Changed (2 modules required) 3) Program Number of occupied I/O points: Not changed (32=16×2) 4) Specifications Rated output voltage: Not changed Rated output current: Not changed (However, contact life span is reduced to half.) 5) Functions: Not changed
	AY13E	QY10	1) External wiring: Changed 2) Number of slots: Changed (2 modules required) 3) Program Number of occupied I/O points: Not changed (32=16×2) 4) Specifications Rated output voltage: Not changed Rated output current: Not changed (However, contact life span is reduced to half.) 5) Functions: Changed (No fuse)
	AY13EU	QY10	1) External wiring: Changed 2) Number of slots: Changed (2 modules required) 3) Program Number of occupied I/O points: Not changed (32=16×2) 4) Specifications Rated output voltage: Not changed Rated output current: Not changed (However, contact life span is reduced to half.) 5) Functions: Not changed

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[Date of Issue] February 2005 (Ver. N: January 2013)

[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to Q series (Q mode)	
Product	Model	Model	Remarks (restrictions)
Output module	AY15EU	QY10	1) External wiring: Changed 2) Number of slots: Changed (2 modules required) 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Not changed Rated output current: Not changed (However, contact life span is reduced to half.) 5) Functions: Not changed
	AY22	QY22	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Not changed Rated output current: Changed (Output 2A → 0.6A) 5) Functions: Changed (No fuse, no varistor)
	AY23	QY22	1) External wiring: Changed 2) Number of slots: Changed (2 modules required) 3) Program Number of occupied I/O points: Not changed (32=16×2) 4) Specifications Rated output voltage: Not changed Rated output current: Not changed 5) Functions: Changed (No fuse)
	AY40	QY40P	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Changed (28.8VDC or more not applicable) Rated output current: Not changed 5) Functions: Not changed
	AY40-UL	QY40P	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Changed (28.8VDC or more not applicable) Rated output current: Not changed 5) Functions: Not changed
	AY40A	QY68A	1) External wiring: Changed 2) Number of slots: Changed (2 modules required) 3) Program Number of occupied I/O points: Changed 4) Specifications Rated output voltage: Changed (28.8VDC or more not applicable) Rated output current: Not changed Response: Slow 5) Functions: Not changed

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[Date of Issue] February 2005 (Ver. N: January 2013)

[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to Q series (Q mode)	
Product	Model	Model	Remarks (restrictions)
Output module	AY41	QY41P	1) External wiring: Changed (Connector terminal block must be converted.) 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Changed (28.8VDC or more not applicable) Rated output current: Changed 5) Functions: Not changed
	AY41-UL	QY41P	1) External wiring: Changed (Connector terminal block must be converted.) 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Changed (28.8VDC or more not applicable) Rated output current: Changed 5) Functions: Not changed
	AY42	QY42P	1) External wiring: Not changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Changed (28.8VDC or more not applicable) Rated output current: Not changed 5) Functions: Not changed
	AY42-S1	QY42P	1) External wiring: Not changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Changed (28.8VDC or more not applicable) Rated output current: Not changed Response time: Changed (from 0.3ms to 1ms or less) 5) Functions: Not changed
	AY42-S3	QY42P	1) External wiring: Not changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Changed (28.8VDC or more not applicable) Rated output current: Not changed 5) Functions: Changed (The short protection function equivalent to fuse included)
	AY42-S4	QY42P	1) External wiring: Changed (External power supply is required) 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Changed (28.8VDC or more not applicable) Rated output current: Not changed 5) Functions: Not changed

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[Title] Production discontinuation of MELSEC-A/QnA (large type) series

[Date of Issue] February 2005 (Ver. N: January 2013)

[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to Q series (Q mode)	
Product	Model	Model	Remarks (restrictions)
Output module	AY50	QY50	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Changed (28.8VDC or more not applicable) Rated output current: Not changed 5) Functions: Changed (Fuse not replaceable)
	AY50-UL	QY50	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Changed (28.8VDC or more not applicable) Rated output current: Not changed 5) Functions: Changed (Fuse not replaceable)
	AY51	QY50	1) External wiring: Changed 2) Number of slots: Changed (2 modules required) 3) Program Number of occupied I/O points: Not changed (32=16×2) 4) Specifications Rated output voltage: Changed (28.8VDC or more not applicable) Rated output current: Not changed 5) Functions: Not changed
	AY51-S1	QY50	1) External wiring: Changed 2) Number of slots: Changed (2 modules required) 3) Program Number of occupied I/O points: Not changed (32=16×2) 4) Specifications Rated output voltage: Changed (28.8VDC or more not applicable) Rated output current: Not changed 5) Functions: Changed (Fuse not replaceable)
	AY51-UL	QY50	1) External wiring: Changed 2) Number of slots: Changed (2 modules required) 3) Program Number of occupied I/O points: Not changed (32=16×2) 4) Specifications Rated output voltage: Changed (28.8VDC or more not applicable) Rated output current: Not changed 5) Functions: Not changed
	AY60	QY68A	1) External wiring: Changed 2) Number of slots: Changed (2 modules required) 3) Program Number of occupied I/O points: Changed 4) Specifications Rated output voltage: Changed (48VDC not applicable) Rated output current: Not changed 5) Functions: Changed (No fuse, independent common)

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[Date of Issue] February 2005 (Ver. N: January 2013)

[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to Q series (Q mode)	
Product	Model	Model	Remarks (restrictions)
Output module	AY60E	QY68A	1) External wiring: Changed 2) Number of slots: Changed (2 modules required) 3) Program Number of occupied I/O points: Changed 4) Specifications Rated output voltage: Changed (48VDC not applicable) Rated output current: Not changed 5) Functions: Changed (No fuse, independent common)
	AY60S	QY68A	1) External wiring: Changed 2) Number of slots: Changed (2 modules required) 3) Program Number of occupied I/O points: Changed 4) Specifications Rated output voltage: Changed (48VDC not applicable) Rated output current: Not changed 5) Functions: Changed (No fuse, independent common)
	AY60S-UL	QY68A	1) External wiring: Changed 2) Number of slots: Changed (2 modules required) 3) Program Number of occupied I/O points: Changed 4) Specifications Rated output voltage: Changed (48VDC not applicable) Rated output current: Not changed 5) Functions: Changed (No fuse, independent common)
	AY70	QY70	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Changed (14.4VDC or more not applicable) Rated output current: Not changed 5) Functions: Not changed
	AY70-UL	QY70	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Changed (14.4VDC or more not applicable) Rated output current: Not changed 5) Functions: Not changed
	AY71	QY71	1) External wiring: Changed (Connector terminal block must be converted.) 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Not changed Rated output current: Not changed 5) Functions: Not changed

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[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to Q series (Q mode)	
Product	Model	Model	Remarks (restrictions)
Output module	AY72	QY71	1) External wiring: Not changed 2) Number of slots: Changed (2 modules required) 3) Program Number of occupied I/O points: Not changed (64=32×2) 4) Specifications Rated output voltage: Not changed Rated output current: Not changed 5) Functions: Not changed
	AY80	QY80	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Changed (28.8VDC or more not applicable) Rated output current: Not changed 5) Functions: Changed (Fuse not replaceable)
	AY81	QY81P	1) External wiring: Changed (Connector terminal block must be converted.) 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Changed (28.8VDC or more not applicable) Rated output current: Changed (Output 0.5A → 0.1A) 5) Functions: Not changed
	AY82-EP	QY81P	1) External wiring: Not changed 2) Number of slots: Changed (2 modules required) 3) Program Number of occupied I/O points: Not changed (64=32×2) 4) Specifications Rated output voltage: Not changed Rated output current: Not changed 5) Functions: Not changed
I/O module	AH42	QH42P	1) External wiring: Not changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Changed (32 points occupied) 4) Specifications Rated input voltage: Changed (12VDC not applicable) Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed Rated output voltage: Changed (28.8VDC or more not applicable) Rated output current: Not changed 5) Functions: Not changed
Dynamic scan I/O module	A42XY	None	Alternating with QX42 and QY42P by converting dynamic of the I/O signal to static is recommended.

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[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to Q series (Q mode)	
Product	Model	Model	Remarks (restrictions)
Dummy module	AG62	None	[Dummy module function] Alternating with QG60 and I/O assignment setting is recommended. [Simulation switch function] Alternating with QX40 and external switch is recommended.
Blanking module	AG60	QG60	No restrictions
Interrupt module	AI61	QI60	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Changed (16 points occupied) 4) Specifications Rated input voltage: Changed (12VDC not applicable) Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed
	AI61-S1	QI60	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Changed (16 points occupied) 4) Specifications Rated input voltage: Not changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed 6) Others: The response time is different.
K/A conversion adaptor	A6KA-CH32	None	Retry the external wiring.
	A6KA-CX42		
	A6KA-CY42		
	A6KA-TX10		
	A6KA-TX11		
	A6KA-TX20		
	A6KA-TX21		
	A6KA-TX40		
	A6KA-TX41		
	A6KA-TY10		
	A6KA-TY11		
	A6KA-TY13		
	A6KA-TY22		
	A6KA-TY23		
	A6KA-TY40		
A6KA-TY41			
A6KA-TY51			
Simulation switch	A6SW16	None	Alternating with the connected external switch is recommended.
	A6SW32		

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[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to Q series (Q mode)	
Product	Model	Model	Remarks (restrictions)
Power supply module	A62P	Q62P	1) External wiring: Changed 2) Number of slots: Not changed 3) Specifications: Current capacity is reduced.
	A61PEU	Q61P	1) External wiring: Changed 2) Number of slots: Not changed 3) Specifications: Current capacity is reduced.
	A62PEU	Q62P	1) External wiring: Changed 2) Number of slots: Not changed 3) Specifications: Current capacity is reduced.
	A68P	None	General-purpose switching power supply (For ±15VDC)
	A61P-UL	Q61P	Current capacity is reduced.
Analog input module	A616AD	Q68ADV Q68ADI	1) External wiring: Cable size is changed. 2) Number of slots: Changed (2 modules required) 3) Program: Number of occupied I/O points, I/O signals and buffer memory address are changed. 4) Performance specifications change: 8CH/module, input signals (Either V or I input) 5) Function specifications: Not changed
	A68AD	Q68ADV Q68ADI	1) External wiring: Cable size is changed. 2) Number of slots: Not changed 3) Program: Number of occupied I/O points, I/O signals and buffer memory address are changed. 4) Performance specifications change: Input signals (Either V or I input) and I/O characteristics 5) Function specifications: Not changed
	A68AD-S2	Q68ADV Q68ADI	1) External wiring: Cable size is changed. 2) Number of slots: Not changed 3) Program: Number of occupied I/O points, I/O signals and buffer memory address are changed. 4) Performance specifications change: Input signals (Either V or I input) and I/O characteristics 5) Function specifications: Not changed
	A68ADN	Q68ADV Q68ADI	1) External wiring: Cable size is changed. 2) Number of slots: Not changed 3) Program: Number of occupied I/O points, I/O signals and buffer memory address are changed. 4) Performance specifications change: Input signals (Either V or I input) and increase in current consumption 5) Function specifications: Not changed
Multiplexer	A60MX	None	Alternating with multiple Q68ADV/Q68ADI modules is recommended.
	A60MXRN	None	Alternating with multiple Q64AD-GH modules is recommended.
	A60MXR	None	Alternating with multiple Q64AD-GH modules is recommended.
	A60MXTN	None	Alternating with multiple Q64TD modules is recommended.
	A60MXT	None	Alternating with multiple Q64TD modules is recommended.

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[Date of Issue] February 2005 (Ver. N: January 2013)

[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to Q series (Q mode)	
Product	Model	Model	Remarks (restrictions)
Analog output module	A616DAI	Q68DAIN	1) External wiring: Cable size is changed. 2) Number of slots: Changed (2 modules are required) 3) Program: Number of occupied I/O points, I/O signals and buffer memory address are changed. 4) Performance specifications change: 8CH/module 5) Function specifications: Not changed
	A616DAV	Q68DAVN	1) External wiring: Cable size is changed. 2) Number of slots: Changed (2 modules are required) 3) Program: Number of occupied I/O points, I/O signals and buffer memory address are changed. 4) Performance specifications change: 8CH/module 5) Function specifications: Not changed
	A62DA	Q62DAN	1) External wiring: Cable size is changed. 2) Number of slots: Not changed 3) Program: Number of occupied I/O points, I/O signals and buffer memory address are changed. 4) Performance specifications change: Output current (minus current not applicable), I/O characteristics 5) Function specifications: Not changed
	A62DA-S1	Q62DAN	1) External wiring: Cable size is changed. 2) Number of slots: Not changed 3) Program: Number of occupied I/O points, I/O signals and buffer memory address are changed. 4) Performance specifications change: Output current (minus current not applicable) 5) Function specifications: Not changed
	A68DAI-S1	Q68DAIN	1) External wiring: Cable size is changed. 2) Number of slots: Not changed 3) Program: Number of occupied I/O points, I/O signals and buffer memory address are changed. 4) Performance specifications change: Increase in current consumption 5) Function specifications: Not changed
	A68DAV	Q68DAVN	1) External wiring: Cable size is changed. 2) Number of slots: Not changed 3) Program: Number of occupied I/O points, I/O signals and buffer memory address are changed. 4) Performance specifications change: Increase in current consumption 5) Function specifications: Not changed

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[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to Q series (Q mode)	
Product	Model	Model	Remarks (restrictions)
Temperature input module	A616TD	Q64TD	1) External wiring: Cable size is changed. 2) Number of slots: Changed (4 modules are required.) 3) Program: Number of occupied I/O points, I/O signals and buffer memory address are changed. 4) Performance specifications change: 4CH/module 5) Function specifications: Not changed
	A68RD3N	Q64RD	1) External wiring: Cable size is changed. 2) Number of slots: Changed (2 modules required) 3) Program: Number of occupied I/O points, I/O signals and buffer memory address are changed. 4) Performance specifications change: 4CH/module 5) Functions: Not changed
	A68RD4N	Q64RD	1) External wiring: Cable size is changed. 2) Number of slots: Changed (2 modules required) 3) Program: Number of occupied I/O points, I/O signals and buffer memory address are changed. 4) Performance specifications change: 4CH/module 5) Functions: Not changed
High-speed counter module	AD61	QD62-H01	1) External wiring: Terminal block wiring → Connector wiring, cable size is changed. 2) Number of slots: Not changed 3) Counting speed (max.): 50KPPS 4) Counting range: 32-bit signed binary (-2147483648 to 2147483647) Review of counting range 5) Program: Number of occupied I/O points, I/O signals and buffer memory address are changed. 6) Performance specifications change: Not changed 7) Function specifications: Not changed
	AD61-S1	QD62-H02	1) External wiring: Terminal block wiring → Connector wiring, cable size is changed. 2) Number of slots: Not changed 3) Counting speed (max.): 1-phase mode: 10KPPS, 2-phase mode: 7KPPS 4) Counting range: 32-bit signed binary (-2147483648 to 2147483647) Review of counting range 5) Program: Number of occupied I/O points, I/O signals and buffer memory address are changed. 6) Performance specifications change: Not changed 7) Function specifications: Not changed
Positioning module	AD70	None	Mount A1SD70 to the QA1S6[]B-type extension base unit. Otherwise, replacing with the QD75 system is recommended.
	AD72	None	Mount two A1SD70 modules to the QA65B-type extension base unit. Otherwise, replacing with the QD75 system is recommended.
	AD75M1	QD75M1	1) External wiring: Connector and manual pulser wiring are changed. 2) Number of slots: Not changed 3) Program: Number of occupied I/O points, XY assignment, buffer memory assignment and different functions are changed. 4) Performance specifications change: Upward-compatibility 5) Function specifications: Some specifications are different.
	AD75M2	QD75M2	1) External wiring: Connector and manual pulser wiring are changed. 2) Number of slots: Not changed 3) Program: Number of occupied I/O points, XY assignment, buffer memory assignment and different functions are changed. 4) Performance specifications change: Upward-compatibility 5) Function specifications: Some specifications are different.

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[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to Q series (Q mode)	
Product	Model	Model	Remarks (restrictions)
Positioning module	AD75M3	QD75M4	1) External wiring: Connector and manual pulser wiring are changed. 2) Number of slots: Not changed 3) Program: Number of occupied I/O points, XY assignment, buffer memory assignment and different functions are changed 4) Performance specifications change: Upward-compatibility 5) Function specifications: Some specifications are different.
	AD75P1-S3	QD75P1	1) External wiring: Connector and manual pulser wiring are changed. 2) Number of slots: Not changed 3) Program: Number of occupied I/O points, XY assignment, buffer memory assignment and different functions are changed. 4) Performance specifications change: Not changed. 5) Function specifications: Some specifications are different. Remark Production of AD71 (S1/S2/S7) has been discontinued since the end of October 2004. For details, refer to Technical Bulletin No. T12-0016.
	AD75P2-S3	QD75P2	1) External wiring: Connector and manual pulser wiring are changed. 2) Number of slots: Not changed 3) Program: Number of occupied I/O points, XY assignment, buffer memory assignment and different functions are changed. 4) Performance specifications change: Not changed. 5) Function specifications: Some specifications are different.
	AD75P3-S3	QD75P4	1) External wiring: Connector and manual pulser wiring are changed. 2) Number of slots: Not changed 3) Program: Number of occupied I/O points, XY assignment, buffer memory assignment and different functions are changed. 4) Performance specifications change: Not changed. 5) Function specifications: Some specifications are different.
Position detection module	A61LS	None	No alternative model
	A62LS-S5	None	No alternative model
	A63LS	None	No alternative model
Intelligent communication	AD51H-S3	QD51 QD51-R24	QD51(R24) is different from AD51H-S3 in the following specifications: AD51H-S3 → QD51(R24) Number of tasks: 8 → 2 Memory: 300 → 60kb Parallel: Available → None Number of slots: 2 → 1 Memory card I/F: 2 → 0 In addition, AD51H-S3 has two RS-232 interfaces and a RS-422 interface; QD51 has two RS-232 interfaces; QD51-R24 has a RS-232 interface and a RS-422 interface.
	AD51-S3	QD51 QD51-R24	Replace the BASIC program with the one for QD51 (R24).
Ethernet module	AJ71E71N-B2	QJ71E71-B2	No restrictions
	AJ71E71N-B5	QJ71E71-B5	No restrictions
	AJ71E71N3-T	QJ71E71-100	No restrictions

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[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to Q series (Q mode)	
Product	Model	Model	Remarks (restrictions)
MELSECNET/B data link module	AJ71AT21B	None	Changing the MELSECNET(II) and /B data link systems with the MELSECNET/H network system is recommended. (Refer to Technical Bulletin No. T99-0049.)
	AJ72T25B	None	
MELSECNET data link module	AJ71AP21	None	
	AJ71AP21-S3	None	
	AJ71AR21	None	
	AJ71P22-S3	None	
	AJ71AP22-S3	None	
	AJ72P25	None	
	AJ72P25-S1	None	
	AJ72P25-S3	None	
	AJ72R25	None	
AJ72R25-S1	None		
CC-Link master/local module	AJ61BT11	QJ61BT11N	No restrictions
MELSECNET/MINI-S3 master module	AJ71PT32-S3	None	Changing the MELSECNET/MINI-S3 system to the CC-Link system is recommended.
	AJ71T32-S3	None	
	AJ71T32-S4	None	
MELSECNET/MINI-S3 slave station module	AJ72PT35	None	
	AJ72T35	None	
MELSEC-I/OLINK K master module	AJ51T64	None	Changing the MELSEC-I/OLINK system to the CC-Link/LT system is recommended.
JEMANET (OPCN-1) interface module	AJ71J92-S3	None	Changing the OPCN-1 system to the MELSECNET/H or CC-Link system is recommended.
B/NET interface module	AJ71B62-S3	B-QIF	1) External wiring: Not changed 2) Number of slot: Not changed 3) Program: Number of occupied I/O points, XY assignment, buffer memory assignment and different functions are changed.
Terminal interface module	AJ71C21-S1	None	No alternative model
Multidrop link module	AJ71C22-S1	None	No alternative model
Host controller high-speed link	AJ71C23-S3	None	No alternative model
Computer link module	AJ71UC24	QJ71C24N QJ71C24N-R2	Q series does not include multi-drop function.
	AJ71C24-S1	None	No alternative model
	AJ71C24-S7	None	No alternative model
MODBUS module	AJ71UC24-S2	None	Connecting the serial/Ethernet converter to QJ71MT91 is recommended.
Profibus-DP interface module	AJ71PB92D	QJ71PB92D	No restrictions
Profibus-FMS interface module	AJ71PB96F	None	Changing to the Ethernet system is recommended.
DeviceNet master module	AJ71DN91	QJ71DN91	Use "GX Configurator-DN" for GX Configurator.
Supersonic linear scale module	A64BTL	None	No alternative model

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[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to Q series (Q mode)	
Product	Model	Model	Remarks (restrictions)
External error check module	AD51FD-S3	None	No alternative model
PC fault detection module	AS91	None	<p>[Error output] Output the processing result of RUN flag: SM1039 and the contact B of self-diagnostics error: SM1 to the output module.</p> <div style="text-align: center;"> <pre> graph LR A[RUN contact SM1039] --- B[Self-diagnostics SM1] B --- C((Y)) </pre> </div> <p>[RUN output] Alternate with the ERR contact of Q6[]P-type power supply module.</p>
Vision sensor module	AS25VS	None	Connecting a commercially available vision sensor and a programmable controller with RS232, Ethernet or Digital I/O for data loading is recommended.
	AS25VS-S1	None	
Camera extension module	AS25CE	None	
Camera power supply	AS25VS-PW	None	
Monitor cable	AC50VS-M0	None	
AS25CE cable	AC02VS-CS	None	
AS25CE cable	AC02VS-C0	None	
Camera cable 5m	AC50VS-V0	None	
Camera cable 25m	AC250VS-V0	None	
Products sold as a set	AS25VS-SETA	None	
	AS25VS-SETB	None	
Vision sensor module	AS50VS	None	
	AS50VS-GN		
	AS50VS-S1		
Camera extension module	AS50CE	None	
Position high-speed detection module	AS50PM	None	
Option module connector	AS50EX1	None	
	AS50EX2		
Camera power supply	AS50VS-PW	None	
Camera cable 2m	AC20VS5-V0	None	
Camera cable 5m	AC50VS5-V0	None	
Camera cable 10m	AC100VS5-V0	None	
Camera cable 20m	AC200VS5-V0	None	
Camera cable 30m	AC300VS5-V0	None	

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[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

12.2 Transition from QnA series to Q series (Q mode)

Production discontinuation		Transition to Q series (Q mode)	
Product	Model	Model	Remarks (restrictions)
CPU module	Q2ACPU	Q02CPU	1) I/O control: Refresh only 2) Processing speed (LD instruction): 0.2 μ s \rightarrow 0.079 μ s 3) PC MIX value: 1.3 \rightarrow 4.4 4) I/O points: 512 points \rightarrow 4096 points 5) Program capacity: 28k steps \rightarrow 28k steps 6) File register points: 1014k points \rightarrow 1014k points 7) Extension stage: 3 stages \rightarrow 7 stages 8) Number of memory cards: 2 cards \rightarrow 1 card 9) Max. memory card SRAM capacity: 2M bytes \times 2 cards \rightarrow 2M bytes \times 1 card
	Q2ACPU-S1	Q06HCPU	1) I/O control: Refresh only 2) Processing speed (LD instruction): 0.2 μ s \rightarrow 0.034 μ s 3) PC MIX value: 1.3 \rightarrow 10.3 4) I/O points: 1024 points \rightarrow 4096 points 5) Program capacity: 60k steps \rightarrow 60k steps 6) File register points: 1014k points \rightarrow 1014k points 7) Extension stage: 7 stages \rightarrow 7 stages 8) Number of memory cards: 2 cards \rightarrow 1 card 9) Max. memory card SRAM capacity: 2M bytes \times 2 cards \rightarrow 2M bytes \times 1 card
	Q3ACPU	Q12HCPU	1) I/O control: Refresh only 2) Processing speed (LD instruction): 0.15 μ s \rightarrow 0.034 μ s 3) PC MIX value: 1.8 \rightarrow 10.3 4) I/O points: 2048 points \rightarrow 4096 points 5) Program capacity: 92k steps \rightarrow 124k steps 6) File register points: 1014k points \rightarrow 1014k points 7) Extension stage: 7 stages \rightarrow 7 stages 8) Number of memory cards: 2 cards \rightarrow 1 card 9) Max. memory card SRAM capacity: 2M bytes \times 2 cards \rightarrow 2M bytes \times 1 card
	Q4ACPU	Q12HCPU	1) I/O control: Refresh only 2) Processing speed (LD instruction): 0.075 μ s \rightarrow 0.034 μ s 3) PC MIX value: 3.8 \rightarrow 10.3 4) I/O points: 4096 points \rightarrow 4096 points 5) Program capacity: 124k steps \rightarrow 124k steps 6) File register points: 1014k points \rightarrow 1014k points 7) Extension stage: 7 stages \rightarrow 7 stages 8) Number of memory cards: 2 cards \rightarrow 1 card 9) Max. memory card SRAM capacity: 2M bytes \times 2 cards \rightarrow 2M bytes \times 1 card
Main base unit	A38HB	Q38B	No restrictions
	A38HBEU	Q38B	No restrictions
Ethernet module	AJ71QE71N-B2	QJ71E71-B2	No restrictions
	AJ71QE71N-B5	QJ71E71-B5	No restrictions
	AJ71QE71N3-T	QJ71E71-100	No restrictions
Serial communication module	AJ71QC24N	QJ71C24N	No restrictions
	AJ71QC24N-R2	QJ71C24N-R2	No restrictions
	AJ71QC24N-R4	QJ71C24N-R4	Q series QJ71C24N-R4 does not include control signals. When controlling signals, connect QJ71C24N to RS232-422 converter.
CC-Link master/local module	AJ61QBT11	QJ61BT11N	No restrictions

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[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

12.3 Transition from Q4AR redundant system to Q series (Q mode)

Production discontinuation		Transition to Q series (Q mode)	
Product	Model	Model	Remarks (restrictions)
CPU module	Q4ARCPU	Q12PRHCPU	1) I/O control: Refresh only 2) Processing speed (LD instruction): 0.075 μ s \rightarrow 0.034 μ s 3) PC MIX value: 3.8 \rightarrow 10.3 4) I/O points: 4096 points \rightarrow 4096 points 5) Program capacity: 124k steps \rightarrow 124k steps 6) File register points: 1014k points \rightarrow 1014k points 7) Extension stage: 7 stages \rightarrow 7 stages 8) Number of memory cards: 2 cards \rightarrow 1 card 9) Max. memory card SRAM capacity: 2M bytes \times 2 cards \rightarrow 2M bytes \times 1 card 10) I/O module connection method: Adjacent I/O (extension cable) \rightarrow NET/H remote I/O
Main base unit	A32RB	Q38RB	Main base: 1 unit \rightarrow 2 units I/O slots: 2 slots \rightarrow 8 slots
	A33RB	Q38RB	Main base: 1 unit \rightarrow 2 units I/O slots: 3 slots \rightarrow 8 slots
	A37RHB	Q38RB	I/O slots: 7 slots \rightarrow 8 slots
Extension base unit	A68RB	Q68RB	No restrictions
Power supply module	A67RP	None	No alternative model
System control module	AS92R	None	No alternative model
Bus switch module	A6RAF	None	No alternative model

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[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

13. MELSEC-A/QnA(large type) series spare parts list (continue production through September 2008)

Below listed are the product models that we will continue production for spare parts usage through September 2008, from the MELSEC-A/QnA (large type) series range.

In the below chart, the correspondence for spare parts provision is listed.

- ◎ : Continue production for spare parts through September 2008.
- : Will discontinue production till the end of September 2006, but replaceable products are available in ◎.
- : Discontinue production at the end of September 2006. (Please purchase spare parts till the end of August 2006)

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[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

13.1 A series spare parts list

Product	Model	Class	Replaceable product	Product	Model	Class	Replaceable product	
CPU module	A1NCPU	○*1	A2UCPU-S1+ A3NMCA-24+ A61PN+24VDC	Main base unit	A38B-UL	-	-	
	A1NCPUP21	○*1	A3ACPUP21+ A3NMCA-24+ A61PN+24VDC		A32B-E	-	-	
	A1NCPUR21	○*1	A3ACPUR21+ A3NMCA-24+ A61PN+24VDC		A35B-E	-	-	
	A2NCPU	○*1	A2UCPU-S1		A38B-E	-	-	
	A2NCPUP21	○*1	A3ACPUP21		A32B-S1	-	-	
	A2NCPUR21	○*1	A3ACPUR21		Extension base unit	A52B	◎	-
	A2NCPU-S1	○*1	A2UCPU-S1			A55B	◎	-
	A2NCPUP21-S1	○*1	A3ACPUP21			A58B	◎	-
	A2NCPUR21-S1	○*1	A3ACPUR21			A62B	◎	-
	A3NCPU	○*1	A3UCPU			A65B	◎	-
	A3NCPUP21	○*1	A3ACPUP21	A68B		◎	-	
	A3NCPUR21	○*1	A3ACPUR21	A55B-UL		-	-	
	A2ACPU	○	A2UCPU-S1	A58B-UL		-	-	
	A2ACPUP21	○	A3ACPUP21	A65B-UL		-	-	
	A2ACPUR21	○	A3ACPUR21	A68B-UL		-	-	
	A2ACPU-S1	○	A2UCPU-S1	Extension cable	AC06B	◎	-	
	A2ACPUP21-S1	○	A3ACPUP21		AC06B-UL	-	-	
	A2ACPUR21-S1	○	A3ACPUR21		AC12B	◎	-	
	A3ACPU	○	A3UCPU		AC12B-UL	-	-	
	A3ACPUP21	◎	-		AC30B	◎	-	
	A3ACPUR21	◎	-		AC30B-UL	-	-	
	A2UCPU	○	A2UCPU-S1		AC50B	◎	-	
	A2UCPU-S1	◎	-		A1SC05NB	◎	-	
	A3UCPU	◎	-		A1SC07NB	◎	-	
	A4UCPU	◎	-		A1SC30NB	◎	-	
	A1NCPUP21-S3	-	-	A1SC50NB	◎	-		
	A2NCPUP21-S3	-	-	Memory cassette	A3NMCA-0	○	A3NMCA-24	
	A2NCPUP21-S4	-	-		A3NMCA-2	○	A3NMCA-24	
	A3NCPUP21-S3	-	-		A3NMCA-4	○	A3NMCA-24	
	A2ACPUP21-S3	-	-		A3NMCA-8	○	A3NMCA-24	
	A2ACPUP21-S4	-	-		A3NMCA-16	○	A3NMCA-24	
	A3ACPUP21-S3	-	-		A3NMCA-24	◎	-	
A2CCPU	-	-	A3NMCA-40		○	A3NMCA-56		
A2CCPUP21	-	-	A3NMCA-56		◎	-		
A2CCPUR21	-	-	A3NMCA-2-UL		-	-		
A2CCPUC24-PRF	-	-	A3NMCA-4-UL		-	-		
A2CCPUC24	-	-	A3NMCA-8-UL	-	-			
A2CJCPU-S3	-	-	A3NMCA-16-UL	-	-			
Main base unit	A32B	◎	-	A3NMCA-24-UL	-	-		
	A35B	◎	-	A3NMCA-40-UL	-	-		
	A38B	◎	-	A3AMCA-96	◎	-		
	A32B-UL	-	-	A4UMCA-128	◎	-		
	A35B-UL	-	-	A4UMCA-8E	○	A4UMCA-128E		
				A4UMCA-32E	○	A4UMCA-128E		
				A4UMCA-128E	◎	-		
				IC-RAM memory	4KRAM	◎	-	



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*1 Regarding transition from the AnNCPU to the AnACPU/AnUCPU, there are some restrictions so please pay attention:

- (1) Some commands (CHK, DI/EI, CHG, SUB(P)) depending on usage, it is necessary to modify the program.
- (2) When using special registers (M9010, M9053) that are not used by AnACPU/AnUCPU, it is necessary to modify the program.
- (3) There is no micro computer mode for AnACPU/AnUCPU, so if you are using micro computer mode, it is necessary to modify the program.
- (4) For the AnACPU/AnUCPU, the input-output control method is fixed at refresh method, so when using direct method, the timing of the input-output will change, so please pay attention.
- (5) The link refresh timing is different, so please modify the program as necessary.
- (6) When using index registers in interrupt programs, it is necessary to modify the program.
- (7) When the A1NCPUP21/A1NCPUR21 with 4KEROM was replaced by the A3ACPUP21/A3ACPUR21, E2PROM operation cannot be performed.

Insert the A3NMCA-24 into the A3ACPUP21/A3ACPUR21 and use it in RAM or ROM operation.

When transition from the AnNCPU to the AnACPU/AnUCPU, the precautions are listed on the manuals of each CPU, so when doing transition, please refer to the following documentation.

· Transition from AnNCPU to the AnACPU

Type A2A(S1)/A3ACPU User's Manual IB-66544

· Transition from AnNCPU to AnUCPU

Type A2U(S1)/A3U/A4UCPU User's Manual IB-66436

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[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Product	Model	Class	Replaceable product	Product	Model	Class	Replaceable product
EEP-ROM memory	4KEROM	◎	-	Output module	AY22	◎	-
EP-ROM memory	32KROM	-	-		AY23	◎	-
	64KROM	-	-		AY40	◎	-
	Input module	AX10	◎		-	AY40-UL	-
AX10-UL		-	-		AY40A	-	-
AX11		◎	-		AY41	◎	-
AX11EU		-	-		AY41-UL	-	-
AX20		◎	-		AY42	◎	-
AX20-UL		-	-		AY42-S1	-	-
AX21		◎	-		AY42-S3	-	-
AX21EU		-	-		AY42-S4	-	-
AX31		-	-		AY50	◎	-
AX31-S1		-	-		AY50-UL	-	-
AX40		◎	-		AY51	◎	-
AX40-UL		-	-		AY51-S1	◎	-
AX41		◎	-		AY51-UL	-	-
AX41-S1		-	-		AY60	-	-
AX41-UL		-	-		AY60E	-	-
AX42		◎	-		AY60S	-	-
AX42-S1		-	-		AY60S-UL	-	-
AX50-S1		-	-		AY70	-	-
AX60-S1		-	-		AY70-UL	-	-
AX70		◎	-		AY71	-	-
AX70-UL		-	-		AY72	-	-
AX71		◎	-		AY80	-	-
AX80		-	-		AY81	◎	-
AX80-UL		-	-		AY82-EP	-	-
AX80E	-	-	I/O module		AH42	◎	-
AX81	◎	-	Dynamic scan I/O module	A42XY	-	-	
AX81B	-	-	Dummy module	AG62	◎	-	
AX81-S1	-	-	Blanking module	AG60	◎	-	
AX81-S2	-	-	Interrupt module	AI61	◎	-	
AX81-S3	-	-		AI61-S1	-	-	
AX82	◎	-	K/A conversion adaptor	A6KA-CH32	-	-	
AY10	◎	-		A6KA-CX42	-	-	
AY10A	◎	-		A6KA-CY42	-	-	
Output module	AY10A-UL	-		-	A6KA-TX10	-	-
	AY11	◎		-	A6KA-TX11	-	-
	AY11-UL	-		-	A6KA-TX20	-	-
	AY11A	◎		-	A6KA-TX21	-	-
	AY11AEU	-		-	A6KA-TX40	-	-
	AY11E	-		-	A6KA-TX41	-	-
	AY11EEU	-		-	A6KA-TY10	-	-
	AY13	◎		-	A6KA-TY11	-	-
	AY13E	◎		-	A6KA-TY13	-	-
	AY13EU	-		-	A6KA-TY22	-	-
	AY15EU	-	-				

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[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Product	Model	Class	Replaceable product	Product	Model	Class	Replaceable product
K/A conversion adaptor	A6KA-TY23	-	-	Intelligent communication	AD51H-S3	◎	-
	A6KA-TY40	-	-		AD51-S3	◎	-
	A6KA-TY41	-	-	Ethernet module	AJ71E71N-B2	-	-
	A6KA-TY51	-	-		AJ71E71N-B5	-	-
Simulation switch	A6SW16	-	-	AJ71E71N3-T	◎	-	
	A6SW32	-	-	MELSECNET/B data link module	AJ71AT21B	◎	-
Power supply module	A61PN	◎	(Continue Production)	AJ72T25B	◎	-	
	A61P	◎	-	MELSECNET data link module	AJ71AP21	◎	-
	A62P	○	A61PN + General-purpose 24VDC power supply		AJ71AP21-S3	-	-
	A63P	◎	(Continue Production)		AJ71AR21	◎	-
	A61PEU	○	A61PN		AJ71P22-S3	-	-
	A62PEU	-	-		AJ71AP22-S3	-	-
	A68P	-	-		AJ72P25	◎	-
	A61P-UL	○	A61PN		AJ72P25-S1	-	-
A61P-UL	○	A61PN	AJ72P25-S3		-	-	
Analog input module	A616AD	◎	-	AJ72R25	◎	-	
	A68AD	◎	-	AJ72R25-S1	-	-	
	A68AD-S2	-	-	CC-Link master/local module	AJ61BT11	◎	-
Multiplexer	A60MX	◎	-	MELSECNET/MINI-S3 master module	AJ71PT32-S3	◎	-
	A60MXRN	◎	-		AJ71T32-S3	○	AJ71PT32-S3
	A60MXR	○	A60MXRN		AJ71T32-S4	-	-
Analog output module	A616DAI	◎	-	AJ71PT32-S3	◎	-	
	A616DAV	◎	-	MELSECNET/MINI-S3 slave station module	AJ72PT35	◎	-
	A62DA	-	-	AJ72T35	○	AJ72PT35	
	A62DA-S1	-	-	MELSEC-I/OLINK master module	AJ51T64	-	-
Temperature input module	A68DAI-S1	-	-	JEMANET(OPCN-1) Interface module	AJ71J92-S3	-	-
	A68DAV	-	-	B/NET interface module	AJ71B62-S3	-	-
	A616TD	◎	-	Terminal interface module	AJ71C21-S1	-	-
	A68RD3N	◎	-	Multidrop link module	AJ71C22-S1	-	-
High-speed counter module	A68RD4N	-	-	Host controller high-speed link	AJ71C23-S3	-	-
	AD61	◎	-	Computer link module	AJ71UC24	◎	-
	AD61-S1	-	-		AJ71C24-S1	-	-
AD61-S1	-	-	AJ71C24-S7		-	-	
Positioning module	AD70	◎	-	MODBUS module	AJ71UC24-S2	-	-
	AD72	-	-	Profibus-DP interface module	AJ71PB92D	-	-
	AD75M1	○	AD75M3	Profibus-FMS interface module	AJ71PB96F	-	-
	AD75M2	○	AD75M3				
	AD75M3	◎	-	DeviceNet master module	AJ71DN91	-	-
	AD75P1-S3	○	AD75P3-S3	Supersonic linear scale module	A64BTL	-	-
	AD75P2-S3	○	AD75P3-S3				
	AD75P3-S3	◎	-				
Position detection module	A61LS	-	-	External error check module	AD51FD-S3	-	-
	A62LS-S5	-	-	PC fault detection module	AS91	-	-
	A63LS	-	-				

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[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Product	Model	Class	Replaceable product	Product	Model	Class	Replaceable product	
External display module	A6DU-B	-	-	Camera extension module	AS50CE	-	-	
Vision sensor module	AS25VS	-	-	Position high-speed detection module	AS50PM	-	-	
	AS25VS-S1	-	-		Option module connector	AS50EX1	-	-
Camera extension module	AS25CE	-	-	AS50EX2		-	-	
Camera power supply	AS25VS-PW	-	-	Camera power supply	AS50VS-PW	-	-	
Monitor cable	AC50VS-MO	-	-	Camera cable	2m	AC20VS5-VO	-	
AS25CE cable	AC02VS-CS	-	-		5m	AC50VS5-VO	-	-
	AC02VS-C0	-	-		10m	AC100VS5-VO	-	-
Camera cable	5m	AC50VS-VO	-		20m	AC200VS5-VO	-	-
	25m	AC250VS-VO	-		30m	AC300VS5-VO	-	-
Products sold as a set	AS25VS-SETA	-	-					
	AS25VS-SETB	-	-					
Vision sensor module	AS50VS	-	-					
	AS50VS-GN	-	-					
	AS50VS-S1	-	-					

13.2 QnA series spare parts list

Product	Model	Class	Replaceable product	Product	Model	Class	Replaceable product
CPU module	Q2ACPU	○	Q2ACPU-S1	Serial communication module	AJ71QC24N	◎	-
	Q2ACPU-S1	◎	-		AJ71QC24N-R2	◎	-
	Q3ACPU	◎	-		AJ71QC24N-R4	◎	-
	Q4ACPU	◎	-	CC-Link master/local module	AJ61QBT11	◎	-
Main base unit	A38HB	◎	-				
	A38HBEU	◎	-				
Ethernet module	AJ71QE71N-B2	-	-				
	AJ71QE71N-B5	-	-				
	AJ71QE71N3-T	◎	-				

13.3 Q4AR redundant system spare parts list

Product	Model	Class	Replaceable product	Product	Model	Class	Replaceable product
CPU module	Q4ARCPU	◎	-	Power supply module	A61RP	-	(Continue Production)
Main base unit	A32RB	◎	-		A67RP	◎	-
	A33RB	◎	-	System control module	AS92R	◎	-
	A37RHB	◎	-		Bus switch module	A6RAF	◎
Extension base unit	A68RB	◎	-				

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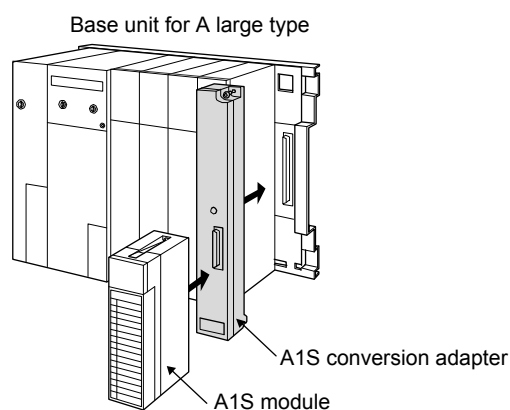
[Title] Production discontinuation of MELSEC-A/QnA (large type) series

[Date of Issue] February 2005 (Ver. N: January 2013)

[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

14. A-A1S module conversion adapter (A1ADP)

Using the A-A1S module conversion adapter, AnS series modules can be mounted to empty slots on the A large type base unit.



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other models

14.1 A-A1S module conversion adapter specifications

The following describes restrictions on the base units to which the A-A1S module conversion adapter can be mounted and on mounted modules.

(1) Applicable base units and the number of mountable adapters

- (a) The A-A1S module conversion adapter can be mounted to a main base unit or extension base unit in 3 slots at maximum.

For a main base unit or extension base unit equipped with 2 slots, the A-A1S module conversion adapter can be mounted in 2 slots (all slots).

- (b) The number of A-A1S module conversion adapters that can be mounted in a system is the total number of the A-A1S module conversion adapters mountable to the main base units and extension base units.

For a system in which 7 stages of extension base units are used, the A-A1S module conversion adapter can be mounted in 24 slots per system at maximum.

Applicable base units and the number of mountable adapters

Product		Model	Number of mountable adapters		
A/QnA series	Main base unit	A38B	Up to 3 slots	24 slots/system (3 slots × 8 bases)	
		A38B-E			
		A38B-UL			
		A38HB			
		A38HBEU			
		A35B			
		A35B-E			
		A35B-UL			
		A32B			2 slots (all slots)
		A32B-E			
	A32B-UL				
	Extension base unit	A68B	A68B		Up to 3 slots
			A68B-UL		
			A65B		
		A65B-UL	2 slots (all slots)		
		A62B			
A58B		Up to 3 slots			
A58B-UL					
A55B					
A55B-UL					
A52B	2 slots (all slots)				
Q4AR series	Main base unit	A37RHB	Up to 3 slots	-	
		A33RB	Up to 2 slots each for System A and System B	For network module, Ethernet module, and serial communication module only	
		A32RB	Up to 1 slot each for System A and System B		
	Extension base unit	A68RB	Up to 3 slots/base (Up to 7 stages)	-	
Q series	Extension base unit	QA65B	Up to 3 slots/base (Up to 7 stages)	-	
		QA68B			

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(2) Restrictions

- (a) Any power supply module or CPU module (as a module for the MELSECNET/B remote I/O station or MELSECNET/10 remote I/O station, which is mounted to the same position as the CPU module on a main base unit) cannot be mounted to the A/QnA series. For details, refer to Section 14.2 Compatible models list.
- (b) The A-A1S module conversion adapter cannot be used for a 2-slot type module (Type A1SD70 positioning module).
- (c) Online module change is not available. (Same as the AnS/QnAS series)

(3) Conformation to the EMC and Low Voltage Directives

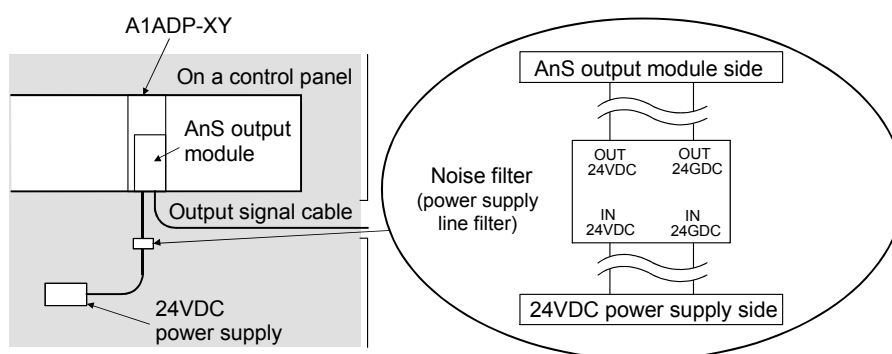
To configure a system meeting the requirements of the EMC and Low Voltage Directives when incorporating the Mitsubishi programmable controller (EMC and Low Voltage Directives compliant) into other machinery or equipment, refer to Chapter 3 "EMC and Low Voltage Directives" in the user's manual (hardware) for the CPU module used. For products compliant to the EMC and Low Voltage Directives, the CE marking is printed on the rating plate of the product.

In addition, to conform the products to the EMC and Low Voltage Directives, a noise filter (power supply line filter) needs to be attached as shown below.

- (a) When using the A1ADP-XY with an AnS series output module, attach any of the following noise filters (power supply line filters) to reduce conductive noise of 24VDC external supply power cable.

Noise filter model name	ZHC2203-11	ZHC2206-11	ZHC2210-11	MBS4830
Manufacturer	TDK		DENSEI-LAMBDA	
Rated current	3A	6A	10A	30A
Rated voltage	250V		48V	

- (b) Referring to the following, attach a noise filter (power supply line filter) to the 24VDC external supply power cable connected to the AnS series output module.



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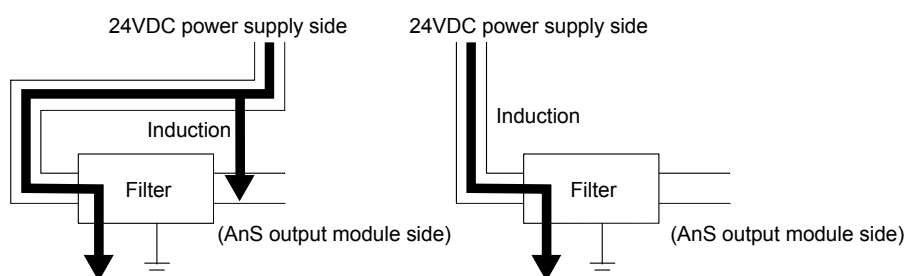
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other models

(c) The following describes the precautions for attaching a noise filter.

- 1) Do not bundle the wires on the input side and output side of the noise filter.

When bundled, the input side noise will be induced into the output side wires from which the noise was filtered.



- 1) The noise will be included when the input and output wires are bundled.

- 2) Separate and lay the input and output wires.

- 2) Earth the noise filter earthing terminal to the control cabinet with the shortest wire possible (approx. 10cm (3.94 in.)).

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other models

14.2 Compatible models list

The following table shows the availability of the AnS/QnAS series modules mounted to the A-A1S module conversion adapter.

"Mounting of the A1ADP" field ○: Mountable ×: Not mountable

"Applicable adapter" field XY: A1ADP-XY SP: A1ADP-SP -: Not available

Product	Model	Mounting of the A1ADP			Applicable adapter
		QCPU	QnACPU	ACPU	
CPU module	A1SJHCPU		×		-
	A1SHCPU		×		-
	A1SCPUC24-R2		×		-
	A2SHCPU		×		-
	A2USCPU		×		-
	A2USHCPU-S1		×		-
	Q2ASCPU		×		-
	Q2ASCPU-S1		×		-
	Q2ASHCPU		×		-
	Q2ASHCPU-S1		×		-
Input module	A1SX10		○		XY
	A1SX10EU		○		XY
	A1SX20		○		XY
	A1SX20EU		○		XY
	A1SX30		○		XY
	A1SX40		○		XY
	A1SX40-S1		○		XY
	A1SX40-S2		○		XY
	A1SX41		○		XY
	A1SX41-S1		○		XY
	A1SX41-S2		○		XY
	A1SX42		○		XY
	A1SX42-S1		○		XY
	A1SX42-S2		○		XY
	A1SX71		○		XY
	A1SX80		○		XY
	A1SX80-S1		○		XY
	A1SX80-S2		○		XY
	A1SX81		○		XY
	A1SX81-S2		○		XY
A1SX82-S1		○		XY	

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[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Product	Model	Mounting of the A1ADP			Applicable adapter
		QCPU	QnACPU	ACPU	
Output module	A1SY10		○		XY
	A1SY10EU		○		XY
	A1SY14EU		○		XY
	A1SY18A		○		XY
	A1SY18AEU		○		XY
	A1SY22		○		XY
	A1SY28A		○		XY
	A1SY40		○		XY
	A1SY40P		○		XY
	A1SY41		○		XY
	A1SY41P		○		XY
	A1SY42P		○		XY
	A1SY50		○		XY
	A1SY60		○		XY
	A1SY60E		○		XY
	A1SY68A		○		XY
	A1SY71		○		XY
	A1SY80		○		XY
	A1SY81		○		XY
	A1SY82		○		XY
I/O module	A1SH42		○		XY
	A1SH42P		○		XY
	A1SH42-S1		○		XY
	A1SH42P-S1		○		XY
	A1SX48Y58		○		XY
	A1SX48Y18		○		XY
	A1SJ-56DR		×		-
	A1SJ-56DT		×		-
Dynamic scan input module	A1S42X		○		XY
Dynamic scan output module	A1S42Y		○		XY
Dummy module	A1SG62		○		XY
Interrupt module	A1SI61		○		XY ^{*1}
Power supply module	A1S61PN		×		-
	A1S62PN		×		-
	A1S63P		×		-
Pulse catch module	A1SP60		○		XY
Analog timer module	A1ST60		○		XY

*1: Take care since the combination of the module type configured in the I/O assignment setting and the A1ADP model that can be combined differs.

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other models

Product	Model	Mounting of the A1ADP			Applicable adapter
		QCPU	QnACPU	ACPU	
Analog input module	A1S64AD		○		SP
	A1S68AD		○		SP
Analog output module	A1S62DA		○		SP
	A1S68DAI		○		SP
	A1S68DAV		○		SP
Analog I/O module	A1S63ADA		○		SP
	A1S66ADA		○		XY
Temperature input module	A1S62RD3N		○		SP
	A1S62RD4N		○		SP
	A1S68TD		○		SP
Temperature control module	A1S62TCTT-S2		○		SP
	A1S62TCRTBW-S2		○		SP
	A1S62TCRT-S2		○		SP
	A1S62TCTTBW-S2		○		SP
	A1S64TCTT-S1		○		SP
	A1S64TCTTBW-S1		○		SP
	A1S64TCRT-S1		○		SP
	A1S64TCRTBW-S1		○		SP
	A1S64TCTRT		○		SP
	A1S64TCTRTBW		○		SP
High-speed counter module	A1SD61		○		SP
	A1SD62		○		SP
	A1SD62E		○		SP
	A1SD62D		○		SP
	A1SD62D-S1		○		SP
Positioning module	A1SD70		×		-
	A1SD75M1		○		SP
	A1SD75M2		○		SP
	A1SD75M3		○		SP
	A1SD75P1-S3		○		SP
	A1SD75P2-S3		○		SP
	A1SD75P3-S3		○		SP
Position detection module	A1S62LS		○		SP
Intelligent communication module	A1SD51S		○		SP
Ethernet module	A1SJ71E71N-B2	×	○	○	SP
	A1SJ71E71N-B5	×	○	○	SP
	A1SJ71E71N3-T	×	○	○	SP
	A1SJ71QE71N-B2	×	○	×	SP
	A1SJ71QE71N-B5	×	○	×	SP
	A1SJ71QE71N3-T	×	○	×	SP

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other models

Product	Model	Mounting of the A1ADP			Applicable adapter
		QCPU	QnACPU	ACPU	
Serial communication module	A1SJ71QC24N	×	○	×	SP
	A1SJ71QC24N-R2	×	○	×	SP
	A1SJ71QC24N1	×	○	×	SP
	A1SJ71QC24N1-R2	×	○	×	SP
MELSECNET/B data link module	A1SJ71AT21B	×	○	○	SP
	A1SJ72T25B		×		-
MELSECNET data link module	A1SJ71AP21	×	○	○	SP
	A1SJ71AR21	×	○	○	SP
MELSECNET, MELSECNET/B local station data link module	A1SJ71AP23Q	○	×	×	SP
	A1SJ71AR23Q	○	×	×	SP
	A1SJ71AT23BQ	○	×	×	SP
MELSECNET/10 network module	A1SJ71LP21	×	×	○	SP
	A1SJ71BR11	×	×	○	SP
	A1SJ71LR21	×	×	○	SP
	A1SJ71QLP21	×	○	×	SP
	A1SJ71QLP21S		×		-
	A1SJ71QBR11	×	○	×	SP
	A1SJ71QLR21	×	○	×	SP
CC-Link system master/local module	A1SJ61BT11	×	×	○	SP
	A1SJ61QBT11	×	○	×	SP
MELSECNET/ MINI-S3 master module	A1SJ71PT32-S3		○ ^{*3}		SP
MELSEC-I/O LINK master module	A1SJ51T64		○		SP ^{*1}
JEMANET (OPCN-1) interface module	A1SJ71J92-S3		○		SP
	A1SJ72J95		×		-
B/NET interface module	A1SJ71B62-S3		○		SP
Computer link module	A1SJ71UC24-R2	×	○	○	SP
	A1SJ71UC24-PRF	×	○	○	SP
	A1SJ71UC24-R4	○ ^{*2}	○	○	SP
S-LINK master module	A1SJ71SL92N		○		SP
AS-i master module	A1SJ71AS92		○		SP
Modem interface module	A1SJ71CMO-S3	×	○	○	SP
PC fault detection module	A1SS91		○		SP ^{*1}
Memory card interface module	A1SD59J-S2		○		SP
ID interface module	A1SD35ID1		○		SP
	A1SD35ID2		○		SP
MODBUS module	A1SJ71UC24-R2-S2		○		SP
	A1SJ71UC24-R4-S2		○		SP
Profibus-DP interface module	A1SJ71PB92D		○		SP
	A1SJ71PB93D		○		SP
Profibus-FMS interface module	A1SJ71PB96F		○		SP
DeviceNet master module	A1SJ71DN91		○		SP

*1: Take care since the combination of the module type configured in the I/O assignment setting and the A1ADP model that can be combined differs.

*2: The adapter is mountable only when the multidrop link function is used.

*3: The A1SJ71PT32-S3 will be discontinued in September 2008.

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[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

14.3 Transition from the A series module to the AnS series module

Example

Production discontinuation		Transition to AnS series			
Product	Model	Model	Remarks (restrictions)	A1ADP usage ^{*1}	
					Usable adapter ^{*3}
Ethernet module	AJ71E71N-B2 670mA ^{*2}	A1SJ71E71N-B2 660mA ^{*2}	No restrictions	○	SP
Input module	AX21 110mA	A1SX20 50mA	1) External wiring: Changed Screw size: M3→M3.5 2) Number of slots: Changed (2 modules required) 3) Program Number of occupied I/O points: Not changed (32=16×2)	△	XY
	AX50-S1 55mA	None	Alternating with A1SX40 is recommended. 1) External wiring: Changed Connect a 4.7kΩ (1/2W or more) to the external signal wire serially. 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed	×	Not used
Positioning module	AD70 300mA	A1SD70 300mA	1) External wiring: Changed (The terminal block is changed.) 2) Number of slots: 1 slot→2 slots 3) Program: Not changed 4) Performance specifications change: Not changed 5) Function specifications: Not changed	×	Not used

*1: Indicates whether any restriction is given or not when mounting the A-A1S module conversion adapter and an A1S module (A module with the name provided in the Model column.).

○ : No restrictions

△ : Partially restricted.

The restriction outline is described in the Remark (restrictions) column.

×

The alternating method is described in the Remark (restrictions) column.

×(△ as for specifications) :

The performance specifications are compatible while the module cannot be mounted due to the expanded module width.

*2: Indicates 5VDC internal current consumption for each module.

Since the 5VDC internal current consumption for the A1ADP-XY (3.4mA) is not included to the value for the AnS series module, add it to each AnS series module. (A1ADP-XY (3.4mA) + AnS series module [5VDC internal current consumption])
The A1ADP-SP, whose 5VDC internal current consumption is 0mA, does not require the addition.

*3: Indicates the types of the A-A1S module conversion adapters can be mounted.

XY : A-A1S module conversion adapter of the A1ADP-XY type

SP: A-A1S module conversion adapter of the A1ADP-SP type

Not used : No mountable A-A1S module conversion adapters.

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other models

Point

- (1) When replacing the A series module by the A1ADP + AnS series module, the 5VDC internal current consumption may increase.
At replacement, make sure to check the 5VDC internal current consumption of the modules before and after replacement. If the 5VDC internal current consumption increases after the replacement, confirm that the current consumption of the modules used does not exceed the rated output current of the power supply module used.
- (2) When the A1ADP + AnS series module is installed to an extension base unit not needing a power supply module (A52B, A55B, or A58B) in the case that the increase in 5VDC internal current consumption may cause, voltage drop increases in the extension cable. Therefore, recalculating the receiving end voltage is required.
(For confirmation method, refer to the "Application standards of Extension Base Units" (A52B, A55B, or A58B) in the CPU module's User's Manual.)
- (3) If the total of 5VDC internal current consumption exceeds the rated output current of a power supply module, or receiving port voltage drops to less than 4.75VDC by the execution of (1) or (2) above, take the following measures.
 - 1) Review the system configuration.
 - 2) Do not use the transition models.
- (4) As for the following nine models, the current consumption is greatly increased by the transition. Pay special attention to the models in (1) to (3) above.
 - 1) AY70(100mA) → A1SY71(400mA)
 - 2) AY81(230mA) → A1SY81(500mA)
 - 3) AY82EP(290mA) → A1SY82(930mA)
 - 4) AH42(245mA) → A1SH42(500mA)
 - 5) A68DAI-S1(150mA) → A1S68DAI(850mA)
 - 6) A68DAV(150mA) → A1S68DAV(650mA)
 - 7) AJ71E71N-T(400mA) → A1SJ71E71N3-T(690mA)

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other models

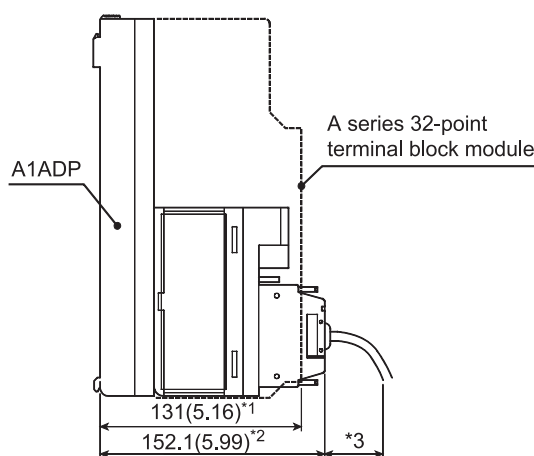
Point

(5) AnS series 32-point I/O modules and special function modules are connector type. Accordingly, when installing them to an A series base unit using the A1ADP, its depth is deeper than when installing an A series 32-point module.

When using the AnS series 32-point I/O modules or special function modules, confirm that there is enough room.

Example

When replacing the A series 32-point module



Unit: mm (inch)

*1: Depth dimension of the A series 32-point terminal block module

*2: Depth dimension of the A1ADP + AnS series 32-point connector type module

*3: Consider the bending radius of a connector cable.

(6) The AnS series output module with a fuse detects fuse blown if external supply power has not been input. Use special relay M9084 (error check) at power-on with the external supply power OFF so that fuse blown may not be detected.

(7) When mounting the A1ADP-XY+AnS series output module with a fuse on the MELSECNET/II remote I/O station (AJ72P25 or AJ72R25), the CPU module of the master station may detect "UNIT VERIFY ERR.". However, note that the AJ72P25 or AJ72R25 whose software version is "P" or later is used, "UNIT VERIFY ERR." will not be detected. Turning ON the power supply of the master station after turning ON the power supply of the remote I/O station and the 24VDC external power supply enables to avoid "UNIT VERIFY ERR.". Also, if the fuse blown is detected, cancel the error by the reset operation of the CPU module used.

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[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

(1) Transition from A series to AnS series

Production discontinuation		Transition to AnS series			
Product	Model	Model	Remarks (restrictions)	AIADP usage	
					Usable adapter
Input module	AX10	A1SX10	1) External wiring: Changed Screw size: M3→M3.5 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Not changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed	△	XY
	55mA	50mA	5) Functions: Not changed		
	AX10-UL	A1SX10	1) External wiring: Changed Screw size: M3→M3.5 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Not changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed	△	XY
	55mA	50mA	5) Functions: Not changed		
AX11	A1SX10	A1SX10	1) External wiring: Changed Screw size: M3→M3.5 2) Number of slots: Changed (2 modules required) 3) Program Number of occupied I/O points: Not changed (32=16×2) 4) Specifications Rated input voltage: Not changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed	△	XY
			110mA	50mA	5) Functions: Not changed
AX11EU	A1SX10EU	A1SX10EU	1) External wiring: Changed 2) Number of slots: Changed (2 modules required) 3) Program Number of occupied I/O points: Not changed (32=16×2) 4) Specifications Rated input voltage: Not changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed	△	XY
	150mA	50mA	5) Functions: Not changed		

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other models

Production discontinuation		Transition to AnS series			
Product	Model	Model	Remarks (restrictions)	AIADP usage	
					Usable adapter
Input module	AX20	A1SX20	1) External wiring: Changed Screw size: M3→M3.5 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Not changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed	△	XY
	55mA	50mA			
	AX20-UL	A1SX20	1) External wiring: Changed Screw size: M3→M3.5 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Not changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed	△	XY
	55mA	50mA			
	AX21	A1SX20	1) External wiring: Changed Screw size: M3→M3.5 2) Number of slots: Changed (2 modules required) 3) Program Number of occupied I/O points: Not changed (32=16×2) 4) Specifications Rated input voltage: Changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed	△	XY
	110mA	50mA			
	AX21EU	A1SX20EU	1) External wiring: Changed 2) Number of slots: Changed (2 modules required) 3) Program Number of occupied I/O points: Not changed (32=16×2) 4) Specifications Rated input voltage: Not changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed	△	XY
	150mA	50mA			

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[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to AnS series			
Product	Model	Model	Remarks (restrictions)	AIADP usage	
					Usable adapter
Input module	AX31	A1SX30	1) External wiring: Changed Screw size: M3→M3.5 2) Number of slots: Changed (2 modules required) 3) Program Number of occupied I/O points: Not changed (32=16×2) 4) Specifications Rated input voltage: Changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed	△	XY
	110mA	50mA			
	AX31-S1	A1SX41	1) External wiring: Changed (Connector terminal block must be converted.) 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Not changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed	△	XY
	110mA	80mA			
	AX40	A1SX40	1) External wiring: Changed Screw size: M3→M3.5 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Not changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed	△	XY
	55mA	50mA			
	AX40-UL	A1SX40	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Not changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed	△	XY
	55mA	50mA			

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[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to AnS series			
Product	Model	Model	Remarks (restrictions)	A1ADP usage	
					Usable adapter
Input module	AX41	A1SX41	1) External wiring: Changed (Connector terminal block must be converted.) 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Not changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed	△	XY
	110mA	80mA	5) Functions: Not changed		
	AX41-S1	A1SX41-S1	1) External wiring: Changed (Connector terminal block must be converted.) 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Changed (12VDC not applicable) Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed 6) Since 5VDC internal current consumption increases by combination with the A1ADP-XY, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	△	XY
	110mA	120mA			
	AX41-UL	A1SX41	1) External wiring: Changed (Connector terminal block must be converted.) 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Not changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed	△	XY
	110mA	80mA	5) Functions: Not changed		
	AX42	A1SX42	1) External wiring: Not changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Not changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed	△	XY
	120mA	90mA	5) Functions: Not changed		

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[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to AnS series			
Product	Model	Model	Remarks (restrictions)	A1ADP usage	
					Usable adapter
Input module	AX42-S1	A1SX42-S1	1) External wiring: Not changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Changed (12VDC not applicable) Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed 6) Since 5VDC internal current consumption increases by combination with the A1ADP-XY, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	△	XY
	120mA	160mA			
	AX50-S1	None	Alternating with A1SX40 is recommended. 1) External wiring: Changed Connect a 4.7kΩ (1/2W or more) to the external signal wire serially. 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed	×	Not used
	55mA				
	AX60-S1	None	Alternating with A1SX40 is recommended. 1) External wiring: Changed Connect a 15kΩ (3W or more) to the external signal wire serially. 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed	×	Not used
	55mA				

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[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to AnS series			
Product	Model	Model	Remarks (restrictions)	AIADP usage	
					Usable adapter
Input module	AX70	A1SX71	1) External wiring: Changed (Connector terminal block must be converted.) 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Changed 4) Specifications Rated input voltage: Not changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed 6) Since 5VDC internal current consumption increases by combination with the AIADP-XY, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	△	XY
	55mA	75mA			
	AX70-UL	A1SX71	1) External wiring: Changed (Connector terminal block must be converted.) 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Changed 4) Specifications Rated input voltage: Not changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed 6) Since 5VDC internal current consumption increases by combination with the AIADP-XY, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	△	XY
	55mA	75mA			
	AX71	A1SX71	1) External wiring: Changed (Connector terminal block must be converted.) 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Not changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed	△	XY
	110mA	75mA			

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[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to AnS series			
Product	Model	Model	Remarks (restrictions)	AIADP usage	
					Usable adapter
Input module	AX80	A1SX80	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Not changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed	△	XY
	55mA	50mA	5) Functions: Not changed		
	AX80-UL	A1SX80	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Not changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed	△	XY
	55mA	50mA	5) Functions: Not changed		
	AX80E	A1SX80-S1	1) External wiring: Changed Screw size: M3→M3.5 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Changed (12VDC not applicable) Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed	△	XY
	55mA	50mA	5) Functions: Not changed		
	AX81	A1SX81	1) External wiring: Changed (Connector terminal block must be converted.) 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Not changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed	△	XY
	110mA	80mA	5) Functions: Not changed		

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[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to AnS series			
Product	Model	Model	Remarks (restrictions)	AIADP usage	
					Usable adapter
Input module	AX81B	None	Alternating with A1SX81 is recommended. 1) External wiring: Changed (Connector terminal block must be converted.) 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Changed 4) Specifications Rated input voltage: Not changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: The wire breakage detection function not provided	×	Not used
	55mA				
	AX81-S1	A1SX81	1) External wiring: Changed (Connector terminal block must be converted.) 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Not changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed	△	XY
	105mA	80mA			
	AX81-S2	None	Alternating with A1SX81 is recommended. 1) External wiring: Changed (Connector terminal block must be converted.) Connect a 3.3kΩ (1/2W or more) or 8.2kΩ (1W or more) resistor serially to the external signal wire at 48VDC or 60VDC, respectively. 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Changed Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed	×	Not used
	110mA				

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[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to AnS series			
Product	Model	Model	Remarks (restrictions)	A1ADP usage	
					Usable adapter
Input module	AX81-S3	A1SX80-S1	1) External wiring: Changed Screw size: M3→M3.5 2) Number of slots: Changed (2 modules required) 3) Program Number of occupied I/O points: Changed 4) Specifications Rated input voltage: Changed (12VDC not applicable) Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed	△	XY
	110mA	50mA	5) Functions: Not changed		
	AX82	A1SX82-S1	1) External wiring: Changed (D sub→FCN connector) 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated input voltage: Changed (12VDC not applicable) Rated input current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed 6) Since 5VDC internal current consumption increases by combination with the A1ADP-XY, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	△	XY
	120mA	160mA			
Output module	AY10	A1SY10	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Not changed Rated output current: Not changed (However, contact life span is reduced to half.) 5) Functions: Not changed 6) Since 5VDC internal current consumption increases by combination with the A1ADP-XY, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	△	XY
	115mA	120mA			
	AY10A	A1SY18A	1) External wiring: Changed 2) Number of slots: Changed (2 modules required) 3) Program Number of occupied I/O points: Changed 4) Specifications Rated output voltage: Not changed Rated output current: Not changed 5) Functions: Not changed 6) Since 5VDC internal current consumption increases by combination with the A1ADP-XY, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	△	XY
	115mA	240mA			

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[Title] Production discontinuation of MELSEC-A/QnA (large type) series

[Date of Issue] February 2005 (Ver. N: January 2013)

[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to AnS series			
Product	Model	Model	Remarks (restrictions)	A1ADP usage	
					Usable adapter
Output module	AY10A-UL	A1SY18A	1) External wiring: Changed 2) Number of slots: Changed (2 modules required) 3) Program Number of occupied I/O points: Changed 4) Specifications Rated output voltage: Not changed Rated output current: Not changed 5) Functions: Not changed 6) Since 5VDC internal current consumption increases by combination with the A1ADP-XY, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	△	XY
	115mA	240mA			
	AY11	A1SY10	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Not changed Rated output current: Not changed (However, contact life span is reduced to half.) 5) Functions: Changed (No varistor, relay not replaceable) 6) Since 5VDC internal current consumption increases by combination with the A1ADP-XY, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	△	XY
	115mA	120mA			
	AY11-UL	A1SY10	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Not changed Rated output current: Not changed (However, contact life span is reduced to half.) 5) Functions: Changed (No varistor) 6) Since 5VDC internal current consumption increases by combination with the A1ADP-XY, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	△	XY
	115mA	120mA			
	AY11A	A1SY18A	1) External wiring: Changed 2) Number of slots: Changed (2 modules required) 3) Program Number of occupied I/O points: Changed 4) Specifications Rated output voltage: Not changed Rated output current: Not changed 5) Functions: Changed (No varistor) 6) Since 5VDC internal current consumption increases by combination with the A1ADP-XY, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	△	XY
	115mA	240mA			

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[Title] Production discontinuation of MELSEC-A/QnA (large type) series

[Date of Issue] February 2005 (Ver. N: January 2013)

[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to AnS series			
Product	Model	Model	Remarks (restrictions)	A1ADP usage	
					Usable adapter
Output module	AY11AEU	A1SY18AEU	1) External wiring: Changed 2) Number of slots: Changed (2 modules required) 3) Program Number of occupied I/O points: Changed 4) Specifications Rated output voltage: Not changed Rated output current: Not changed 5) Functions: Changed (No varistor) 6) Since 5VDC internal current consumption increases by combination with the A1ADP-XY, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	△	XY
	115mA	240mA			
	AY11E	A1SY10	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Not changed Rated output current: Not changed (However, contact life span is reduced to half.) 5) Functions: Changed (No fuse, no varistor) 6) Since 5VDC internal current consumption increases by combination with the A1ADP-XY, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	△	XY
	115mA	120mA			
	AY11EEU	A1SY10EU	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Not changed Rated output current: Not changed (However, contact life span is reduced to half.) 5) Functions: Changed (No fuse, no varistor) 6) Since 5VDC internal current consumption increases by combination with the A1ADP-XY, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	△	XY
	115mA	120mA			
	AY13	A1SY10	1) External wiring: Changed 2) Number of slots: Changed (2 modules required) Since 5VDC internal current consumption increases by combination with the A1ADP-XY, when using the two modules, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3). 3) Program Number of occupied I/O points: Not changed (32=16×2) 4) Specifications Rated output voltage: Not changed Rated output current: Not changed (However, contact life span is reduced to half.) 5) Functions: Not changed	△	XY
	230mA	120mA			

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[Title] Production discontinuation of MELSEC-A/QnA (large type) series

[Date of Issue] February 2005 (Ver. N: January 2013)

[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to AnS series			
Product	Model	Model	Remarks (restrictions)	AIADP usage	
					Usable adapter
Output module	AY13E	A1SY10	1) External wiring: Changed 2) Number of slots: Changed (2 modules required) Since 5VDC internal current consumption increases by combination with the AIADP-XY, when using the two modules, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3). 3) Program Number of occupied I/O points: Not changed (32=16×2) 4) Specifications Rated output voltage: Not changed Rated output current: Not changed (However, contact life span is reduced to half.) 5) Functions: Changed (No fuse)	△	XY
	230mA	120mA			
	AY13EU	A1SY10EU	1) External wiring: Changed 2) Number of slots: Changed (2 modules required) Since 5VDC internal current consumption increases by combination with the AIADP-XY, when using the two modules, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3). 3) Program Number of occupied I/O points: Not changed (32=16×2) 4) Specifications Rated output voltage: Not changed Rated output current: Not changed (However, contact life span is reduced to half.) 5) Functions: Not changed	△	XY
	230mA	120mA			
	AY15EU	A1SY14EU	1) External wiring: Changed 2) Number of slots: Changed (2 modules required) Since 5VDC internal current consumption increases by combination with the AIADP-XY, when using the two modules, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3). 3) Program Number of occupied I/O points: Not changed (32=16×2) 4) Specifications Rated output voltage: Not changed Rated output current: Not changed (However, contact life span is reduced to half.) 5) Functions: Not changed	△	XY
	150mA	120mA			
	AY22	A1SY22	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Not changed Rated output current: Changed (Output 2A→0.6A) 5) Functions: Changed (No fuse, no varistor)	△	XY
	305mA	270mA			

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[Title] Production discontinuation of MELSEC-A/QnA (large type) series

[Date of Issue] February 2005 (Ver. N: January 2013)

[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to AnS series			
Product	Model	Model	Remarks (restrictions)	AIADP usage	
					Usable adapter
Output module	AY23	A1SY22	1) External wiring: Changed 2) Number of slots: Changed (2 modules required) 3) Program Number of occupied I/O points: Not changed (32=16×2) 4) Specifications Rated output voltage: Not changed Rated output current: Not changed	△	XY
	590mA	270mA	5) Functions: Changed (No fast blow fuse)		
	AY40	A1SY40P	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Not changed Rated output current: Not changed	△	XY
	115mA	79mA	5) Functions: Not changed		
	AY40-UL	A1SY40	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Not changed Rated output current: Not changed 5) Functions: Not changed 6) Since 5VDC internal current consumption increases by combination with the A1ADP-XY, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	△	XY
	115mA	270mA			
	AY40A	A1SY68A	1) External wiring: Changed 2) Number of slots: Changed (2 modules required) Since 5VDC internal current consumption increases by combination with the A1ADP-XY, when using the two modules, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3). 3) Program Number of occupied I/O points: Changed 4) Specifications Rated output voltage: Not changed Rated output current: Not changed Response: Slow	△	XY
	190mA	110mA	5) Functions: Not changed		

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[Title] Production discontinuation of MELSEC-A/QnA (large type) series

[Date of Issue] February 2005 (Ver. N: January 2013)

[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to AnS series			
Product	Model	Model	Remarks (restrictions)	AIADP usage	
					Usable adapter
Output module	AY41	A1SY41P	1) External wiring: Changed (Connector terminal block must be converted.) 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Not changed Rated output current: Not changed 5) Functions: Not changed	△	XY
	230mA	141mA			
	AY41-UL	A1SY41P	1) External wiring: Changed (Connector terminal block must be converted.) 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Not changed Rated output current: Not changed 5) Functions: Not changed 6) Since SVDC internal current consumption increases by combination with the AIADP-XY, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	△	XY
	230mA	141mA			
	AY42	A1SY42P	1) External wiring: Not changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Not changed Rated output current: Not changed 5) Functions: Not changed	○	XY
	340mA	170mA			
	AY42-S1	A1SY42P	1) External wiring: Not changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Not changed Rated output current: Not changed Response time: Changed (from 0.3ms to 1ms or less) 5) Functions: Not changed	△	XY
	290mA	170mA			
	AY42-S3	A1SY42P	1) External wiring: Not changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Not changed Rated output current: Not changed 5) Functions: Changed (The short protection function equivalent to fuse included)	○	XY
	290mA	170mA			

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[Title] Production discontinuation of MELSEC-A/QnA (large type) series

[Date of Issue] February 2005 (Ver. N: January 2013)

[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to AnS series			
Product	Model	Model	Remarks (restrictions)	AIADP usage	
					Usable adapter
Output module	AY42-S4	A1SY42P	1) External wiring: Changed (External power supply is required.) 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Not changed Rated output current: Not changed 5) Functions: Not changed	△	XY
	500mA	170mA			
	AY50	A1SY50	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Not changed Rated output current: Not changed 5) Functions: Changed (Fuse not replaceable) 6) Since 5VDC internal current consumption increases by combination with the A1ADP-XY, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	△	XY
	115mA	120mA			
	AY50-UL	A1SY50	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Not changed Rated output current: Not changed 5) Functions: Changed (Fuse not replaceable) 6) Since 5VDC internal current consumption increases by combination with the A1ADP-XY, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	△	XY
	115mA	120mA			
	AY51	A1SY50	1) External wiring: Changed 2) Number of slots: Changed (2 modules required) Since 5VDC internal current consumption increases by combination with the A1ADP-XY, when using the two modules, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3). 3) Program Number of occupied I/O points: Not changed (32=16×2) 4) Specifications Rated output voltage: Not changed Rated output current: Not changed 5) Functions: Not changed	△	XY
	230mA	120mA			

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[Title] Production discontinuation of MELSEC-A/QnA (large type) series

[Date of Issue] February 2005 (Ver. N: January 2013)

[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to AnS series			
Product	Model	Model	Remarks (restrictions)	AIADP usage	
					Usable adapter
Output module	AY51-S1	A1SY50	1) External wiring: Changed 2) Number of slots: Changed (2 modules required) Since 5VDC internal current consumption increases by combination with the A1ADP-XY, when using the two modules, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3). 3) Program Number of occupied I/O points: Not changed (32=16×2) 4) Specifications Rated output voltage: Not changed Rated output current: Not changed	△	XY
	230mA	120mA	5) Functions: Changed (Fuse not replaceable)		
	AY51-UL	A1SY50	1) External wiring: Changed 2) Number of slots: Changed (2 modules required) Since 5VDC internal current consumption increases by combination with the A1ADP-XY, when using the two modules, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3). 3) Program Number of occupied I/O points: Not changed (32=16×2) 4) Specifications Rated output voltage: Not changed Rated output current: Not changed	△	XY
	230mA	120mA	5) Functions: Not changed		
	AY60	A1SY60	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Changed (48VDC not applicable) Rated output current: Not changed 5) Functions: Not changed 6) Since 5VDC internal current consumption increases by combination with the A1ADP-XY, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	△	XY
	115mA	120mA			
	AY60E	A1SY60E	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Changed (48VDC not applicable) Rated output current: Not changed 5) Functions: Not changed 6) Since 5VDC internal current consumption increases by combination with the A1ADP-XY, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	△	XY
	115mA	200mA			

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[Title] Production discontinuation of MELSEC-A/QnA (large type) series

[Date of Issue] February 2005 (Ver. N: January 2013)

[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to AnS series			
Product	Model	Model	Remarks (restrictions)	AIADP usage	
					Usable adapter
Output module	AY60S	A1SY60	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Changed 4) Specifications Rated output voltage: Changed (48VDC not applicable) Rated output current: Not changed 5) Functions: Not changed 6) Since 5VDC internal current consumption increases by combination with the A1ADP-XY, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	△	XY
	75mA	120mA			
	AY60S-UL	A1SY60	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Changed 4) Specifications Rated output voltage: Changed (48VDC not applicable) Rated output current: Not changed 5) Functions: Not changed 6) Since 5VDC internal current consumption increases by combination with the A1ADP-XY, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	△	XY
	75mA	120mA			
	AY70	A1SY71	1) External wiring: Changed (Connector terminal block must be converted.) 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Changed 4) Specifications Rated output voltage: Not changed Rated output current: Not changed 5) Functions: Not changed 6) Since 5VDC internal current consumption increases by combination with the A1ADP-XY, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	△	XY
	100mA	400mA			
	AY70-UL	A1SY71	1) External wiring: Changed (Connector terminal block must be converted.) 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Changed 4) Specifications Rated output voltage: Not changed Rated output current: Not changed 5) Functions: Not changed 6) Since 5VDC internal current consumption increases by combination with the A1ADP-XY, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	△	XY
	100mA	400mA			

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[Title] Production discontinuation of MELSEC-A/QnA (large type) series

[Date of Issue] February 2005 (Ver. N: January 2013)

[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to AnS series			
Product	Model	Model	Remarks (restrictions)	AIADP usage	
					Usable adapter
Output module	AY71	A1SY71	1) External wiring: Changed (Connector terminal block must be converted.) 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Not changed Rated output current: Not changed 5) Functions: Not changed 6) Since 5VDC internal current consumption increases by combination with the A1ADP-XY, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	△	XY
	200mA	400mA			
	AY72	A1SY71	1) External wiring: Not changed 2) Number of slots: Changed (2 modules required) 3) Program Number of occupied I/O points: Not changed (64=32×2) 4) Specifications Rated output voltage: Not changed Rated output current: Not changed 5) Functions: Not changed 6) Since 5VDC internal current consumption increases by combination with the A1ADP-XY, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	△	XY
	300mA	400mA			
	AY80	A1SY80	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Not changed Rated output current: Not changed 5) Functions: Changed (Fuse not replaceable) 6) Since 5VDC internal current consumption increases by combination with the A1ADP-XY, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	△	XY
	115mA	120mA			
	AY81	A1SY81	1) External wiring: Changed (Connector terminal block must be converted.) 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Not changed Rated output current: Changed (Output 0.5A→0.1A) 5) Functions: Not changed 6) Since 5VDC internal current consumption increases by combination with the A1ADP-XY, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	△	XY
	230mA	500mA			

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[Title] Production discontinuation of MELSEC-A/QnA (large type) series

[Date of Issue] February 2005 (Ver. N: January 2013)

[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to AnS series			
Product	Model	Model	Remarks (restrictions)	AIADP usage	
					Usable adapter
Output module	AY82-EP	A1SY82	1) External wiring: Changed (D sub→FCN connector) 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Not changed Rated output current: Not changed 5) Functions: Not changed 6) Since 5VDC internal current consumption increases by combination with the A1ADP-XY, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	△	XY
	290mA	930mA			
I/O module	AH42	A1SH42	1) External wiring: Not changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Changed (32 points occupied) 4) Specifications Rated output voltage: Changed (12VDC not applicable) Rated output current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed 6) Since 5VDC internal current consumption increases by combination with the A1ADP-XY, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	△	XY
	245mA	500mA			
Dynamic scan I/O module	A42XY	A1S42X	1) External wiring: Changed 2) Number of slots: Changed Since 5VDC internal current consumption increases by combination with the A1ADP-XY, when using the two modules, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3). 3) Program Number of occupied I/O points: Changed (128 points occupied: 64×2) 4) Specifications Rated output voltage: Changed (12VDC not applicable) Rated output current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Not changed	△	XY
		80mA A1S42Y			
Dummy module	AG62 70mA	A1SG62 60mA	No restrictions	○	XY
Blanking module	AG60	A1SG60	No restrictions	○	XY/SP

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[Title] Production discontinuation of MELSEC-A/QnA (large type) series

[Date of Issue] February 2005 (Ver. N: January 2013)

[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to AnS series			
Product	Model	Model	Remarks (restrictions)	AIADP usage	
					Usable adapter
Interrupt module	AI61	A1SI61	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Not changed 4) Specifications Rated output voltage: Not changed Rated output current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Changed (Interrupt processing condition can be set in 4-point unit.)	△	XY
	140mA	57mA			
	AI61-S1	A1SI61	1) External wiring: Changed 2) Number of slots: Not changed 3) Program Number of occupied I/O points: Changed (16 points occupied) 4) Specifications Rated output voltage: Not changed Rated output current: Changed ON voltage/ON current: Changed OFF voltage/OFF current: Changed Input resistance: Changed 5) Functions: Changed (Interrupt processing condition can be set in 4-point unit.) 6) Others: The response time is different.	△	XY
	140mA	57mA			
Analog input module	A616AD	None	Using the A1S68AD is recommended. 1) External wiring: Changed (Terminal block is different.) 2) Number of slots: Changed (2 modules required) 3) Program: I/O signals and buffer memory address are changed. 4) Performance specifications change: 8CH/module, input signals (Only plus current can be input.) 5) Function specifications: Multiplexer function not available	×	Not used
	1000mA				
	A68AD	A1S68AD	1) External wiring: Changed (Terminal block is different.) 2) Number of slots: Not changed 3) Program: I/O signals and buffer memory address are changed. 4) Performance specifications change: I/O characteristics 5) Function specifications: Setting method of the A/D conversion disable function has been changed. 6) Since 5VDC internal current consumption increases by combination with the A1ADP-SP, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	△	SP
	390mA	400mA			
	A68AD-S2	A1S68AD	1) External wiring: Changed (Terminal block is different.) 2) Number of slots: Not changed 3) Program: I/O signals and buffer memory address are changed. 4) Performance specifications change: I/O characteristics 5) Function specifications: Not changed 6) Since 5VDC internal current consumption increases by combination with the A1ADP-SP, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	△	SP
	390mA	400mA			

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[Title] Production discontinuation of MELSEC-A/QnA (large type) series

[Date of Issue] February 2005 (Ver. N: January 2013)

[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to AnS series			
Product	Model	Model	Remarks (restrictions)	AIADP usage	
					Usable adapter
Analog input module	A68ADN	A1S68AD	1) External wiring: Changed (Terminal block is different.) 2) Number of slots: Not changed 3) Program: I/O signals and buffer memory address are changed. 4) Performance specifications change: I/O characteristics and resolution 5) Function specifications: Not changed	△	SP
	400mA	400mA			
Multiplexer	A60MX 650mA	None	Alternating with multiple A1S68AD modules is recommended.	×	Not used
	A60MXRN 350mA	None	Using multiple A1S68ADs and perform isolation between channels is recommended.	×	Not used
	A60MXR 500mA	None	Using multiple A1S68ADs and perform isolation between channels is recommended.	×	Not used
	A60MXTN 640mA	None	Alternating with multiple A1S68TD modules is recommended.	×	Not used
	A60MXT 800mA	None	Alternating with multiple A1S68TD modules is recommended.	×	Not used
Analog output module	A616DAI	None	Using the A1S68DAI is recommended. 1) External wiring: Changed (Terminal block is different.) 2) Number of slots: Changed (2 modules required) 3) Program: I/O signals and buffer memory address are changed. 4) Performance specifications change: 8CH/module, input current range 5) Function specifications: The relation between the D/A conversion disable channel and the conversion time is changed.	×	Not used
	300mA				
	A616DAV	None	Using the A1S68DAV is recommended. 1) External wiring: Changed (Terminal block is different.) 2) Number of slots: Changed (2 modules required) 3) Program: I/O signals and buffer memory address are changed. 4) Performance specifications change: 8CH/module, resolution and accuracy 5) Function specifications: The relation between the D/A conversion disable channel and the conversion time is changed.	×	Not used
	380mA				
Analog output module	A62DA	A1S62DA	1) External wiring: Changed (Terminal block is different.) 2) Number of slots: Not changed 3) Program: I/O signals and buffer memory address are changed. 4) Performance specifications change: I/O characteristics and conversion time 5) Function specifications: Not changed 6) Since 5VDC internal current consumption increases by combination with the A1ADP-SP, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	△	SP
	600mA	800mA			
	A62DA-S1	A1S62DA			
600mA	800mA			△	SP

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[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to AnS series			
Product	Model	Model	Remarks (restrictions)	AIADP usage	
					Usable adapter
Analog output module	A68DAI-S1	A1S68DAI	1) External wiring: Changed (Terminal block is different.) 2) Number of slots: Not changed 3) Program: I/O signals and buffer memory address are changed. 4) Performance specifications change: Output current range, I/O characteristics, and increased current consumption 5) Function specifications: Not changed 6) Since 5VDC internal current consumption increases by combination with the A1ADP-SP, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	△	SP
	150mA	850mA			
	A68DAV	A1S68DAV	1) External wiring: Changed (Terminal block is different.) 2) Number of slots: Not changed 3) Program: I/O signals and buffer memory address are changed. 4) Performance specifications change: Output current range, I/O characteristics, and increased current consumption 5) Function specifications: Not changed 6) Since 5VDC internal current consumption increases by combination with the A1ADP-SP, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	△	SP
	150mA	650mA			
Temperature input module	A616TD	None	Using the A1S68TD is recommended. 1) External wiring: Changed (Terminal block is different.) 2) Number of slots: Changed (2 modules required) 3) Program: I/O signals and buffer memory address are changed. 4) Performance specifications change: 8CH/module, input temperature range, and conversion accuracy 5) Function specifications: The relation between the conversion disable channel and the conversion time is changed.	×	Not used
	1000mA				
	A68RD3N	None	Using the A1S62RD3N is recommended. 1) External wiring: Changed (Terminal block is different.) 2) Number of slots: Changed (4 modules required) 3) Program: Changed 4) Performance specifications change: 4CH/module 5) Function specifications: Not changed	×	Not used
	940mA				
	A68RD4N	None	Using the A1S62RD4N is recommended. 1) External wiring: Changed (Terminal block is different.) 2) Number of slots: Changed (4 modules required) 3) Program: Changed 4) Performance specifications change: 4CH/module 5) Function specifications: Not changed	×	Not used
	410mA				

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[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to AnS series			
Product	Model	Model	Remarks (restrictions)	AIADP usage	
					Usable adapter
High-speed counter module	AD61	A1SD62	1) External wiring: Changed (Terminal block is different.) 2) Number of slots: Not changed 3) Program: Buffer memory address is changed. 4) Performance specifications change: Upward-compatibility 5) Function specifications: Upward-compatibility	△	SP
	300mA	100mA			
	AD61-S1	A1SD62	1) External wiring: Changed (Terminal block is different.) 2) Number of slots: Not changed 3) Program: Buffer memory address is changed. 4) Performance specifications change: Upward-compatibility 5) Function specifications: Upward-compatibility	△	SP
	300mA	100mA			
Positioning module	AD70	A1SD70	1) External wiring: Changed (Terminal block is different.) 2) Number of slots: 1 slot→2 slots 3) Program: Not changed 4) Performance specifications change: Not changed 5) Function specifications: Not changed	×	Not used
	300mA	300mA		*1	
	AD72	None	No alternative model	×	Not used
	900mA				
	AD75M1	A1SD75M1	No restrictions The A1SD75-C01HA cable is required since the peripheral device connection connector is different.	○	SP
	700mA	700mA			
	AD75M2	A1SD75M2	No restrictions The A1SD75-C01HA cable is required since the peripheral device connection connector is different.	○	SP
	700mA	700mA			
	AD75M3	A1SD75M3	No restrictions The A1SD75-C01HA cable is required since the peripheral device connection connector is different.	○	SP
	700mA	700mA			
AD75P1-S3	A1SD75P1-S3	No restrictions The A1SD75-C01HA cable is required since the peripheral device connection connector is different.	○	SP	
700mA	700mA				
AD75P2-S3	A1SD75P2-S3	No restrictions The A1SD75-C01HA cable is required since the peripheral device connection connector is different.	○	SP	
700mA	700mA				
AD75P3-S3	A1SD75P3-S3	No restrictions The A1SD75-C01HA cable is required since the peripheral device connection connector is different.	○	SP	
700mA	700mA				
Position detection module	A61LS	None	No alternative model	×	Not used
	800mA				
	A62LS-S5	None	No alternative model	×	Not used
1500mA					
A63LS	None	No alternative model	×	Not used	
1350mA					

*1: As for specifications, △

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other models

Production discontinuation		Transition to AnS series			
Product	Model	Model	Remarks (restrictions)	AIADP usage	
					Usable adapter
Intelligent communication	AD51H-S3 1000mA	A1SD51S 400mA	The A1SD51S is different from the AD51H-S3 in the following specifications. AD51H-S3 → A1SD51S 1) Number of tasks: 8→2 2) Memory: 300k→60kbytes 3) Parallel: Available→None 4) RS-232 connector: 25-pin→9-pin 5) Number of slots: 2→1 (One slot will be an empty slot.) 6) Memory card I/F: 2→0 (File creation is disabled.) 7) LED display not provided 8) Program record medium: Memory card, EPROM→built-in EEPROM	△	SP
	AD51-S3 1300mA	A1SD51S 400mA	Replace the BASIC program with a program for A1SD51S.	△	SP
Ethernet module	AJ71E71N-B2 670mA	A1SJ71E71N-B2 660mA	No restrictions	○	SP
	AJ71E71N-B5 550mA	A1SJ71E71N-B5 570mA	Since 5VDC internal current consumption increases by combination with the A1ADP-SP, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	○	SP
	AJ71E71N3-T 690mA	A1SJ71E71N3-T 690mA	No restrictions	○	SP
MELSECNET/10 network module	AJ71LP21 650mA	A1SJ71LP21 650mA	No restrictions	○	SP
	AJ71LP21G 650mA	None	No alternative model	×	Not used
	AJ71BR11 800mA	A1SJ71BR11 800mA	No restrictions	○	SP
	AJ71LR21 1200mA	A1SJ71LR21 1140mA	No restrictions	○	SP
MELSECNET/B data link module	AJ71AT21B 720mA	A1SJ71AT21B 660mA	No restrictions	○	SP
MELSECNET data link module	AJ71AP21 500mA	A1SJ71AP21 330mA	No restrictions	○	SP
	AJ71AR21 900mA	A1SJ71AR21 800mA	No restrictions	○	SP
CC-Link master/local module	AJ61BT11 450mA	A1SJ61BT11 400mA	No restrictions	○	SP
MELSECNET/MINI-S3 master module	AJ71PT32-S3 350mA	A1SJ71PT32-S3 350mA	Monitor station function not available	△	SP
	AJ71T32-S3 300mA	A1SJ71PT32-S3 350mA	1) Monitor station function not available 2) Since 5VDC internal current consumption increases by combination with the A1ADP-SP, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	△	SP
	AJ71T32-S4 300mA	None	Changing the system from MELSECNET/MINI-S3 to CC-Link is recommended.	×	Not used
MELSEC-I/OLINK master module	AJ51T64 115mA	A1SJ51T64 115mA	No restrictions	○	SP

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[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to AnS series			
Product	Model	Model	Remarks (restrictions)	AIADP usage	
					Usable adapter
JEMANET (OPCN-1) interface module	AJ71J92-S3 500mA	A1SJ71J92-S3 400mA	No restrictions	○	SP
B/NET interface module	AJ71B62-S3 170mA	A1SJ71B62-S3 80mA	No restrictions	○	SP
Terminal interface module	AJ71C21-S1 900mA	None	No alternative model	×	Not used
Multidrop link module	AJ71C22-S1 1400mA	A1SJ71UC24-R4 100mA	The following functions are different. 1) Buffer memory Work area: 61h to 07FF→71h to 0DFFh 2) LED For slave station I/O monitor display: Available→None 3) Setting switch Baud rate setting: Fixed to 38400bps→Settable to 19200/38400 Master/local: Fixed to master→Settable 4) Terminal block screw M4→M3.5 5) Terminal resistor Built-in→externally connected	△	SP
Host controller high-speed link	AJ71C23-S3 1500mA	None	No alternative model	×	Not used
Computer link module	AJ71UC24	A1SJ71UC24-PRF 100mA	1) Transmission specification setting switches When this module meets the following two requirements, turn on the SW03 switch by using the module that has software version X or later. • For installing the A1SJ71UC24-PRF/R2/R4 to the unit that has a AnACPU. • For using the computer link function. 2) Either the RS-232 connector or RS-422/485 terminal block A1SJ71UC24-PRF/R2/R4 is available. 3) For the A1SJ71UC24-PRF/R2/R4, the linked operation function between the RS-232 and RS-422 is not available. 4) Number of RS-232 connector pins 25-pin→9-pin	△	SP
		A1SJ71UC24-R2 100mA			
		A1SJ71UC24-R4 100mA			
		300mA			
	AJ71C24-S1 1400mA	None	No alternative model	×	Not used
	AJ71C24-S7 1400mA	None	No alternative model	×	Not used
MODBUS module	AJ71UC24-S2 1400mA	A1SJ71UC24-R2-S2 100mA	Either RS-232 or RS-422/485 interface is available. For AnS series, the linked operation between the RS-232 and RS-422 is not available. RS-232 connector: 25-pin→9-pin	△	SP
		A1SJ71UC24-R4-S2 100mA			
Profibus-DP interface module	AJ71PB92D 540mA	A1SJ71PB92D 560mA	Since 5VDC internal current consumption increases by combination with the AIADP-SP, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	○	SP
Profibus-FMS interface module	AJ71PB96F 540mA	A1SJ71PB96F 560mA	Since 5VDC internal current consumption increases by combination with the AIADP-SP, checking power capacity and receiving end voltage is required (Refer to POINT (1) to (3) in Section 14.3).	○	SP

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[Date of Issue] February 2005 (Ver. N: January 2013)

[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Production discontinuation		Transition to AnS series			
Product	Model	Model	Remarks (restrictions)	AIADP usage	
					Usable adapter
DeviceNet master module	AJ71DN91 240mA	A1SJ71DN91 240mA	No restrictions	○	SP
Supersonic linear scale module	A64BTL 1050mA	None	No alternative model	×	Not used
External error check module	AD51FD-S3 1000mA	None	No alternative model	×	Not used
PC fault detection module	AS91 80mA	A1SS91 80mA	No restrictions	○	SP
Vision sensor module	AS25VS 2620mA	None	Connecting a commercially available vision sensor and a programmable controller with RS232, Ethernet or Digital I/O for data loading is recommended.	×	Not used
	AS50VS 3300mA	None	Connecting a commercially available vision sensor and a programmable controller with RS232, Ethernet or Digital I/O for data loading is recommended.	×	Not used

(2) Transition from QnA series to AnS series

Production discontinuation		Transition to AnS series			
Product	Model	Model	Remarks (restrictions)	AIADP usage	
					Usable adapter
Ethernet module	AJ71QE71N-B2 560mA	A1SJ71QE71N-B2 530mA	No restrictions	○	SP
	AJ71QE71N-B5 400mA	A1SJ71QE71N-B5 400mA	No restrictions	○	SP
	AJ71QE71N3-T 530mA	A1SJ71QE71N3-T 530mA	No restrictions	○	SP
Serial communication module	AJ71QC24N 400mA	A1SJ71QC24N 350mA	RS-232 connector: 25-pin→9-pin	△	SP
	AJ71QC24N-R2 300mA	A1SJ71QC24N-R2 300mA	RS-232 connector: 25-pin→9-pin	△	SP
	AJ71QC24N-R4 600mA	A1SJ71QC24N 350mA	For Q2AS series, use A1SJ71QC24N and connect the RS232-422 converter to 1ch.	△	SP
CC-Link master/local module	AJ61QBT11 450mA	A1SJ61QBT11 100mA	No restrictions	○	SP
MELSECNET/10 network module	AJ71QLP21 650mA	A1SJ71QLP21 400mA	No restrictions	○	SP
	AJ71QLP21S 650mA	A1SJ71QLP21S 400mA	No restrictions	○	SP
	AJ71QLP21G 650mA	None	No alternative model	×	Not used
	AJ71QBR11 800mA	A1SJ71QBR11 800mA	No restrictions	○	SP
	AJ71QLR21 1140mA	A1SJ71QLR21 1140mA	No restrictions	○	SP

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[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

(3) Transition from Q4AR series to AnS series

Production discontinuation		Transition to AnS series			
Product	Model	Model	Remarks (restrictions)	A1ADP usage	
					Usable adapter
Ethernet module	AJ71QE71N-B2 560mA	A1SJ71QE71N-B2 530mA	No restrictions	○	SP
	AJ71QE71N-B5 400mA	A1SJ71QE71N-B5 400mA	No restrictions	○	SP
	AJ71QE71N3-T 530mA	A1SJ71QE71N3-T 530mA	No restrictions	○	SP
Serial communication module	AJ71QC24N 400mA	A1SJ71QC24N 350mA	RS-232 connector: 25-pin→9-pin	△	SP
	AJ71QC24N-R2 300mA	A1SJ71QC24N-R2 300mA	RS-232 connector: 25-pin→9-pin	△	SP
	AJ71QC24N-R4 600mA	A1SJ71QC24N 350mA	For Q2AS series, use A1SJ71QC24N and connect the RS232-422 converter to 1ch.	△	SP
CC-Link master/local module	AJ61QBT11 450mA	A1SJ61QBT11 100mA	No restrictions	○	SP
MELSECNET/10 network module	AJ71QLP21 650mA	A1SJ71QLP21 400mA	No restrictions	○	SP
	AJ71QLP21S 650mA	A1SJ71QLP21S 400mA	No restrictions	○	SP
	AJ71QLP21G 650mA	None	No alternative model	×	Not used
	AJ71QBR11 800mA	A1SJ71QBR11 800mA	No restrictions	○	SP
	AJ71QLR21 1140mA	A1SJ71QLR21 1140mA	No restrictions	○	SP

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[Relevant Models] AnNCPU, AnACPU, AnUCPU, QnACPU, Q4ARCPU, A2C(J)CPU,
other models

Revision

Version	Print date	Revision
-	February 2005	First edition
A	February 2005	Writing errors have been corrected.
B	March 2005	Information such as maintenance support tools has been added.
C	May 2005	The descriptions of spare parts and the A-A1S module conversion adapter have been added.
D	July 2005	The "Spare parts storage" section has been added. The specifications of the A-A1S module conversion adapter have been added.
E	August 2005	Writing errors have been corrected. Descriptions of the AJ71E71N-T and AJ71QE71N-T, which was discontinued in July 2005, have been deleted. (For details, refer to the TECHNICAL BULLETIN T09-0018.)
F	June 2006	Writing errors have been corrected. The specifications of the A-A1S module conversion adapter have been added.
G	October 2006	The description in "Countermeasures for preventing aluminum electrolytic capacitor characteristics deterioration" has been corrected. The following modules have been added. AC06B-UL, AC12B-UL, AC30B-UL, A3NMCA-2-UL, A3NMCA-4-UL, A3NMCA-8-UL, A3NMCA-16-UL, A3NMCA-24-UL, A3NMCA-40-UL, AX10-UL, AX20-UL, AX70-UL, AX80-UL, AY10A-UL, AY40-UL, AY50-UL, AY60S-UL, AY70-UL Since production of the large type MELSECNET/10 network module has been continued, the following modules have been deleted from the "Compatible models list" for the A-A1S module conversion adapter. A1SJ71LP21, A1SJ71BR11, A1SJ71LR21, A1SJ71QLP21, A1SJ71QLP21S, A1SJ71QLR21, A1SJ72QLP25, A1SJ72QLR25, A1SJ71QBR11, A1SJ72QBR15
H	May 2008	The model whose production has been continued has been changed as follows: A61P → A61PN The alternative models have been changed as follows: Q62DA → Q62DAN, Q68DAV → Q68DAVN, Q68DAI → Q68DAIN Writing errors have been corrected. The description on the A6MEM-1024KAW, which was discontinued in September 2000, has been deleted. (For details, refer to the TECHNICAL BULLETIN T10-0009.)
I	October 2008	The replaceable models of the A1NCPUR21, A1NCPUP21, and A1NCPUR21 have been corrected. The QD62-H01 has been added as an alternative model of the discontinued model, AD61. The QD62-H02 has been added as an alternative model of the discontinued model, AD61-S1.
J	October 2008	The A1S64TCTRT and A1S64TCTRTBW have been added to the "Compatible models list" for the A-A1S module conversion adapter.
K	December 2008	The restriction of the A1SJ71UC24-PRF, A1SJ71UC24-R2, and A1SJ71UC24-R4 has been corrected.
L	September 2010	The "Compatible models list" for the A-A1S module conversion adapter has been reviewed.
M	October 2012	<ul style="list-style-type: none"> • Descriptions on the production discontinuation timing of the large type MELSECNET/10 network modules have been corrected. • Descriptions on the production discontinuation of the AnS/Q2AS (small type) series have been corrected. • The following modules have been added to the "Compatible models list" for the A-A1S module conversion adapter: A1SJ71LP21, A1SJ71BR11, A1SJ71LR21, A1SJ71QLP21, A1SJ71QLP21S, A1SJ71QBR11, and A1SJ71QLR21.
N	January 2013	<ul style="list-style-type: none"> • Descriptions on the production discontinuation timing of the large type MELSECNET/10 network modules have been corrected. • Descriptions on the production discontinuation of the AnS/Q2AS (small type) series have been corrected. • Descriptions on MELSECNET/10 network module have been added in the "Transition from A series to AnS series" table.

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other models

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