

[1/89]

FA-A-0285-A

Transition to Made-to-order Production and Production Discontinuation of the CC-Link Remote I/O Modules, Analog Modules, High-speed Counter Module, and Wiring Conversion Adapters

■Date of Issue

September 2019

■Relevant Models

AJ65BTB1-16D, AJ65BTB2-16D, AJ65BTB1-16T, AJ65BTB2-16T, AJ65BTB2-16R, AJ65BTB1-16DT, AJ65BTB2-16DT, AJ65BTB2-16DR, AJ65BT-64AD, AJ65BT-64DAV, AJ65BT-64DAI, AJ65BT-64RD3, AJ65BT-64RD4, AJ65BT-68TD, AJ65BT-D62D-S1, A6ADP-1MC16D, A6ADP-2MC16D, A6ADP-1MC16T

Thank you for your continued support of Mitsubishi Electric programmable controllers, MELSEC series. Production of some CC-Link remote I/O modules, analog modules, high-speed counter module, and wiring conversion adapters will be discontinued.

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1 MODELS TO BE DISCONTINUED

1.1 CC-Link Remote I/O Module

Model to be disco	ntinued	Alternative model		
Model	Specifications	Model	Specifications	
AJ65BTB1-16D	DC input (1-wire, positive/negative common shared type)	AJ65SBTB1-16D	DC input (1-wire, positive/negative common shared type)	
AJ65BTB2-16D	DC input (2-wire, positive/negative common shared type)	AJ65SBTB3-16D	DC input (3-wire, positive/negative common shared type)	
AJ65BTB1-16T	Transistor output (1-wire, sink type)	AJ65SBTB1-16T1	Transistor output (1-wire, sink type)	
AJ65BTB2-16T	Transistor output (2-wire, sink type)	AJ65SBTB2-16T1	Transistor output (2-wire, sink type)	
AJ65BTB2-16R	Relay output	AJ65SBTB2N-16R	Relay output	
AJ65BTB1-16DT	DC input (1-wire, positive common type) Transistor output (1-wire, sink type)	AJ65SBTB1-16DT2	DC input (1-wire, positive common type) Transistor output (1-wire, sink type)	
AJ65BTB2-16DT	DC input (2-wire, positive common type) Transistor output (2-wire, sink type)	AJ65SBTB32-16DT2	DC input (3-wire, positive common type) Transistor output (2-wire, sink type)	
AJ65BTB2-16DR	DC input (2-wire, positive/negative common shared type) Relay output	AJ65SBTB32-16DR	DC input (3-wire, positive/negative common shared type) Relay output	

1.2 CC-Link Analog Module

Model to be discontinued		Alternative model		
Model	Specifications	Model	Specifications	
AJ65BT-64AD	Voltage/current input	AJ65SBT2B-64AD	Voltage/current input	
		AJ65SBT-64AD	Voltage/current input	
AJ65BT-64DAV	Voltage output	AJ65SBT2B-64DA	Voltage/current output	
		AJ65SBT-62DA	Voltage/current output	
AJ65BT-64DAI	Current output	AJ65SBT2B-64DA	Voltage/current output	
		AJ65SBT-62DA	Voltage/current output	
AJ65BT-64RD3	3-wire type platinum temperature-measuring resistor (Pt100, JPt100) input	AJ65SBT2B-64RD3	3-wire type platinum temperature-measuring resistor (Pt100, JPt100, Ni100) input	
AJ65BT-64RD4	4-wire type platinum temperature-measuring resistor (Pt100, JPt100) input			
AJ65BT-68TD	Thermocouple (B, R, S, K, E, J, T) input	AJ65SBT2B-64TD	Thermocouple (B, R, S, K, E, J, T, N) input	

1.3 CC-Link High-speed Counter Module

Model to be discontinued		Alternative model	
Model Specifications		Model	Specifications
AJ65BT-D62D-S1	Differential input/sink output (preset: differential input)	AJ65BT-D62D	Differential input/sink output (preset: DC input)

1.4 MELSECNET/MINI-S3 of CC-Link Module Wiring Conversion Adapter

Model to be discontinued	Alternative model
Model	Model
A6ADP-1MC16D	None
A6ADP-2MC16D	
A6ADP-1MC16T	

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2 SCHEDULE

• Transition to made-to-order: March 31, 2020

• Order acceptance: Until August 10, 2021

• Production discontinuation: September 30, 2021

3 REASON FOR DISCONTINUATION

Main parts of the above product are now obsolete, and we will have difficulty to maintain our production system.

4 REPAIR SUPPORT

Repair support period: Until September 30, 2028 (for seven years after the discontinuation of production)

5 RECOMMENDABLE PROPOSALS

The delivery time for the made-to-order production is 3.5 months.

Please allow for this time and purchase the models to be discontinued early enough.

For details on the delivery time, please consult your local Mitsubishi representative.

6 SPECIFICATIONS COMPARISON BETWEEN THE DISCONTINUED AND ALTERNATIVE MODELS

6.1 Input Module

AJ65BTB1-16D

Model to be discontinued		Alternative model		
Model Specifications		Model	Specifications	
AJ65BTB1-16D	DC input (1-wire, positive/negative common shared type)	AJ65SBTB1-16D	DC input (1-wire, positive/negative common shared type)	

■Performance specifications comparison

 \bigcirc : Compatible, \triangle : Check required, \times : Not compatible

Item		Model to be discontinued	Alternative model	Compatibility	Precautions for
		AJ65BTB1-16D	AJ65SBTB1-16D		replacement
Number of input points		16 points		0	_
Isolation met	thod	Photocoupler		0	_
Rated input	voltage	24VDC (ripple ratio: within 5%)	24VDC	0	_
Rated input	current	Approx. 7mA		0	_
Operating voltage range		19.2 to 28.8VDC	19.2 to 26.4VDC (ripple ratio: within 5%)	Δ	Check the output voltage range and the voltage drop for wire length of power supply device to use.
Maximum nu simultaneous	umber of s input points	100%		0	_
ON voltage/0	ON current	14VDC or higher/3.5mA or higher		0	_
OFF voltage/OFF current		6VDC or lower/1.7mA or lower		0	_
Input resistance		Approx. 3.3kΩ		0	_
Response time	OFF → ON ON → OFF	10ms or less	1.5ms or less (at 24VDC) 1.5ms or less (at 24VDC)	Δ	The response time may be incorrect due to noise. Verify the time in the system.
Wiring metho	od for common	16 points/common (1-wire, terminal block type)	16 points/common (2 points) (1-wire, terminal block type)	0	_
Input type		Positive/negative common shared type (sink/source shared type)		0	_
Number of o	ccupied stations	1 station		0	_
Module power supply	Voltage	24VDC (ripple ratio: within 5%) (allowable voltage range: 15.6 to 28.8VDC)	20.4 to 26.4VDC (ripple ratio: within 5%)	Δ	Check the output voltage range and the voltage drop for wire length of power supply device to use.
	Current	60mA or lower (at TYP. 24VDC)	35mA or lower (at 24VDC, all points ON)	0	_
Noise immunity		Noise voltage 500Vp-p, noise width 1μs, noise frequency 25 to 60Hz (DC type noise simulator condition)		0	_
Withstand vo	oltage	500VAC for 1 minute between all DC external terminals and ground		0	_
Insulation re	sistance	$10 M\Omega$ or higher between all DC external terminals and ground (500VDC insulation resistance tester)		0	_

Item		Model to be discontinued	Alternative model	Compatibility	Precautions for
		AJ65BTB1-16D	AJ65SBTB1-16D		replacement
External interface	Communication part, module power supply part	27-point terminal block (M3.5 screw) Including transmission path and module power supply terminals	7-point two-piece terminal block M3×5.2 screw (tightening torque range: 0.59 to 0.88 N•m) Applicable solderless terminal: 2 or less	Х	The screw size is different.
	I/O part		18-point direct-mount terminal block M3×5.2 screw (tightening torque range: 0.59 to 0.88 N•m) Applicable solderless terminal: 2 or less	×	
Applicable solderless terminal Applicable wire size*1		RAV1.25-3.5 (compliant with JIS C 2805) RAV2-3.5 0.75 to 2mm (18 to 14 AWG) stranded wire	RAV1.25-3 (compliant with JIS C 2805) [Applicable wire size: 0.3 to 1.25mm² (22 to 16 AWG) stranded wire] V2-MS3 (JST Mfg. Co., Ltd.), RAP2-3SL (Nippon Tanshi Co., Ltd.), TGV2-3N (NICHIFU Co., Ltd.) [Applicable wire size: 1.25 to 2.0mm² (16 to 14 AWG) stranded wire]	×	The screw size is different. Change the solderless terminals.
Wire	Material	Copper		0	_
	Temperature rating	75°C or higher		0	_
External	Height (H)	65mm	50mm	0	_
dimensions	Width (W)	151.9mm	118mm	0	_
	Depth (D)	46mm	40mm	0	_
Installation hall		φ4.5 (2 places)	4.5×5.1 (2 places)	0	_
Installation	Height (H)	56mm	_	×	The installation hall
hall pitch	Width (W)	142.9mm	109mm	×	pitch is different.
Weight		0.32kg	0.18kg	0	_

^{*1} Use the wires suitable for the solderless terminals to use and install the terminals with suitable tightening torque. Use the UL certified solderless terminals and use the recommended tools to crimp the terminals.

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AJ65BTB2-16D

Model to be disconti	Model to be discontinued		Alternative model		
Model Specifications		Model	Specifications		
AJ65BTB2-16D	J65BTB2-16D DC input (2-wire, positive/negative common shared type) AJ65SBTB3-16D		DC input (3-wire, positive/negative common shared type)		

■Performance specifications comparison

 \bigcirc : Compatible, \triangle : Check required, \times : Not compatible

Item		Model to be discontinued	Alternative model	Compatibility	Precautions for
		AJ65BTB2-16D	AJ65SBTB3-16D		replacement
Number of input points		16 points		0	_
Isolation me	thod	Photocoupler		0	_
Rated input	voltage	24VDC (ripple ratio: within 5%)	24VDC	0	_
Rated input	current	Approx. 7mA		0	_
Operating vo	oltage range	19.2 to 28.8VDC	19.2 to 26.4VDC (ripple ratio: within 5%)	Δ	Check the output voltage range and the voltage drop fo wire length of power supply device to use.
Maximum nı simultaneou	umber of s input points	100%		0	_
ON voltage/	ON current	14VDC or higher/3.5mA or higher		0	_
OFF voltage	e/OFF current	6VDC or lower/1.7mA or lower		0	_
Input resista	ince	Approx. 3.3kΩ		0	_
Response	$OFF \to ON$	10ms or less	1.5ms or less (at 24VDC)	Δ	The response time
time	ON → OFF	10ms or less	1.5ms or less (at 24VDC)		may be incorrect due to noise. Verify the time in the system.
Wiring meth	od for common	16 points/common (2-wire, terminal block type)	16 points/common (3-wire, terminal block type)	Δ	The wiring method is different.
Input type		Positive/negative common shared type (sink/source shared type)		0	_
Supply curre device	ent for connected	_	1.0A or lower/common	Δ	Check the maximum current value per common
Number of c	occupied stations	1 station		0	_
Module power supply	Voltage	24VDC (ripple ratio: within 5%) (allowable voltage range: 15.6 to 28.8VDC)	20.4 to 26.4VDC (ripple ratio: within 5%)	Δ	Check the output voltage range and the voltage drop fo wire length of power supply device to use.
	Current	60mA or lower (at TYP. 24VDC)	45mA or lower (at 24VDC, all points ON)	0	_
Noise immu	nity	Noise voltage 500Vp-p, noise width 1μs, noise frequency 25 to 60Hz (DC type noise simulator condition)		0	_
Withstand v	oltage	500VAC for 1 minute between all DC external terminals and ground		0	_
Insulation resistance		10M Ω or higher between all DC external terminals and ground (500VDC insulation resistance tester)		0	_
External interface	Communication part, module power supply part	37-point terminal block (M3.5 screw) Including transmission path and module power supply terminals	7-point two-piece terminal block M3×5.2 screw (tightening torque range: 0.59 to 0.88 N•m) Applicable solderless terminal: 2 or less	×	The screw size is different.
	I/O part		34-point direct-mount terminal block M3×5.2 screw (tightening torque range: 0.59 to 0.88 N•m) Applicable solderless terminal: 2 or less	×	

Item		Model to be discontinued	Alternative model	Compatibility	Precautions for	
		AJ65BTB2-16D	AJ65SBTB3-16D		replacement	
Applicable solderless terminal Applicable wire size*1		RAV1.25-3.5 (compliant with JIS C 2805) RAV2-3.5 0.75 to 2mm² (18 to 14 AWG) stranded wire	RAV1.25-3 (compliant with JIS C 2805) [Applicable wire size: 0.3 to 1.25mm² (22 to 16 AWG) stranded wire] V2-MS3 (JST Mfg. Co., Ltd.), RAP2-3SL (Nippon Tanshi Co., Ltd.), TGV2-3N (NICHIFU Co., Ltd.) [Applicable wire size: 1.25 to 2.0mm² (16 to 14 AWG) stranded wire]	×	The screw size is different. Change the solderless terminals.	
Wire	Material	Copper		0	_	
	Temperature rating	75°C or higher		0	_	
External	Height (H)	65mm	50mm	0	_	
dimensions	Width (W)	197.4mm	179mm	0	_	
	Depth (D)	46mm	40mm	0	_	
Installation h	all	φ4.5 (2 places)	4.5×5.1 (2 places)	0	_	
Installation	Height (H)	56mm	_	×	The installation hall	
hall pitch	Width (W)	188.4mm	170mm	×	pitch is different.	
Weight		0.40kg	0.25kg	0	_	

^{*1} Use the wires suitable for the solderless terminals to use and install the terminals with suitable tightening torque. Use the UL certified solderless terminals and use the recommended tools to crimp the terminals.

6.2 Output Module

AJ65BTB1-16T

		Alternative model		
		Model	Specifications	
AJ65BTB1-16T	Transistor output (1-wire, sink type)	AJ65SBTB1-16T1	Transistor output (1-wire, sink type)	

■Performance specifications comparison

 \bigcirc : Compatible, \triangle : Check required, \times : Not compatible

Item		Model to be discontinued	Alternative model	Compatibility	Precautions for
		AJ65BTB1-16T	AJ65SBTB1-16T1		replacement
Number of output points		16 points	0	_	
Isolation metho	bc	Photocoupler		0	_
Rated load vol	tage	12/24VDC (ripple ratio: within 5%)	12/24VDC	0	_
Operating load	l voltage range	10.2 to 28.8VDC	10.2 to 26.4VDC (ripple ratio: within 5%)	Δ	Check the output voltage range and the voltage drop for wire length of power supply device to use.
Maximum load	current	0.5A/point 4A/common (Ta = 45°C) 2.8A/common (Ta = 55°C)	0.5A/point 3.6A/common	Δ	The maximum load current per common is different.
Maximum inrus	sh current	4A, 10ms or less	1A, 10ms or less	0	_
Leakage current at OFF		0.1mA or lower	0.25mA or lower	Δ	The leakage current at OFF is lower. Check that the current value does not affect the load.
Maximum voltage drop at ON		0.9VDC or lower (TYP.): 0.5A, 1.5VDC or lower (MAX.): 0.5A	0.3VDC or lower (TYP.): 0.5A, 0.6VDC or lower (MAX.): 0.5A	0	_
Output type		Sink type		0	_
Response	$OFF \to ON$	2ms or less	0.5ms or less	Δ	Check the output
time	$ON \rightarrow OFF$	2ms or less (resistive load)	1.5ms or less (resistive load)]	timing.
External power supply for output part	Voltage	10.2 to 28.8VDC (ripple ratio: within 5%)	10.2 to 26.4VDC (ripple ratio: within 5%)	Δ	Check the output voltage range and the voltage drop for wire length of power supply device to use.
	Current	100mA or lower/common (TYP. 24VDC) Not including external load current.	30mA or lower/common (TYP. 24VDC) Not including external load current.	0	_
Surge suppres	sor	Zener diode		0	_
Wiring method for common		8 points/common (1-wire, terminal block type)	16 points/common (1-wire, terminal block type)	Δ	The common type is different, so the common wiring is different.
Number of occupied stations		1 station		0	_
Module power supply	Voltage	24VDC (ripple ratio: within 5%) (allowable voltage range: 15.6 to 28.8VDC)	20.4 to 26.4VDC (ripple ratio: within 5%)	Δ	Check the output voltage range and the voltage drop for wire length of power supply device to use.
	Current	80mA or lower (at TYP. 24VDC)	50mA or lower (at 24VDC, all points ON)	0	_

Item		Model to be discontinued	Alternative model	Compatibility	Precautions for
		AJ65BTB1-16T	AJ65SBTB1-16T1		replacement
Noise immunit	ty	Noise voltage 500Vp-p, noise width 1μs, noise simulator condition)	noise frequency 25 to 60Hz (DC type	0	_
Withstand volt	age	500VAC for 1 minute between all DC ext	ernal terminals and ground	0	_
Insulation resi	stance	10M Ω or higher between all DC external resistance tester)	terminals and ground (500VDC insulation	0	_
External interface	Communication part, module power supply part	27-point terminal block (M3.5 screw) Including transmission path and module power supply terminals	7-point two-piece terminal block M3×5.2 screw (tightening torque range: 0.59 to 0.88 N•m) Applicable solderless terminal: 2 or less	×	The screw size is different.
	I/O part		18-point direct-mount terminal block M3×5.2 screw (tightening torque range: 0.59 to 0.88 N•m) Applicable solderless terminal: 2 or less	×	
Applicable solderless terminal Applicable wire size*1		RAV1.25-3.5 (compliant with JIS C 2805) RAV2-3.5 0.75 to 2mm² (18 to 14 AWG) stranded wire	RAV1.25-3 (compliant with JIS C 2805) [Applicable wire size: 0.3 to 1.25mm² (22 to 16 AWG) stranded wire] V2-MS3 (JST Mfg. Co., Ltd.), RAP2-3SL (Nippon Tanshi Co., Ltd.), TGV2-3N (NICHIFU Co., Ltd.) [Applicable wire size: 1.25 to 2.0mm² (16 to 14 AWG) stranded wire]	×	The screw size is different. Change the solderless terminals.
Wire	Material	Copper		0	_
	Temperature rating	75°C or higher		0	_
External	Height (H)	65mm	50mm	0	_
dimensions	Width (W)	151.9mm	118mm	0	_
	Depth (D)	46mm	40mm	0	_
Installation hall		φ4.5 (2 places)	4.5×5.1 (2 places)	0	_
Installation	Height (H)	56mm	_	×	The installation hall
hall pitch	Width (W)	142.9mm	109mm	×	pitch is different.
Weight	•	0.34kg	0.18kg	0	_

^{*1} Use the wires suitable for the solderless terminals to use and install the terminals with suitable tightening torque. Use the UL certified solderless terminals and use the recommended tools to crimp the terminals.

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AJ65BTB2-16T

Model to be discontinued		Alternative model	
Model Specifications		Model	Specifications
AJ65BTB2-16T	Transistor output (2-wire, sink type)	AJ65SBTB2-16T1	Transistor output (2-wire, sink type)

■Performance specifications comparison

 $\bigcirc :$ Compatible, $\triangle :$ Check required, $\times :$ Not compatible

Item		Model to be discontinued	Alternative model	Compatibility	Precautions for
		AJ65BTB2-16T	AJ65SBTB2-16T1		replacement
Number of output points		16 points		0	_
Isolation metho	od	Photocoupler		0	_
Rated load vol	tage	12/24VDC (ripple ratio: within 5%)	12/24VDC	0	_
Operating load	voltage range	10.2 to 28.8VDC	10.2 to 26.4VDC (ripple ratio: within 5%)	Δ	Check the output voltage range and the voltage drop for wire length of power supply device to use.
Maximum load	current	0.5A/point 4A/common	0.5A/point 3.6A/common	Δ	The maximum load current per common is lower.
Maximum inrus	sh current	4A, 10ms or less	1.0A, 10ms or less	0	_
Leakage curre	nt at OFF	0.1mA or lower		0	_
Maximum volta	age drop at ON	0.9VDC or lower (TYP.): 0.5A, 1.5VDC or lower (MAX.): 0.5A	0.3VDC or lower (TYP.): 0.5A, 0.6VDC or lower (MAX.): 0.5A	0	_
Output type		Sink type		0	_
Response	$OFF \to ON$	2ms or less	0.5ms or less	Δ	Check the output
time	$ON \rightarrow OFF$	2ms or less (resistive load)	1.5ms or less (resistive load)	1	timing.
External power supply for output part	Voltage	10.2 to 28.8VDC (ripple ratio: within 5%)	10.2 to 26.4VDC (ripple ratio: within 5%)	Δ	Check the output voltage range and the voltage drop for wire length of power supply device to use.
	Current	100mA or lower/common (TYP. 24VDC) Not including external load current.	24.2mA or lower/common (TYP. 24VDC) Not including external load current.	0	_
Surge suppres	sor	Zener diode		0	_
Wiring method	for common	8 points/common (2-wire, terminal block type)	16 points/common (2-wire, terminal block type)	Δ	The common type is different, so the common wiring is different.
Number of occ	upied stations	1 station		0	_
Module power supply	Voltage	24VDC (ripple ratio: within 5%) (allowable voltage range: 15.6 to 28.8VDC)	20.4 to 26.4VDC (ripple ratio: within 5%)	Δ	Check the output voltage range and the voltage drop for wire length of power supply device to use.
	Current	80mA or lower (at TYP. 24VDC)	55mA or lower (at 24VDC, all points ON)	0	_
Noise immunity		Noise voltage 500Vp-p, noise width 1μs, noise frequency 25 to 60Hz (DC type noise simulator condition)		0	_
Withstand volta	age	500VAC for 1 minute between all DC ex	ternal terminals and ground	0	_
Insulation resis	stance	10M Ω or higher between all DC external resistance tester)	terminals and ground (500VDC insulation	0	_

Item		Model to be discontinued	Alternative model	Compatibility	Precautions for
		AJ65BTB2-16T	AJ65SBTB2-16T1		replacement
External Communicati interface part, module power supply part		37-point terminal block (M3.5 screw) Including transmission path and module power supply terminals	7-point two-piece terminal block M3×5.2 screw (tightening torque range: 0.59 to 0.88 N•m) Applicable solderless terminal: 2 or less	×	The screw size is different.
	I/O part		34-point direct-mount terminal block M3×5.2 screw (tightening torque range: 0.59 to 0.88 N•m) Applicable solderless terminal: 2 or less	×	
Applicable solderless terminal Applicable wire size*1		RAV1.25-3.5 (compliant with JIS C 2805) RAV2-3.5 0.75 to 2mm (18 to 14 AWG) stranded wire	RAV1.25-3 (compliant with JIS C 2805) [Applicable wire size: 0.3 to 1.25mm² (22 to 16 AWG) stranded wire] V2-MS3 (JST Mfg. Co., Ltd.), RAP2-3SL (Nippon Tanshi Co., Ltd.), TGV2-3N (NICHIFU Co., Ltd.) [Applicable wire size: 1.25 to 2.0mm² (16 to 14 AWG) stranded wire]	×	The screw size is different. Change the solderless terminals.
Wire	Material	Copper		0	_
	Temperature rating	75°C or higher		0	_
External	Height (H)	65mm	50mm	0	_
dimensions	Width (W)	197.4mm	179mm	0	_
	Depth (D)	46mm	40mm	0	_
Installation hall		φ4.5 (2 places)	4.5×5.1 (2 places)	0	_
Installation	Height (H)	56mm	_	×	The installation hall
hall pitch	Width (W)	188.4mm	170mm	×	pitch is different.
Weight		0.41kg	0.25kg	0	_

^{*1} Use the wires suitable for the solderless terminals to use and install the terminals with suitable tightening torque. Use the UL certified solderless terminals and use the recommended tools to crimp the terminals.

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AJ65BTB2-16R

Model to be discontinued		Alternative model	
Model	Specifications	Model	Specifications
AJ65BTB2-16R	Relay output	AJ65SBTB2N-16R	Relay output

■Performance specifications comparison

 $\bigcirc :$ Compatible, $\triangle :$ Check required, $\times :$ Not compatible

Item		Model to be discontinued	Alternative model	Compatibility	Precautions for
		AJ65BTB2-16R	AJ65SBTB2N-16R		replacement
Number of output points		16 points		0	_
Isolation met	hod	Photocoupler	Relay	Δ	The isolation method is different
Rated load v	oltage/current	24VDC (resistive load), 240VAC (COSφ= 2A/point, 8A/common	= 1)	0	_
Minimum swi	itching load	5VDC, 1mA		0	_
Maximum sw	vitching voltage	250VAC, 110VDC	264VAC, 125VDC	0	_
Response	$OFF \rightarrow ON$	10ms or less		0	_
time	$ON \rightarrow OFF$	12ms or less		0	_
Life	Mechanical	20 million times or more		0	_
	Electrical	Rated switching voltage/current load: 10 200VAC/1.5A, 240VAC/1A (COSφ= 0.7): 200VAC/1A, 240VAC/0.5A (COSφ= 0.35 24VDC/1A, 100VDC/0.1A (L/R = 7ms): 1	100 thousand times or more): 100 thousand times or more	0	_
Maximum sw	vitching frequency	3600 times/hour		0	_
External power	Voltage	24VDC ±10%, ripple ratio: 4Vp-p or lower	None	0	_
supply for output part (I/O: 24V, 24G)	Current	90mA or lower (at TYP. 24VDC and all points ON)	None	0	_
Surge suppre	essor	None		0	_
Wiring metho	od for common	8 points/common (2-wire, terminal block type)	16 points/common (2-wire, terminal block type)	Δ	The common type is different, so the common wiring is different.
Number of o	ccupied stations	1 station		0	_
Module power supply	Voltage	24VDC (ripple ratio: within 5%) (allowable voltage range: 15.6 to 28.8VDC)	20.4 to 26.4VDC (ripple ratio: within 5%)	Δ	Check the output voltage range and the voltage drop fo wire length of power supply device to use.
	Current	85mA or lower (at TYP. 24VDC)	120mA or lower (at 24VDC, all points ON)	0	_
Noise immunity		Noise voltage 1500Vp-p for AC, noise voltage 500Vp-p for DC, noise width 1µs, noise frequency 25 to 60Hz (noise simulator condition)	Noise voltage 1500Vp-p for AC, noise voltage 500Vp-p for DC, noise width 1µs, noise frequency 25 to 60Hz (noise simulator condition) Fast transient/burst immunity test IEC 61000-4-4: 1kV	0	_
Withstand voltage		1500VAC for 1 minute between all AC external terminals and ground 500VAC for 1 minute between all DC external terminals and ground	2830VACrms for 3 cycles between all AC external terminals and ground (2000m above sea level) 500VAC for 1 minute between all DC external terminals and ground	0	_
Insulation resistance		resistance tester)	terminals and ground (500VDC insulation terminals and ground (500VDC insulation	0	_

Item		Model to be discontinued	Alternative model	Compatibility	Precautions for
		AJ65BTB2-16R	AJ65SBTB2N-16R		replacement
External interface	Communication part, module power supply part	37-point terminal block (M3.5 screw) Including transmission path and module power supply terminals	7-point two-piece terminal block M3×5.2 screw (tightening torque range: 0.59 to 0.88 N•m) Applicable solderless terminal: 2 or less	×	The screw size is different.
	I/O part		34-point direct-mount terminal block M3×5.2 screw (tightening torque range: 0.59 to 0.88 N•m) Applicable solderless terminal: 2 or less	×	
Applicable solderless terminal Applicable wire size*1		RAV1.25-3.5 (compliant with JIS C 2805) RAV2-3.5 0.75 to 2mm² (18 to 14 AWG) stranded wire	RAV1.25-3 (compliant with JIS C 2805) [Applicable wire size: 0.3 to 1.25mm² (22 to 16 AWG) stranded wire] V2-MS3 (JST Mfg. Co., Ltd.), RAP2-3SL (Nippon Tanshi Co., Ltd.), TGV2-3N (NICHIFU Co., Ltd.) [Applicable wire size: 1.25 to 2.0mm² (16 to 14 AWG) stranded wire]	×	The screw size is different. Change the solderless terminals.
Wire	Material	Copper		0	_
	Temperature rating	75°C or higher		0	_
External	Height (H)	65mm	50mm	0	_
dimensions	Width (W)	197.4mm	179mm	0	_
	Depth (D)	46mm	40mm	0	_
Installation hall		φ4.5 (2 places)	4.5×5.1 (2 places)	0	_
Installation	Height (H)	56mm	_	×	The installation hall
hall pitch	Width (W)	188.4mm	170mm	×	pitch is different.
Weight		0.47kg	0.35kg	0	_

^{*1} Use the wires suitable for the solderless terminals to use and install the terminals with suitable tightening torque. Use the UL certified solderless terminals and use the recommended tools to crimp the terminals.

6.3 I/O Combined Module

AJ65BTB1-16DT

Model to be discontinued		Alternative model		
Model Specifications		Model	Specifications	
AJ65BTB1-16DT	DC input (1-wire, positive common type) Transistor output (1-wire, sink type)		DC input (1-wire, positive common type) Transistor output (1-wire, sink type)	

■Performance specifications comparison

 $\bigcirc :$ Compatible, $\triangle :$ Check required, $\times :$ Not compatible

Item			Model to be discontinued	Alternative model	Compatibility	Precautions
			AJ65BTB1-16DT	AJ65SBTB1-16DT2	_	for replacement
Input	Number of inp	out points	8 points		0	_
part	Isolation meth	nod	Photocoupler		0	_
	Rated input ve	oltage	24VDC (ripple ratio: within 5%)	24VDC	0	-
	Rated input co	urrent	Approx. 7mA		0	_
	Operating voltage range		19.2 to 28.8VDC	19.2 to 26.4VDC (ripple ratio: within 5%)	Δ	Check the output voltage range and the voltage drop for wire length of power supply device to use.
	Maximum nur simultaneous		100%		0	_
	ON voltage/O	N current	14VDC or higher/3.5mA or higher		0	_
	OFF voltage/0	OFF current	6VDC or lower/1.7mA or lower		0	_
	Input resistan	се	Approx. 3.3 kΩ		0	_
	Response	$OFF \to ON$	10ms or less	1.5ms or less (at 24VDC)	Δ	The response
	time	ON → OFF	10ms or less	1.5ms or less (at 24VDC)		time may be incorrect due to noise. Verify the time in the system.
	Input type		Positive common (sink type)		0	_
	Wiring method for common		8 points/common (1-wire, terminal block type)	16 points/common (1-wire, terminal block type), common shared type	Δ	The common type is different, so the common wiring is different.

Item	Item		Model to be discontinued	Alternative model	Compatibility	Precautions
			AJ65BTB1-16DT	AJ65SBTB1-16DT2		for replacement
Output	Number of outp	out points	8 points		0	_
part	Isolation metho	od	Photocoupler		0	_
	Rated load volt	age	12/24VDC (ripple ratio: within 5%)	24VDC	Δ	12VDC is not available.
	Operating load voltage range		10.2 to 28.8VDC	19.2 to 26.4VDC (ripple ratio: within 5%)	Δ	Check the output voltage range and the voltage drop for wire length of power supply device to use.
	Maximum load current		0.5A/point 4A/common	0.5A/point 2.4A/common	Δ	The maximum load current per common is lower.
	Maximum inrus	sh current	4A, 10ms or less	1.0A, 10ms or less	0	_
	Leakage currer	nt at OFF	0.1mA or lower		0	_
	Maximum volta	ge drop at ON	0.9VDC or lower (TYP.): 0.5A, 1.5VDC or lower (MAX.): 0.5A	0.3VDC or lower (TYP.): 0.5A, 0.6VDC or lower (MAX.): 0.5A	0	_
	Output type		Sink type		0	_
	Response	$OFF \rightarrow ON$	2ms or less	0.5ms or less	Δ	Check the output
	time	$ON \rightarrow OFF$	2ms or less (resistive load)	1.5ms or less (resistive load)	Δ	timing.
	External power supply for output part	Voltage	12/24VDC (ripple ratio: within 5%) (allowable voltage range: 10.2 to 28.8VDC)	19.2 to 26.4VDC (ripple ratio: within 5%)	Δ	Check the output voltage range and the voltage drop for wire length of power supply device to use.
		Current	50mA or lower/common (TYP. 24VDC) Not including external load current.	17.8mA or lower (at 24VDC, all points ON) Not including external load current.	0	_
	Surge suppres	sor	Zener diode		0	_
	Wiring method for common		8 points/common (1-wire, terminal block type)	16 points/common (1-wire, terminal block type), common shared type	Δ	The common type is different, so the common wiring is different.

Item			Model to be discontinued	Alternative model	Compatibility	Precautions
			AJ65BTB1-16DT	AJ65SBTB1-16DT2		for replacement
Common	Number of occupied stations		1 station		0	
part	Module power supply	Voltage	24VDC (ripple ratio: within 5%) (allowable voltage range: 15.6 to 28.8VDC)	20.4 to 26.4VDC (ripple ratio: within 5%)	Δ	Check the output voltage range and the voltage drop for wire length of power supply device to use.
		Current	70mA or lower (at TYP. 24VDC)	50mA or lower (at 24VDC, all points ON)	0	_
	Noise immunity	′	Noise voltage 500Vp-p, noise width (DC type noise simulator condition)		0	_
	Withstand volta	ige	500VAC for 1 minute between all D	C external terminals and ground	0	_
	Insulation resis	tance	10MΩ or higher between all DC ext (500VDC insulation resistance testo		0	_
	External interface	Communication part, module power supply part	27-point terminal block (M3.5 screw) Including transmission path and module power supply terminals	7-point two-piece terminal block M3×5.2 screw (tightening torque range: 0.59 to 0.88 N•m) Applicable solderless terminal: 2 or less	×	The screw size is different.
		I/O part		18-point direct-mount terminal block M3×5.2 screw (tightening torque range: 0.59 to 0.88 N•m) Applicable solderless terminal: 2 or less	×	
	Applicable solderless terminal Applicable wire size*1		RAV1.25-3.5 (compliant with JIS C 2805) RAV2-3.5 0.75 to 2mm (18 to 14 AWG) stranded wire	RAV1.25-3 (compliant with JIS C 2805) [Applicable wire size: 0.3 to 1.25mm² (22 to 16 AWG) stranded wire] V2-MS3 (JST Mfg. Co., Ltd.), RAP2-3SL (Nippon Tanshi Co., Ltd.), TGV2-3N (NICHIFU Co., Ltd.) [Applicable wire size: 1.25 to 2.0mm² (16 to 14 AWG) stranded wire]	×	The screw size is different. Change the solderless terminals.
	Wire	Material	Copper		0	_
		Temperature rating	75°C or higher		0	_
	External	Height (H)	65mm	50mm	0	_
	dimensions	Width (W)	151.9mm	118mm	0	_
		Depth (D)	46mm	40mm	0	_
	Installation hall		φ4.5 (2 places)	4.5×5.1 (2 places)	0	_
	Installation hall pitch	Height (H)	56mm		×	The installation hall pitch is
	·	Width (W)	142.9mm	109mm	×	different.
	Weight		0.33kg	0.18kg	0	_

^{*1} Use the wires suitable for the solderless terminals to use and install the terminals with suitable tightening torque. Use the UL certified solderless terminals and use the recommended tools to crimp the terminals.

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AJ65BTB2-16DT

Model to be disconti	nued	Alternative model		
Model Specifications		Model	Specifications	
AJ65BTB2-16DT	DC input (2-wire, positive common type) Transistor output (2-wire, sink type)	AJ65SBTB32-16DT2	DC input (3-wire, positive common type) Transistor output (2-wire, sink type)	

■Performance specifications comparison

 $\bigcirc :$ Compatible, $\triangle :$ Check required, $\times :$ Not compatible

Item	ltem		Model to be discontinued	Alternative model	Compatibility	Precautions
			AJ65BTB2-16DT	AJ65SBTB32-16DT2	-	for replacement
Input	Number of inp	ut points	8 points		0	_
part	Isolation meth-	od	Photocoupler		0	_
	Rated input vo	ltage	24VDC (ripple ratio: within 5%)	24VDC	0	_
	Rated input cu	ırrent	Approx. 7mA		0	_
	Operating volt	age range	19.2 to 28.8VDC	19.2 to 26.4VDC (ripple ratio: within 5%)	Δ	Check the output voltage range and the voltage drop for wire length of power supply device to use.
	Maximum number of simultaneous input points		100%		0	_
	ON voltage/Of	N current	14VDC or higher/3.5mA or higher		0	_
	OFF voltage/C	FF current	6VDC or lower/1.7mA or lower		0	_
	Input resistant	ce	Approx. 3.3kΩ		0	_
	Response	$OFF \rightarrow ON$	10ms or less	1.5ms or less (at 24VDC)	Δ	The response
	time	$ON \to OFF$	10ms or less	1.5ms or less (at 24VDC)		time may be incorrect due to noise. Verify the time in the system.
	Input type		Positive common (sink type)		0	_
	Wiring method for common		8 points/common (2-wire, terminal block type)	16 points/common (common to input part and output part) (3-wire, terminal block type)	Δ	The common type is different, so the common wiring is different.
	Supply current for connected device		_	1.0A or lower/common	Δ	Check the current value per common.

Item	Item		Model to be discontinued	Alternative model	Compatibility	Precautions
			AJ65BTB2-16DT	AJ65SBTB32-16DT2		for replacement
Output	Output Number of output points		8 points		0	_
part	Isolation metho	od	Photocoupler		0	_
	Rated load volt	age	12/24VDC (ripple ratio: within 5%)	24VDC	Δ	12VDC is not available.
Operating load voltage range		voltage range	10.2 to 28.8VDC	19.2 to 26.4VDC (ripple ratio: within 5%)	Δ	Check the output voltage range and the voltage drop for wire length of power supply device to use.
	Maximum load current		0.5A/point 4A/common	0.5A/point 2.4A/common	Δ	The maximum load current per common is lower.
	Maximum inrus	h current	4A, 10ms or less	1.0A, 10ms or less	0	-
	Leakage currer	nt at OFF	0.1mA or lower		0	_
	Maximum volta	ge drop at ON	0.9VDC or lower (TYP.): 0.5A, 1.5VDC or lower (MAX.): 0.5A	0.3VDC or lower (TYP.): 0.5A, 0.6VDC or lower (MAX.): 0.5A	0	_
	Output type		Sink type		0	_
	Response	$OFF \to ON$	2ms or less	0.5ms or less	Δ	Check the output
	time	$ON \rightarrow OFF$	2ms or less (resistive load)	1.5ms or less (resistive load)	Δ	timing.
	External power supply for output part	Voltage	12/24VDC (ripple ratio: within 5%) (allowable voltage range: 10.2 to 28.8VDC)	19.2 to 26.4VDC (ripple ratio: within 5%)	Δ	Check the output voltage range and the voltage drop for wire length of power supply device to use.
		Current	50mA or lower/common (TYP. 24VDC) Not including external load current.	17.8mA or lower (at 24VDC, all points ON) Not including external load current.	0	_
	Surge suppress	sor	Zener diode		0	_
	Wiring method for common		8 points/common (2-wire, terminal block type)	16 points/common (common to input part and output part) (2-wire, terminal block type)	Δ	The common type is different, so the common wiring is different.

Item			Model to be discontinued	Alternative model	Compatibility	Precautions
			AJ65BTB2-16DT	AJ65SBTB32-16DT2		for replacement
Common	Number of occ	upied stations	1 station		0	_
part	Module power supply	Voltage	24VDC (ripple ratio: within 5%) (allowable voltage range: 15.6 to 28.8VDC)	20.4 to 26.4VDC (ripple ratio: within 5%)	Δ	Check the output voltage range and the voltage drop for wire length of power supply device to use.
		Current	70mA or lower (at TYP. 24VDC)	50mA or lower (at 24VDC, all points ON)	0	_
	Noise immunity	1	Noise voltage 500Vp-p, noise width (DC type noise simulator condition)		0	_
	Withstand volta	ige	500VAC for 1 minute between all D	C external terminals and ground	0	_
	Insulation resis	tance	10MΩ or higher between all DC ext (500VDC insulation resistance testo		0	_
	External interface	Communication part, module power supply part	37-point terminal block (M3.5 screw) Including transmission path and module power supply terminals	7-point two-piece terminal block M3×5.2 screw (tightening torque range: 0.59 to 0.88 N•m) Applicable solderless terminal: 2 or less	×	The screw size is different.
		I/O part		34-point direct-mount terminal block M3×5.2 screw (tightening torque range: 0.59 to 0.88 N•m) Applicable solderless terminal: 2 or less	×	
	Applicable solderless terminal Applicable wire size*1		RAV1.25-3.5 (compliant with JIS C 2805) RAV2-3.5 0.75 to 2mm (18 to 14 AWG) stranded wire	RAV1.25-3 (compliant with JIS C 2805) [Applicable wire size: 0.3 to 1.25mm² (22 to 16 AWG) stranded wire] V2-MS3 (JST Mfg. Co., Ltd.), RAP2-3SL (Nippon Tanshi Co., Ltd.), TGV2-3N (NICHIFU Co., Ltd.) [Applicable wire size: 1.25 to 2.0mm² (16 to 14 AWG) stranded wire]	×	The screw size is different. Change the solderless terminals.
	Wire	Material	Copper		0	_
		Temperature rating	75°C or higher		0	_
	External	Height (H)	65mm	50mm	0	_
	dimensions	Width (W)	197.4mm	179mm	0	_
		Depth (D)	46mm	40mm	0	_
	Installation hall		φ4.5 (2 places)	4.5×5.1 (2 places)	0	_
	Installation	Height (H)	56mm	_	×	The installation
	hall pitch	Width (W)	188.4mm	170mm	×	hall pitch is different.
	Weight		0.41kg	0.25kg	0	_

^{*1} Use the wires suitable for the solderless terminals to use and install the terminals with suitable tightening torque. Use the UL certified solderless terminals and use the recommended tools to crimp the terminals.

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AJ65BTB2-16DR

Model to be discontinued		Alternative model	
Model	Specifications	Model	Specifications
AJ65BTB2-16DR	DC input (2-wire, positive/negative common shared type) Relay output	AJ65SBTB32-16DR	DC input (3-wire, positive/negative common shared type) Relay output

■Performance specifications comparison

 \bigcirc : Compatible, \triangle : Check required, \times : Not compatible

Item	Item		Model to be discontinued	Alternative model	Compatibility	Precautions
			AJ65BTB2-16DR	AJ65SBTB32-16DR	-	for replacement
Input	Number of inpu	ut points	8 points		0	_
part	Isolation metho	od	Photocoupler		0	_
	Rated input vo	Itage	24VDC (ripple ratio: within 5%)	24VDC	0	_
	Rated input cu	rrent	Approx. 7mA		0	_
	Operating volta	age range	19.2 to 28.8VDC	19.2 to 26.4VDC (ripple ratio: within 5%)	Δ	Check the output voltage range and the voltage drop for wire length of power supply device to use.
	Maximum number of simultaneous input points		100%		0	_
	ON voltage/ON	l current	14VDC or higher/3.5mA or higher 6VDC or lower/1.7mA or lower Approx. 3.3kΩ		0	_
	OFF voltage/O	FF current			0	_
	Input resistance	е			0	_
	Response	$OFF \to ON$	10ms or less	1.5ms or less (at 24VDC)	Δ	The response
	time	ON → OFF 10ms or less	10ms or less	1.5ms or less (at 24VDC)		time may be incorrect due to noise. Verify the time in the system.
	Input type		Positive/negative common shared	type (sink/source shared type)	0	_
	Wiring method for common		8 points/common (2-wire, terminal block type)	8 points/common (3-wire, terminal block type)	Δ	The wiring method is different.
	Supply current for connected device		_	1.0A or lower/common	Δ	Check the current value per common.

Item			Model to be discontinued	Alternative model	Compatibility	Precautions
			AJ65BTB2-16DR	AJ65BTB2-16DR AJ65SBTB32-16DR		for replacement
Output	Number of out	tput points	8 points		0	_
part	Isolation method	od	Photocoupler	Relay	Δ	The isolation method is different.
	Rated load vol	ltage/current	24VDC (resistive load), 240VAC (COSφ= 1) 2A/point 8A/common	24VDC (resistive load), 240VAC (COSφ= 1) 2A/point 4A/common	Δ	The maximum load current per common is lower.
	Minimum switch	ching load	5VDC, 1mA		0	_
	Maximum swit	tching voltage	250VAC, 110VDC	264VAC, 125VDC	0	_
	Response	$OFF \rightarrow ON$	10ms or less		0	_
	time	$ON \rightarrow OFF$	12ms or less		0	_
	Life	Mechanical	20 million times or more	20 million times or more		_
		Electrical	Rated switching voltage/current load: 100 thousand times or more 200VAC/1.5A, 240VAC/1A (COSφ= 0.7): 100 thousand times or more 200VAC/1A, 240VAC/0.5A (COSφ= 0.35): 100 thousand times or more 24VDC/1A, 100VDC/0.1A (L/R = 7ms): 100 thousand times or more		0	_
	Maximum swit	tching frequency	3600 times/hour		0	_
	Surge suppressor		None		0	_
	Wiring method for common		8 points/common (2-wire, terminal block type)	4 points/common (2-wire, terminal block type)	Δ	The number of commons is different, so the common wiring is different.

Item			Model to be discontinued	Alternative model	Compatibility	Precautions
			AJ65BTB2-16DR	AJ65SBTB32-16DR		for replacement
Common	Number of occ	upied stations	1 station		0	_
part	Module power supply	Voltage	24VDC (ripple ratio: within 5%) (allowable voltage range: 15.6 to 28.8VDC)	20.4 to 26.4VDC (ripple ratio: within 5%)	Δ	Check the output voltage range and the voltage drop
		Current	70mA or lower (at TYP. 24VDC)	85mA or lower (at 24VDC, all points ON)	Δ	for wire length of power supply device to use.
	Noise immunity	,	Noise voltage 1500Vp-p for AC, nowidth $1\mu s$, noise frequency 25 to 60		0	_
	Withstand volta	ige	1500VAC for 1 minute between all AC external terminals and ground 500VAC for 1 minute between all DC external terminals and ground	2830VACrms for 3 cycles between all AC external terminals and ground (2000m above sea level) 500VAC for 1 minute between all DC external terminals and ground	0	_
	Insulation resis	tance	$10 \text{M}\Omega$ or higher between all AC ext (500VDC insulation resistance test $10 \text{M}\Omega$ or higher between all DC ext (500VDC insulation resistance testor)	er) ternal terminals and ground	0	_
	External interface	Communication part, module power supply part	37-point terminal block (M3.5 screw) Including transmission path and module power supply terminals	7-point two-piece terminal block M3×5.2 screw (tightening torque range: 0.59 to 0.88 N•m) Applicable solderless terminal: 2 or less	×	The screw size is different.
		I/O part		34-point direct-mount terminal block M3×5.2 screw (tightening torque range: 0.59 to 0.88 N•m) Applicable solderless terminal: 2 or less	×	
	Applicable solderless terminal Applicable wire size*1		RAV1.25-3.5 (compliant with JIS C 2805) RAV2-3.5 0.75 to 2mm (18 to 14 AWG) stranded wire	RAV1.25-3 (compliant with JIS C 2805) [Applicable wire size: 0.3 to 1.25mm² (22 to 16 AWG) stranded wire] V2-MS3 (JST Mfg. Co., Ltd.), RAP2-3SL (Nippon Tanshi Co., Ltd.), TGV2-3N (NICHIFU Co., Ltd.) [Applicable wire size: 1.25 to 2.0mm² (16 to 14 AWG) stranded wire]	×	The screw size is different. Change the solderless terminals.
	Wire	Material	Copper		0	_
		Temperature rating	75°C or higher		0	_
	External	Height (H)	65mm	50mm	0	_
	dimensions	Width (W)	197.4mm	179mm	0	_
		Depth (D)	46mm	40mm	0	_
	Installation hall		φ4.5 (2 places)	4.5×5.1 (2 places)	0	_
	Installation hall pitch	Height (H) Width (W)	56mm 188.4mm		×	The installation hall pitch is
						different.
	Weight		0.43kg	0.28kg	0	_

^{*1} Use the wires suitable for the solderless terminals to use and install the terminals with suitable tightening torque. Use the UL certified solderless terminals and use the recommended tools to crimp the terminals.

6.4 Analog Module

Voltage/current input (AJ65BT-64AD)

Replacement from AJ65BT-64AD to AJ65SBT2B-64AD

Model to be discontinued		Alternative model	
Model	Specifications	Model	Specifications
AJ65BT-64AD	Voltage/current input	AJ65SBT2B-64AD	Voltage/current input

■Performance specifications comparison

 \bigcirc : Compatible, \triangle : Check required, \times : Not compatible

Analog input Voltage ±15V 200µs/channel 100 200µs/channel 200µs	Item		Model to be discontinued	Alternative model	Compatibility	Precautions for
Current -20 to 20mADC (input resistance: 250Ω) Current -20 to 20mADC (input resistance: 250Ω) Current -20 to 20mADC (input resistance: 250Ω) Current -20 to 4095 -16384 to 16383 Current -20 to 4095 -16384 to 16383 Current -20 to 4095 Current -20 to 50°C Current			AJ65BT-64AD	AJ65SBT2B-64AD		replacement
Digital output Digital Digit	Analog input	Voltage	-10 to 10VDC (input resistance: $1M\Omega$)		0	_
bits) -2048 to 2047, 0 to 4095 -2048 to 2047, 0 to 4095 1/O characteristics, maximum resolution I/O characteristics, maximum resolution 1		Current	` .	0 to 20mADC (input resistance: 250 Ω)	Δ	Only the positive current is available
Conversion Ambient accuracy Lemperature: 0 to 55°C Lemperature	Digital output		bits)	,	Δ	The range of digita output value is wider.
accuracy temperature: 0 to 55°C Conversion speed		cs, maximum	*1	*2	Δ	The maximum resolution is smaller, resulting in finer control.
Absolute maximum input voltage ±15V		temperature: 0	±1% (±40 digits)	±0.2% (±32 digits)	0	_
Maximum input Eurrent ±30mA Current − Curr	Conversion speed		1ms/channel	200μs/channel	Δ	The conversion accuracy may be degraded due to noise. Verify the accuracy in the system.
Number of analog input channels 4 channels CC-Link station type Remote device station Number of occupied stations 2 stations 1 station Connection cable CC-Link dedicated cable Withstand voltage Between all power supply and communication system terminals and all analog input terminals 500VAC for 1 minute Between all power supply and communication system terminals and all analog input terminals and all analog input terminals: Photocoupler Between input channels: Non-isolation Roise immunity Noise voltage 500Vp-p, noise width 1µs, noise frequency 25 to 60Hz (noise	Absolute	Voltage	±15V		0	_
CC-Link station type Remote device station ○	maximum input	Current	±30mA		0	_
Number of occupied stations 2 stations 1 station Connection cable CC-Link dedicated cable Withstand voltage Between all power supply and communication system terminals and all analog input terminals 500VAC for 1 minute Between all power supply and communication system terminals and all analog input terminals and all analog input terminals and all analog input terminals: Photocoupler Between input channels: Non-isolation Noise immunity Noise voltage 500Vp-p, noise width 1µs, noise frequency 25 to 60Hz (noise simulator condition)	Number of analog	g input channels	4 channels	0	_	
Connection cable CC-Link dedicated cable Withstand voltage Between all power supply and communication system terminals and all analog input terminals 500VAC for 1 minute Between all power supply and communication system terminals and all analog input terminals and all analog input terminals and all analog input terminals: Photocoupler Between power supply system terminal and all analog input terminals: Transformer isolation Between input channels: Non-isolation Noise immunity Noise voltage 500Vp-p, noise width 1µs, noise frequency 25 to 60Hz (noise immulator condition)	CC-Link station t	type	Remote device station		0	_
Withstand voltage Between all power supply and communication system terminals and all analog input terminals 500VAC for 1 minute Between all power supply and communication system terminals and all analog input terminals and all analog input terminals: Photocoupler Between input channels: Non-isolation Photocoupler Between input channels: Non-isolation Between input channels: Non-isolation Noise immunity Between communication system terminals and all analog input terminals and all analog input terminals: Photocoupler Between power supply system terminal and all analog input terminals: Transformer isolation Between input channels: Non-isolation Noise voltage 500Vp-p, noise width 1µs, noise frequency 25 to 60Hz (noise immulator condition)	Number of occup	oied stations	2 stations	1 station	0	_
input terminals 500VAC for 1 minute Between all power supply and communication system terminals and all analog input terminals: Photocoupler Between input channels: Non-isolation Between input channels: Non-isolation Noise immunity Between communication system terminals and all analog input terminals and all analog input terminals: Terminals: Photocoupler Between power supply system terminal and all analog input terminals: Transformer isolation Between input channels: Non-isolation Noise voltage 500Vp-p, noise width 1µs, noise frequency 25 to 60Hz (noise immulator condition)	Connection cable	е	CC-Link dedicated cable		0	_
communication system terminals and all analog input terminals: Photocoupler Between input channels: Non-isolation Noise immunity Communication system terminals and all analog input terminals: Photocoupler Between power supply system terminal and all analog input terminals: Transformer isolation Between input channels: Non-isolation Noise voltage 500Vp-p, noise width 1µs, noise frequency 25 to 60Hz (noise Communication system terminals and all analog input terminals: Transformer isolation Between input channels: Non-isolation Noise immunity Omits immunity Noise voltage 500Vp-p, noise width 1µs, noise frequency 25 to 60Hz (noise	Withstand voltag	е	input terminals	cation system terminals and all analog	0	_
simulator condition)	Isolation method		communication system terminals and all analog input terminals: Photocoupler	terminals and all analog input terminals: Photocoupler Between power supply system terminal and all analog input terminals: Transformer isolation	0	_
	Noise immunity		Noise voltage 500Vp-p, noise width 1µs, noise frequency 25 to 60Hz (noise		0	_
Built-in terminating resistor None Disable/Enable (110Ω): Switchable \bigcirc —	Built-in terminati	ng resistor	None	Disable/Enable (110Ω): Switchable	0	_
Number of offset/gain settings — 10 thousand times maximum O —	Number of offset	/gain settings	_	10 thousand times maximum	0	_

Item		Model to be discontinued	Alternative model	Compatibility	Precautions for	
		AJ65BT-64AD	AJ65SBT2B-64AD		replacement	
External power supply	Voltage	24VDC (18 to 30VDC)	24VDC (20.4 to 28.8VDC)	Δ	Check the output voltage range and the voltage drop for wire length of power supply device to use.	
	Inrush current	_	Inrush current: 1.6A, 4.0ms or less	0	_	
	Current consumption	0.12A (at 24VDC)		0	_	
External Communication part, module power supply part		27-point two-piece terminal block	7-point two-piece terminal block M3×5.2 screw (tightening torque range: 0.59 to 0.88 N•m) Applicable solderless terminal: 2 or less	×	The screw size is different.	
	I/O part		18-point two-piece terminal block M3×5.2 screw (tightening torque range: 0.59 to 0.88 N•m) Applicable solderless terminal: 2 or less	×		
Applicable wire s	size	0.75 to 2.0mm (18 to 14 AWG)	0.3 to 2.0mm (22 to 14 AWG)	0	_	
Applicable solderless terminal		RAV1.25-3.5, RAV2-3.5	RAV1.25-3 (compliant with JIS C 2805) [Applicable wire size: 0.3 to 1.25mm² (22 to 16 AWG)] V2-MS3 (JST Mfg. Co., Ltd.), RAP2-3SL (Nippon Tanshi Co., Ltd.), TGV2-3N (NICHIFU Co., Ltd.) [Applicable wire size: 1.25 to 2.0mm² (16 to 14 AWG)]	×	The screw size is different. Change the solderless terminals.	
External	Height (H)	65mm	50mm	0	_	
dimensions	Width (W)	151.9mm	122mm	0	_	
	Depth (D)	63mm	54mm	0	_	
Installation hall	•	φ4.5 (2 places)	4.5×5.1 (2 places)	0	_	
Installation hall	Height (H)	56mm	0mm	×	The installation hall	
pitch	Width (W)	142.9mm	109.5mm	×	pitch is different.	
Weight		0.35kg	0.25kg	0	_	

^{*1} The following table lists the I/O characteristics and maximum resolution of AJ65BT-64AD.

Analog inpu	t range	Digital output value	Maximum resolution
Voltage	-10 to 10V	0 to 4000	5mV
_	0 to 10V	or -2000 to 2000	2.5mV
	0 to 5V	-2000 to 2000	1.25mV
	1 to 5V		1mV
Current	-20 to 20mA	0 to 4000	20μΑ
	0 to 20mA	or -2000 to 2000	10μΑ
	4 to 20mA	-2000 to 2000	4μΑ

 $^{^{*}2}$ The following table lists the I/O characteristics and maximum resolution of AJ65SBT2B-64AD.

Analog input range		Digital output value	Maximum resolution
Voltage	-10 to 10V	-16000 to 16000	0.625mV
User range setting 1 (-10 to 10V)			0.5mV
	User range setting 2 (-5 to 5V)		0.25mV
	0 to 5V	0 to 16000	0.3125mV
	1 to 5V		0.25mV

Analog input range		Digital output value	Maximum resolution
Current	0 to 20mA	0 to 16000	1.25μΑ
	4 to 20mA		1μΑ
	User range setting 2	-16000 to 16000	1μΑ

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■Functional comparison

○: Available, ×: Not available

Item	Description	Model to be discontinued	Alternative model	Precautions for replacement	
		AJ65BT-64AD	AJ65SBT2B-64AD		
A/D conversion enable/disable setting	The A/D conversion can be enabled or disabled for each channel using this function. By setting A/D conversion disabled for unnecessary channels, the sampling period shortens.	0	0	The remote register settings of AJ65BT-64AD is changed to the remote I/O signal settings of AJ65SBT2B-64AD.	
Sampling processing/ averaging processing specification	The sampling processing or average processing can be specified using this function.	Count average: 1 to 10000 times Time average: 4 to 10000ms Moving average: Not available	Count average: 4 to 255 times Time average: 0 to 25.5ms Moving average: 4, 8, 16, 32, 64, 128 times	The accuracy may be degraded due to noise. Verify the speed in the system.	
Input range setting function	The analog input range can be specified using this function.	(All channels)	(Each channel)	The pin settings and remote I/O signal settings of AJ65BT-64AD are changed to the remote I/O signal settings of AJ65SBT2B-64AD.	
Offset/gain settings	Offset/gain values can be set using this function	0	0	_	
Transmission speed auto-tracking function	When the module is powered on, the transmission speed is set automatically depending on the setting in the master module.	×	0	_	
Data format setting	The display format of digital output value for each channel can be set using this function.	0	×	The equivalent function is available by operation in the sequence program.	
Error code	When the write data error occurs, the error code is stored.	0	×	No error codes. Check the error cause using the ON/OFF state of remote input signal. Page 27 Comparison of remote I/O signals	
Error flag	The flag is set when an error other than the watchdog timer error occurs.	0	0	_	

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■Comparison of remote I/O signals

The remote I/O signals are different. Change the sequence program.

For details, refer to the user's manuals.

n: Address assigned to the master station by station number setting

Signal direct	ion: Remote module –	Master module	Signal direction: Master module $ ightarrow$ Remote module			
Remote	Signal name		Remote	Signal name		
input (RX)	Model to be discontinued	Alternative model	output (RY)	Model to be discontinued	Alternative model	
	AJ65BT-64AD	J65BT-64AD AJ65SBT2B-64AD		AJ65BT-64AD	AJ65SBT2B-64AD	
RXn0	CH1 A/D conversion cor	mpletion flag	RYn0	Offset/gain value selection	CH1 A/D conversion enable/disable setting	
RXn1	CH2 A/D conversion cor	mpletion flag	RYn1	Voltage/current selection	CH2 A/D conversion enable/disable setting	
RXn2	CH3 A/D conversion cor	mpletion flag	RYn2	Use prohibited	CH3 A/D conversion enable/disable setting	
RXn3	CH4 A/D conversion cor	mpletion flag	RYn3		CH4 A/D conversion enable/disable setting	
RXn4	Use prohibited	Use prohibited	RYn4		CH1 Input range setting (0th bit)	
RXn5			RYn5		CH1 Input range setting (1 bit)	
RXn6			RYn6		CH1 Input range setting (2nd bit)	
RXn7			RYn7		CH2 Input range setting (0th bit)	
RXn8			RYn8		CH2 Input range setting (*bit)	
RXn9			RYn9		CH2 Input range setting (2nd bit)	
RXnA		Hardware error flag	RYnA		CH3 Input range setting (0th bit)	
RXnB		User range read error flag	RYnB		CH3 Input range setting (1 bit)	
RXnC		Flash memory write error flag	RYnC		CH3 Input range setting (2nd bit)	
RXnD		Number of offset/gain settings excess flag	RYnD		CH4 Input range setting (0th bit)	
RXnE		Use prohibited	RYnE		CH4 Input range setting (1 bit)	
RXnF		Test mode flag	RYnF		CH4 Input range setting (2nd bit)	
RX(n+1)0		Use prohibited	RY(n+1)0		Use prohibited	
RX(n+1)1			RY(n+1)1			
RX(n+1)2			RY(n+1)2			
RX(n+1)3			RY(n+1)3			
RX(n+1)4			RY(n+1)4			
RX(n+1)5			RY(n+1)5			
RX(n+1)6			RY(n+1)6			
RX(n+1)7			RY(n+1)7			
RX(n+1)8	Initial data processing re	equest flag	RY(n+1)8	Initial data processing comp	letion flag	
RX(n+1)9	Initial data setting comp	letion flag	RY(n+1)9	Initial data setting request fla	ag	
RX(n+1)A	Error flag		RY(n+1)A	Error reset request flag		

Signal direct	Signal direction: Remote module → Master module			Signal direction: Master module → Remote module		
Remote	Signal name	Signal name		Signal name		
input (RX)	Model to be discontinued	Alternative model	output (RY)	Model to be discontinued	Alternative model	
	AJ65BT-64AD	AJ65SBT2B-64AD		AJ65BT-64AD	AJ65SBT2B-64AD	
RX(n+1)B	Remote READY	Remote READY		Use prohibited Use	Use prohibited	
RX(n+1)C : RX(n+1)F	Use prohibited	Use prohibited	RY(n+1)C : RY(n+1)F			
RX(n+2)0 : RX(n+3)F	Use prohibited	Unusable (not assigned because the number of occupied stations is 1)	RY(n+2)0 : RY(n+3)F	Use prohibited	Unusable (not assigned because the number of occupied stations is 1)	

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■Comparison of remote register

The assignment of remote resisters is changed. Change the sequence program.

For details, refer to the user's manuals.

m, n: Address assigned to the master station by station number setting

Direction	Address	Model to be discontinued		Alternative model			
		AJ65BT-64AD		AJ65SBT2B-64AD	AJ65SBT2B-64AD		
		Description	Default	Description	Default		
Master → Remote	RWwm	Average processing specification	0	CH1 Average processing setting	0		
	RWwm+1	CH1 average time, number of times	0	CH2 Average processing setting	0		
	RWwm+2	CH2 average time, number of times	0	CH3 Average processing setting	0		
	RWwm+3	CH3 average time, number of times	0	CH4 Average processing 0 setting			
	RWwm+4	CH4 average time, number of times	0	Unusable (not assigned beca occupied stations is 1)	Unusable (not assigned because the number of occupied stations is 1)		
	RWwm+5	Data format	0		1		
	RWwm+6	A/D conversion enable/prohibit specification	0				
	RWwm+7	Use prohibited	_				
Remote → Master	RWrn	CH1 digital output value	0	CH1 Digital output value	0		
	RWrn+1	CH2 digital output value	0	CH2 Digital output value	0		
	RWrn+2	CH3 digital output value	0	CH3 Digital output value	0		
	RWrn+3	CH4 digital output value	0	CH4 Digital output value	0		
	RWrn+4	Error code	0	Unusable (not assigned beca	use the number of		
	RWrn+5	Use prohibited	_	occupied stations is 1)			
	RWrn+6						
	RWrn+7						

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Replacement from AJ65BT-64AD to AJ65SBT-64AD

Model to be discontinued		Alternative model	
Model	Specifications	Model	Specifications
AJ65BT-64AD	Voltage/current input	AJ65SBT-64AD	Voltage/current input

■Performance specifications comparison

 $\bigcirc :$ Compatible, $\triangle :$ Check required, $\times :$ Not compatible

Item		Model to be discontinued	Alternative model	Compatibility	Precautions for
		AJ65BT-64AD	AJ65SBT-64AD		replacement
Analog input	Voltage	-10 to 10VDC (input resistance: $1M\Omega$)		0	_
	Current	-20 to 20mADC (input resistance: 250Ω)	0 to 20mADC (input resistance: 250Ω)	Δ	Only the positive current is available.
Digital output		16-bit signed binary data (data: 12 bits) -2048 to 2047, 0 to 4095	16-bit signed binary data -4096 to 4095	Δ	The range of digital output value is wider.
I/O characteris resolution	tics, maximum	*1	*2	Δ	The maximum resolution is smaller, resulting in finer control.
Conversion accuracy	Ambient temperature: 25±5°C	±1% (±40 digits)	±0.2% (±8 digits)	0	_
	Ambient temperature: 0 to 55°C		±0.4% (±16 digits)		
Conversion sp	eed	1ms/channel	0	_	
Absolute	Voltage	±15V	0	_	
maximum Current input		±30mA		0	_
Number of ana channels	alog input	4 channels	0	_	
CC-Link station	n type	Remote device station		0	_
Number of occ	upied stations	2 stations	1 station	0	_
Connection ca	ble	CC-Link dedicated cable		0	_
Withstand volta	age	Between all power supply and commun input terminals 500VAC for 1 minute	0	_	
Isolation method		Between all power supply and communication system terminals and all analog input terminals: Photocoupler Between input channels: Non-isolation	Between communication system terminals and all analog input terminals: Photocoupler Between power supply system terminals and all analog input terminals: Photocoupler Between input channels: Non-isolation	0	_
Noise immunity		Noise voltage 500Vp-p, noise width 1µs, noise frequency 25 to 60Hz (noise simulator condition)		0	_
External power supply	Voltage	24VDC (18 to 30VDC)	24VDC (20.4 to 26.4VDC)	Δ	Check the output voltage range and the voltage drop for wire length of power supply device to use.
	Inrush current	_	Inrush current: 8.5A, 2.3ms or less	0	_
	Current consumption	0.12A (at 24VDC)	0.09A (at 24VDC)	0	_

Item		Model to be discontinued	Alternative model	Compatibility	Precautions for
	AJ65BT-64AD AJ65SBT-64AD		AJ65SBT-64AD		replacement
External interface	Communication part, module power supply part	27-point two-piece terminal block	7-point two-piece terminal block M3×5.2 screw (tightening torque range: 0.59 to 0.88 N•m) Applicable solderless terminal: 2 or less	×	The screw size is different.
	I/O part		18-point direct-mount terminal block M3×5.2 screw (tightening torque range: 0.59 to 0.88 N•m) Applicable solderless terminal: 2 or less	×	
Applicable wir	e size	0.75 to 2.0mm (18 to 14 AWG)	0.3 to 0.75mm (22 to 18 AWG)	×	The screw size is
Applicable solderless terminal		RAV1.25-3.5, RAV2-3.5	RAV1.25-3 (compliant with JIS C 2805) [Applicable wire size: 0.3 to 1.25mm² (22 to 16 AWG)] V2-MS3 (JST Mfg. Co., Ltd.), RAP2-3SL (Nippon Tanshi Co., Ltd.), TGV2-3N (NICHIFU Co., Ltd.) [Applicable wire size: 1.25 to 2.0mm² (16 to 14 AWG)]	×	different. Change the solderless terminals.
External	Height (H)	65mm	50mm	0	_
dimensions	Width (W)	151.9mm	118mm	0	_
Depth (D)		63mm	40mm	0	_
Installation hall		φ4.5 (2 places)	4.5×5.1 (2 places)	0	_
Installation	Height (H)	56mm	0mm	×	The installation hall
hall pitch	Width (W)	142.9mm	109.0mm	×	pitch is different.
Weight		0.35kg	0.20kg	0	_

^{*1} The following table lists the I/O characteristics and maximum resolution of AJ65BT-64AD.

Analog input	t range	Digital output value	Maximum resolution
Voltage	-10 to 10V	0 to 4000	5mV
	0 to 10V	or -2000 to 2000	2.5mV
	0 to 5V	1.25mV	
	1 to 5V		1mV
Current	-20 to 20mA	0 to 4000	20μΑ
	0 to 20mA	or -2000 to 2000	10μΑ
	4 to 20mA	-2000 to 2000	4μΑ

^{*2} The following table lists the I/O characteristics and maximum resolution of AJ65SBT-64AD.

Analog inpu	t range	Digital output value	Maximum resolution
Voltage	-10 to 10V	-4000 to 4000	2.5mV
	User range setting 1 (-10 to 10V)		
	0 to 5V	0 to 4000	1.25mV
	1 to 5V		1mV
	User range setting 2 (0 to 5V)		
Current	0 to 20mA	0 to 4000	5μΑ
	4 to 20mA		4μΑ
	User range setting 3		

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■Functional comparison

○: Available, ×: Not available

Item	Description	Model to be discontinued	Alternative model	Precautions for replacement	
		AJ65BT-64AD	AJ65SBT-64AD		
A/D conversion enable/ disable setting	The A/D conversion can be enabled or disabled for each channel using this function. By setting A/D conversion disabled for unnecessary channels, the sampling period shortens.	0	0	_	
Sampling processing/ averaging processing specification	The sampling processing or average processing can be specified using this function.	Count average: 1 to 10000 times Time average: 4 to 10000ms Moving average: Not available	Count average: Not available Time average: Not available Moving average: 4, 8, 16, 32 times	The accuracy may be degraded due to noise. Verify the speed in the system.	
Input range setting function	The analog input range can be specified using this function.	(All channels)	(Each channel)	The pin settings and remote I/O signal settings AJ65BT-64AD is changed to the remote register settings of AJ65SBT-64AD.	
Offset/gain settings	The offset/gain setting can be set for each channel using this function.	0	0	_	
Data format setting	The display format of digital output value can be set for each channel using this function.	0	×	The equivalent function is available by operation in the sequence program.	
Error code	When the write data error occurs, the error code is stored.	0	×	No error codes. Check the error cause using the ON/OFF state of remote input signal. Page 33 Comparison of remote I/O signals	
Error flag	The flag is set when an error other than the watchdog timer error occurs.	0	0	-	

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■Comparison of remote I/O signals

The remote I/O signals are different. Change the sequence program.

For details, refer to the user's manuals.

n: Address assigned to the master station by station number setting

Signal dir	ection: Remote module	e → Master module	Signal dir	ection: Master module $ ightarrow$	Remote module	
Remote	Signal name		Remote	Signal name		
input (RX)	Model to be discontinued	Alternative model	output (RY)	Model to be discontinued	Alternative model	
	AJ65BT-64AD	AJ65SBT-64AD		AJ65BT-64AD	AJ65SBT-64AD	
RXn0	CH1 A/D conversion com	pletion flag	RYn0	Offset/gain value selection	CH1 moving average processing specifying flag	
RXn1	CH2 A/D conversion completion flag		RYn1	Voltage/current selection	CH2 moving average processing specifying flag	
RXn2	CH3 A/D conversion completion flag		RYn2	Use prohibited CH3 moving average processpecifying flag		
RXn3	CH4 A/D conversion completion flag		RYn3		CH4 moving average processing specifying flag	
RXn4	Use prohibited	CH1 range error flag	RYn4		Use prohibited	
RXn5		CH2 range error flag	RYn5			
RXn6		CH3 range error flag	RYn6			
RXn7		CH4 range error flag	RYn7			
RXn8		Use prohibited	RYn8			
RXn9			RYn9			
RXnA			RYnA			
RXnB			RYnB			
RXnC		E ² PROM write error flag	RYnC			
RXnD		Use prohibited	RYnD			
RXnE			RYnE			
RXnF		Test mode flag	RYnF			
RX(n+1)0		Use prohibited	RY(n+1)0			
RX(n+1)1			RY(n+1)1			
RX(n+1)2			RY(n+1)2			
RX(n+1)3			RY(n+1)3			
RX(n+1)4			RY(n+1)4			
RX(n+1)5			RY(n+1)5			
RX(n+1)6			RY(n+1)6			
RX(n+1)7			RY(n+1)7			
RX(n+1)8	Initial data processing red	quest flag	RY(n+1)8	Initial data processing completion flag		
RX(n+1)9	Initial data setting completion flag		RY(n+1)9	Initial data setting request flag		
RX(n+1)A	Error status flag		RY(n+1)A	Error reset request flag		
RX(n+1)B	Remote READY		RY(n+1)B	Use prohibited	Use prohibited	
RX(n+1)C :	Use prohibited	Use prohibited	RY(n+1)C :			
RX(n+1)F			RY(n+1)F			
RX(n+2)0 :	Use prohibited	Unusable (not assigned because the number of	RY(n+2)0 :	Use prohibited	Unusable (not assigned because the number of occupied	
RX(n+3)F		occupied stations is 1)	RY(n+3)F		stations is 1)	

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■Comparison of remote register

The assignment of remote resisters is changed. Change the sequence program.

For details, refer to the user's manuals.

m, n: Address assigned to the master station by station number setting

Direction	Address	Model to be discontinued AJ65BT-64AD		Alternative model	Alternative model AJ65SBT-64AD	
				AJ65SBT-64AD		
		Description	Default	Description	Default	
Master → Remote	RWwm	Average processing specification	0	A/D conversion enable/prohibit specification	0	
	RWwm+1	CH1 average time, number of times	0	Input range setting	0	
	RWwm+2	CH2 average time, number of times	0	Moving average processing count setting	0	
	RWwm+3	CH3 average time, number of times	0	Use prohibited	_	
	RWwm+4	CH4 average time, number of times	0	Unusable (not assigned because the number of occupied stations is 1)		
	RWwm+5	Data format	0	1		
	RWwm+6	A/D conversion enable/prohibit specification	0	1		
	RWwm+7	Use prohibited	_			
Remote → Master	RWrn	CH1 digital output value	0	CH1 digital output value	0	
	RWrn+1	CH2 digital output value	0	CH2 digital output value	0	
	RWrn+2	CH3 digital output value	0	CH3 digital output value	0	
	RWrn+3	CH4 digital output value	0	CH4 digital output value	0	
	RWrn+4	Error code	0	Unusable (not assigned because	Unusable (not assigned because the number of	
	RWrn+5	Use prohibited	_	occupied stations is 1)		
	RWrn+6					
	RWrn+7					

Voltage output (AJ65BT-64DAV)

Replacement from AJ65BT-64DAV to AJ65SBT2B-64DA

Model to be discontinued		Alternative model	
Model	Specifications	Model	Specifications
AJ65BT-64DAV	Voltage output	AJ65SBT2B-64DA	Voltage/current output

■Performance specifications comparison

 $\bigcirc :$ Compatible, $\triangle :$ Check required, $\times :$ Not compatible

Item		Model to be discontinued	Alternative model	Compatibility	Precautions for replacement
		AJ65BT-64DAV	AJ65SBT2B-64DA		
Digital input value	Voltage	16-bit signed binary data (data: 12 bits) -2048 to 2047	16-bit signed binary data -16384 to 16383, -12288 to 12287, -288 to 12287	Δ	The range of digital input value is wider.
	Current	_	16-bit signed binary data -288 to 12287	0	_
Analog output value	Voltage	-10 to 10VDC (external load resistance: $2k\Omega$ to $1M\Omega)$	-10 to 10VDC (external load resistance: $1k\Omega$ to $1M\Omega$)	0	_
	Current	_	0 to 20mADC (external load resistance: 0 to 600Ω)	0	_
I/O characteristics, maximum resolution		*1	*2	Δ	The maximum resolution is smaller, resulting in finer control.
Total accuracy (accuracy for the maximum analog output value)	Ambient temperature: 25±5°C	±1% (±100mV)	±0.2% • -10 to 10V, user range setting 2: ±20mV • 0 to 5V, 1 to 5V: ±10mV • 0 to 20mA, 4 to 20mA, user range setting 1: ±40µA	0	_
	Ambient temperature: 0 to 55°C		±0.3% • -10 to 10V, user range setting 2: ±30mV • 0 to 5V, 1 to 5V: ±15mV • 0 to 20mA, 4 to 20mA, user range setting 1: ±60µA		
Conversion speed		1ms/channel	200μs/channel	0	The conversion accuracy may be degraded due to noise. Verify the accuracy in the system.
Output short circuit protection		Available		0	_
Number of analog output channels		4 channels		0	_
Number of flash memory writes		_	10 thousand times maximum	0	_
CC-Link station type		Remote device station		0	-
Number of occupied stations		2 stations	1 station	0	_
Connection cable		CC-Link dedicated cable		0	-
Withstand voltage		Between all power supply and communication system terminals and all analog output terminals 500VAC for 1 minute		0	_

Isolation method		Model to be discontinued	Alternative model	Compatibility	Precautions for replacement
		AJ65BT-64DAV	AJ65SBT2B-64DA		
		Between power supply system terminals and all analog output terminals: Transformer isolation Between output channels: Non- isolation Between output channels: Non- isolation Between output channels: Non- isolation Between output channels: Non- isolation		0	_
Noise immunity		Noise voltage 500Vp-p, noise width 1µs, noise frequency 25 to 60Hz (noise simulator condition)		0	_
Built-in terminati	ng resistor	None	Disable/Enable (110Ω): Switchable	0	_
Offset/gain settir	ngs	Available		0	_
External power	Voltage	24VDC (20.4 to 26.4VDC)	24VDC (20.4 to 28.8VDC)	0	_
supply	Inrush current	1.5A, 0.67ms or less	2.6A, 3.2ms or less	Δ	The inrush current is higher. Check that the current value does not affect the system.
	Current consumption	0.18A (at 24VDC)	0.24A (at 24VDC)	Δ	The current consumption is higher. Check that the current consumption does not affect the system.
External interface	Communication part, module power supply part	27-point two-piece terminal block (M3.5×7)	7-point two-piece terminal block M3×5.2 screw (tightening torque range: 0.59 to 0.88 N•m) Applicable solderless terminal: 2 or less	×	The screw size is different.
	I/O part		18-point two-piece terminal block M3×5.2 screw (tightening torque range: 0.59 to 0.88 N•m) Applicable solderless terminal: 2 or less	×	
Applicable wire s	size	0.75 to 2.0mm (18 to 14 AWG)	0.3 to 2.0mm (22 to 14 AWG)	0	_
Applicable solderless terminal		RAV1.25-3.5 (compliant with JIS C 2805), RAV2-3.5	RAV1.25-3 (compliant with JIS C 2805) [Applicable wire size: 0.3 to 1.25mm² (22 to 16 AWG)] V2-MS3 (JST Mfg. Co., Ltd.), RAP2-3SL (Nippon Tanshi Co., Ltd.), TGV2-3N (NICHIFU Co., Ltd.) [Applicable wire size: 1.25 to 2.0mm² (16 to 14 AWG)]	×	The screw size is different. Change the solderless terminals.
External	Height (H)	65mm	50mm	0	_
dimensions	Width (W)	151.9mm	122mm	0	_
	Depth (D)	63mm	54mm	0	_
Installation hall		φ4.5 (2 places)	4.5×5.1 (2 places)	0	_
Installation hall	Height (H)	56mm	0mm	×	The installation
pitch	Width (W)	143mm	109.5mm	×	hall pitch is different.
Weight		0.4kg	0.25kg	0	_

^{*1} The following table lists the I/O characteristics and maximum resolution of AJ65BT-64DAV.

Analog output range		Digital input value	Maximum resolution	
Voltage	-10 to 10V	-2000 to 2000	5mV	

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 $^{\star}2$ The following table lists the I/O characteristics and maximum resolution of AJ65SBT2B-64DA.

Analog outpo	ut range	Digital input value	Maximum resolution
Voltage	-10 to 10V	-16000 to 16000	0.625mV
	0 to 5V	0 to 12000	0.416mV
	1 to 5V		0.333mV
	User range setting 2 (-10 to 10V)	-12000 to 12000	0.3125mV
Current	0 to 20mA	0 to 12000	1.66μΑ
	4 to 20mA		1.33μΑ
	User range setting 1 (0 to 20mA)		0.95μΑ

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■Functional comparison

○: Available, ×: Not available

Item	Description	Model to be discontinued	Alternative model	Precautions for replacement
		AJ65BT-64DAV	AJ65SBT2B-64DA	
D/A output enable/ disable function	Specifies a value to be output, D/A conversion value or offset value, for each channel. The conversion speed is constant regardless of the output enable/ disable setting.	0	0	_
D/A conversion enable/disable function	Sets the D/A conversion enable/ disable status for each channel. Setting "Disable" for the unused channel shortens the conversion speed.	0	0	The remote register settings of AJ65BT-64DAV is changed to the remote I/O signal settings of AJ65SBT2B-64DA.
Output range switching function	Sets the analog output range for each channel to change the I/O conversion characteristics.	×	0	_
Analog output hold/ clear function when the programmable controller CPU is in the STOP status (HOLD/ CLEAR setting)	Specifies whether to hold or clear an analog value (output an offset value) output from each channel immediately before the programmable controller CPU has entered the STOP status or the module has stopped D/A conversion due to an error.	(All channels)	(Each channel)	The terminal wiring of AJ65BT-64DAV is changed to the remote I/O signal settings of AJ65SBT2B-64DA.
Offset/gain settings	The offset/gain setting can be set for each channel using this function.	0	0	_
Transmission speed auto-tracking function	When the module is powered on, the transmission speed is set automatically depending on the setting in the master module.	×	0	_
Error code	When the write data error occurs, the error code is stored.	0	0	_
Error flag	The flag is set when an error other than the watchdog timer error occurs.	0	0	_

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■Comparison of remote I/O signals

The remote I/O signals are different. Change the sequence program.

For details, refer to the user's manuals.

n: Address assigned to the master station by station number setting

Signal direct	ion: Remote module -	Master module	Signal direction: Master module → Remote module			
Remote	Signal name		Remote	Signal name		
input (RX)	Model to be discontinued	Alternative model	output (RY)	Model to be discontinued	Alternative model	
	AJ65BT-64DAV	AJ65SBT2B-64DA	1	AJ65BT-64DAV	AJ65SBT2B-64DA	
RXn0	Use prohibited	Use prohibited	RYn0	CH1 Analog output enable/d	isable flag	
RXn1			RYn1	CH2 Analog output enable/d	isable flag	
RXn2			RYn2	CH3 Analog output enable/d	isable flag	
RXn3			RYn3	CH4 Analog output enable/d	isable flag	
RXn4			RYn4	Offset/gain value selection	CH1 Input range setting (0th bit)	
RXn5			RYn5	Use prohibited	CH1 Input range setting (1st bit)	
RXn6			RYn6		CH1 Input range setting (2nd bit)	
RXn7			RYn7		CH2 Input range setting (0th bit)	
RXn8			RYn8		CH2 Input range setting (1st bit)	
RXn9			RYn9		CH2 Input range setting (2nd bit)	
RXnA		Flash memory write error flag	RYnA		CH3 Input range setting (0th bit)	
RXnB		User range read error flag	RYnB		CH3 Input range setting (1st bit)	
RXnC		Flash memory write error flag	RYnC		CH3 Input range setting (2nd bit)	
RXnD		Use prohibited	RYnD		CH4 Input range setting (0th bit)	
RXnE			RYnE		CH4 Input range setting (1st bit)	
RXnF		Test mode flag	RYnF		CH4 Input range setting (2nd bit)	
RX(n+1)0	Use prohibited	Use prohibited	RY(n+1)0	Use prohibited	CH1 HOLD/CLEAR setting	
RX(n+1)1			RY(n+1)1		CH2 HOLD/CLEAR setting	
RX(n+1)2			RY(n+1)2		CH3 HOLD/CLEAR setting	
RX(n+1)3			RY(n+1)3		CH4 HOLD/CLEAR setting	
RX(n+1)4			RY(n+1)4		CH1 Conversion enable/ disable setting	
RX(n+1)5			RY(n+1)5		CH2 Conversion enable/ disable setting	
RX(n+1)6			RY(n+1)6		CH3 Conversion enable/ disable setting	
RX(n+1)7			RY(n+1)7		CH4 Conversion enable/ disable setting	
RX(n+1)8	Initial data processing re	equest flag	RY(n+1)8	Initial data processing comp	letion flag	
RX(n+1)9	Initial data setting compl	etion flag	RY(n+1)9	Initial data setting request fla	ag	
RX(n+1)A	Error status flag		RY(n+1)A	Error reset request flag		

Signal direct	ion: Remote module →	Master module	Signal direction: Master module $ ightarrow$ Remote module		
discontinued	Signal name	Signal name		Signal name	
	Alternative model	output (RY)	Model to be discontinued	Alternative model	
	AJ65BT-64DAV	AJ65SBT2B-64DA		AJ65BT-64DAV	AJ65SBT2B-64DA
RX(n+1)B	Remote READY	Remote READY		Use prohibited	Use prohibited
RX(n+1)C : RX(n+1)F	Use prohibited	Use prohibited	RY(n+1)C : RY(n+1)F		
RX(n+2)0 : RX(n+3)F	Use prohibited	Unusable (not assigned because the number of occupied stations is 1)	RY(n+2)0 : RY(n+3)F	Use prohibited	Unusable (not assigned because the number of occupied stations is 1)

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■Comparison of remote register

The assignment of remote resisters is changed. Change the sequence program.

For details, refer to the user's manuals.

m, n: Address assigned to the master station by station number setting

Direction	Address	Model to be discontinued	Model to be discontinued				
		AJ65BT-64DAV		AJ65SBT2B-64DA			
		Description	Default	Description	Default		
Master → Remote	RWwm	CH1 digital value setting	0	CH1 Digital input value setting	0		
	RWwm+1	CH2 digital value setting	0	CH2 Digital input value setting	0		
	RWwm+2	CH3 digital value setting	0	CH3 Digital input value setting	0		
	RWwm+3	CH4 digital value setting	0	CH4 Digital input value setting	0		
	RWwm+4	Analog output enable/disable	0	Unusable (not assigned becaus	e the number of		
	RWwm+5	Use prohibited	_	occupied stations is 1)	occupied stations is 1)		
	RWwm+6						
	RWwm+7						
Remote → Master	RWrn	CH1 set value check code	0	CH1, 2 Check code	0		
	RWrn+1	CH2 set value check code	0	CH3, 4 Check code	0		
	RWrn+2	CH3 set value check code	0	Error code	0		
	RWrn+3	CH4 set value check code	0	Use prohibited	_		
	RWrn+4	Error code	0	Unusable (not assigned becaus	e the number of		
	RWrn+5	Use prohibited	Use prohibited — occupied stations				
	RWrn+6						
	RWrn+7						

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Replacement from AJ65BT-64DAV to AJ65SBT-62DA

Model to be discontinued		Alternative model	
Model	Specifications	Model	Specifications
AJ65BT-64DAV	Voltage output	AJ65SBT-62DA	Voltage/current output

■Performance specifications comparison

 \bigcirc : Compatible, \triangle : Check required, \times : Not compatible

Item		Model to be discontinued	Alternative model	Compatibility	Precautions
		AJ65BT-64DAV	AJ65SBT-62DA		for replacement
Digital input value	Voltage	16-bit signed binary data (data: 12 bits) -2048 to 2047	16-bit signed binary data -4096 to 4095	Δ	The range of digital input value is wider.
	Current	_	16-bit signed binary data 0 to 4095	0	_
Analog output value	Voltage	-10 to 10VDC (external load resistance: $2k\Omega$ to $1M\Omega)$	-10 to 10VDC (external load resistance: $1k\Omega$ to $1M\Omega$)	0	_
	Current	_	0 to 20mADC (external load resistance: 0 to 600Ω)	0	_
I/O characteristic resolution	cs, maximum	*1	*2	Δ	The maximum resolution is smaller, resulting in finer control.
Total accuracy (accuracy for the maximum analog output value) Ambient temperature: 25±5°C		±1% (±100mV)	±0.2% • -10 to 10V, user range setting 1: ±20mV • 0 to 5V, 1 to 5V, user range setting 2: ±10mV • 0 to 20mA, 4 to 20mA, user range setting 3: ±40μA	0	_
	Ambient temperature: 0 to 55°C		±0.4% • -10 to 10V, user range setting 1: ±40mV • 0 to 5V, 1 to 5V, user range setting 2: ±20mV • 0 to 20mA, 4 to 20mA, user range setting 3: ±80µA		
Conversion spec	ed	1ms/channel		0	_
Output short circ	cuit protection	Available		0	_
Number of analochannels	og output	4 channels	2 channels	×	When using three channels o more, use two modules.
CC-Link station	type	Remote device station		0	_
Number of occup	pied stations	2 stations	1 station	0	_
Connection cabl	е	CC-Link dedicated cable		0	_
Withstand voltage		Between all power supply and communication system terminals and all analog output terminals 500VAC for 1 minute		0	_
Isolation method		Between power supply system terminals and all analog output terminals: Transformer isolation Between output channels: Non- isolation	Between communication system terminals and all analog output terminals: Photocoupler Between power supply system terminals and all analog output terminals: Photocoupler Between output channels: Nonisolation	0	_
Noise immunity		Noise voltage 500Vp-p, noise width 1μs, simulator condition)	noise frequency 25 to 60Hz (noise	0	
Built-in terminati	na resistor	None		0	_

Item		Model to be discontinued	Alternative model	Compatibility	Precautions	
		AJ65BT-64DAV	AJ65SBT-62DA		for replacement	
Offset/gain settings		Available	0	_		
External power	Voltage	24VDC (20.4 to 26.4VDC)		0	_	
supply	Inrush current	1.5A, 0.67ms or less	8.2A, 2.1ms or less	Δ	The inrush current is higher. Check that the current consumption does not affect the system.	
	Current consumption	0.18A (at 24VDC)	0.16A (at 24VDC)	0	_	
External interface	Communication part, module power supply part	27-point two-piece terminal block (M3.5×7)	7-point two-piece terminal block M3×5.2 screw (tightening torque range: 0.59 to 0.88 N•m) Applicable solderless terminal: 2 or less	×	The screw size is different.	
	I/O part		18-point direct-mount terminal block M3×5.2 screw (tightening torque range: 0.59 to 0.88 N•m) Applicable solderless terminal: 2 or less	Х		
Applicable wire s	size	0.75 to 2.0mm (18 to 14 AWG)	0.3 to 0.75mm (22 to 18 AWG)	×	The screw size is	
Applicable solderless terminal		RAV1.25-3.5 (compliant with JIS C 2805), RAV2-3.5	RAV1.25-3 (compliant with JIS C 2805) [Applicable wire size: 0.3 to 1.25mm² (22 to 16 AWG)] V2-MS3 (JST Mfg. Co., Ltd.), RAP2-3SL (Nippon Tanshi Co., Ltd.), TGV2-3N (NICHIFU Co., Ltd.) [Applicable wire size: 1.25 to 2.0mm² (16 to 14 AWG)]	×	different. Change the solderless terminals.	
External	Height (H)	65mm	50mm	0	_	
dimensions	Width (W)	151.9mm	118mm	0	_	
	Depth (D)	63mm	40mm	0	_	
Installation hall		φ4.5 (2 places)	4.5×5.1 (2 places)	0	_	
Installation hall	Height (H)	56mm	0mm	×	The installation	
pitch	Width (W)	143mm	109mm	×	hall pitch is different.	
Weight		0.4kg	0.2kg	0	_	

^{*1} The following table lists the I/O characteristics and maximum resolution of AJ65BT-64DAV.

Analog output range		Digital input value	Maximum resolution
Voltage	-10 to 10V	-2000 to 2000	5mV

 $^{^{*}2}$ The following table lists the I/O characteristics and maximum resolution of AJ65SBT-62DA.

Analog output	range	Digital input value	Maximum resolution
Voltage	-10 to 10V	-4000 to 4000	2.5mV
	User range setting 1 (-10 to 10V)		
	0 to 5V	0 to 4000	1.25mV
	1 to 5V		1.0mV
	User range setting 2 (0 to 5V)		
Current	0 to 20mA	0 to 4000	5μΑ
	4 to 20mA		4μΑ
	User range setting 3 (0 to 20mA)	1	

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■Functional comparison

○: Available, ×: Not available

Item	Description	Model to be discontinued	Alternative model	Precautions for replacement
		AJ65BT-64DAV	AJ65SBT-62DA	
D/A output enable/disable function	Specifies a value to be output, D/A conversion value or offset value, for each channel. The conversion speed is constant regardless of the output enable/ disable setting.	0	0	_
D/A conversion enable/ disable function	The D/A conversion can be enabled or disabled for each channel using this function. Setting "Disable" for the unused channel shortens the conversion speed.	0	0	_
Output range switching function	Sets the analog output range for each channel to change the I/O conversion characteristics.	×	0	_
Analog output hold/clear function when the programmable controller CPU is in the STOP status (HOLD/CLEAR setting)	Specifies whether to hold or clear an analog value (output an offset value) output from each channel immediately before the programmable controller CPU has entered the STOP status or the module has stopped D/A conversion due to an error.	(All channels)	(Each channel)	The terminal wiring of AJ65BT-64DAV is changed to the remote I/O signal settings of AJ65SBT-62DA.
Offset/gain settings	The offset/gain setting can be set for each channel using this function.	0	0	_
Error code	When the write data error occurs, the error code is stored.	0	0	_

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■Comparison of remote I/O signals

The remote I/O signals are different. Change the sequence program.

For details, refer to the user's manuals.

n: Address assigned to the master station by station number setting

Remote	Signal name		Remote output (RY)	Signal name		
input (RX)	Model to be discontinued	Alternative model		Model to be discontinued	Alternative model	
	AJ65BT-64DAV	AJ65SBT-62DA		AJ65BT-64DAV	AJ65SBT-62DA	
RXn0	Use prohibited	Use prohibited	RYn0	CH1 analog output enable/o	lisable flag	
RXn1			RYn1	CH2 analog output enable/o	lisable flag	
RXn2			RYn2	CH3 analog output enable/ disable flag	Use prohibited	
RXn3			RYn3	CH4 analog output enable/ disable flag		
RXn4			RYn4	Offset/gain value selection	7	
RXn5			RYn5	Use prohibited	7	
RXn6			RYn6			
RXn7			RYn7			
RXn8	7		RYn8			
RXn9	_		RYn9			
RXnA			RYnA			
RXnB			RYnB			
RXnC		E ² PROM write error flag	RYnC	_		
RXnD		Use prohibited	RYnD			
RXnE			RYnE			
RXnF		Test mode flag	RYnF			
RX(n+1)0	Use prohibited	Use prohibited	RY(n+1)0	Use prohibited	Use prohibited	
RX(n+1)1			RY(n+1)1			
RX(n+1)2			RY(n+1)2			
RX(n+1)3			RY(n+1)3			
RX(n+1)4	_		RY(n+1)4			
RX(n+1)5			RY(n+1)5			
RX(n+1)6			RY(n+1)6			
RX(n+1)7			RY(n+1)7			
RX(n+1)8	Initial data processing red	quest flag	RY(n+1)8	Initial data processing comp	letion flag	
RX(n+1)9	Initial data setting comple	etion flag	RY(n+1)9	Initial data setting request fl	ag	
RX(n+1)A	Error status flag		RY(n+1)A	Error reset request flag		
RX(n+1)B	Remote READY		RY(n+1)B	Use prohibited	Use prohibited	
RX(n+1)C :	Use prohibited	Use prohibited	RY(n+1)C :			
RX(n+1)F			RY(n+1)F			
RX(n+2)0 :	Use prohibited	Unusable (not assigned because the number of	RY(n+2)0 :	Use prohibited	Unusable (not assigned because the number of	
RX(n+3)F		occupied stations is 1)	RY(n+3)F		occupied stations is 1)	

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■Comparison of remote register

The assignment of remote resisters is changed. Change the sequence program.

For details, refer to the user's manuals.

m, n: Address assigned to the master station by station number setting

Direction	Address	Model to be discontinued		Alternative model		
		AJ65BT-64DAV		AJ65SBT-62DA		
		Description	Default	Description	Default	
Master → Remote	RWwm	CH1 digital value setting	0	CH1 digital value setting	0	
	RWwm+1	CH2 digital value setting	0	CH2 digital value setting	0	
	RWwm+2	CH3 digital value setting	0	Analog output enable/disable setting	0	
	RWwm+3	CH4 digital value setting	0	Output range, HOLD/CLEAR setting	0	
	RWwm+4	Analog output enable/disable	0	, ,	Unusable (not assigned because the number of	
	RWwm+5	Use prohibited	_	occupied stations is 1)		
	RWwm+6					
	RWwm+7					
Remote → Master	RWrn	CH1 set value check code	0	CH1 check code	0	
	RWrn+1	CH2 set value check code	0	CH2 check code	0	
	RWrn+2	CH3 set value check code	0	Error code	0	
	RWrn+3	CH4 set value check code	0	Use prohibited	_	
	RWrn+4	Error code	0	Unusable (not assigned because	e the number of	
	RWrn+5	Use prohibited	_	occupied stations is 1)		
	RWrn+6					
	RWrn+7					

Current output (AJ65BT-64DAI)

Replacement from AJ65BT-64DAI to AJ65SBT2B-64DA

Model to be discontinued		Alternative model		
Model Specifications		Model	Specifications	
AJ65BT-64DAI	Current output	AJ65SBT2B-64DA	Voltage/current output	

■Performance specifications comparison

 $\bigcirc :$ Compatible, $\triangle :$ Check required, $\times :$ Not compatible

Item		Model to be discontinued	Alternative model	Compatibility	Precautions
		AJ65BT-64DAI	AJ65SBT2B-64DA		for replacement
Digital input value	Voltage	_	16-bit signed binary data -12288 to 12287, -16384 to 16383, -288 to 12287	0	_
	Current	16-bit signed binary data (data: 12 bits) 0 to 4095	16-bit signed binary data -288 to 12287	Δ	The range of digital input value is wider.
Analog output value	Voltage	_	-10 to 10VDC (external load resistance: 1k Ω to 1M Ω)	0	_
	Current	4 to 20mADC (external load resistance: 0 to 600Ω)	0 to 20mADC (external load resistance: 0 to 600Ω)	0	_
I/O characteristi resolution	cs, maximum	*1	*2	Δ	The maximum resolution is smaller, resulting in finer control.
Total accuracy (accuracy for the maximum analog output value)	Ambient temperature: 25±5°C	±1% (±200μA)	±0.2% • -10 to 10V, user range setting 2: ±20mV • 0 to 5V, 1 to 5V: ±10mV • 0 to 20mA, 4 to 20mA, user range setting 1: ±40μA	0	_
	Ambient temperature: 0 to 55°C		±0.3% • -10 to 10V, user range setting 2: ±30mV • 0 to 5V, 1 to 5V: ±15mV • 0 to 20mA, 4 to 20mA, user range setting 1: ±60µA		
Conversion speed		1ms/channel	200μs/channel	Δ	The conversion accuracy may be degraded due to noise. Verify the accuracy in the system.
Output short circ	cuit protection	Available		0	_
Number of analog output channels		4 channels		0	_
Number of flash memory writes		_	10 thousand times maximum	0	_
CC-Link station	type	Remote device station		0	_
Number of occupied stations		2 stations	1 station	0	_
Connection cab	le	CC-Link dedicated cable	•	0	_
Withstand voltage	ge	Between all power supply and communication system terminals and all analog output terminals 500VAC for 1 minute		0	_

Item		Model to be discontinued	Alternative model	Compatibility	Precautions
		AJ65BT-64DAI	AJ65SBT2B-64DA		for replacement
Isolation method		Between power supply system terminals and all analog output terminals: Transformer isolation Between output channels: Non-isolation	Between communication system terminals and all analog output terminals: Photocoupler Between power supply system terminals and all analog output terminals: Transformer isolation Between output channels: Non-isolation	0	_
Noise immunity		Noise voltage 500Vp-p, noise width 1 simulator condition)	μs, noise frequency 25 to 60Hz (noise	0	_
Built-in terminati	ng resistor	None	Disable/Enable (110Ω): Switchable	0	_
Offset/gain settir	igs	Available		0	_
External power	Voltage	24VDC (20.4 to 26.4VDC)	24VDC (20.4 to 28.8VDC)	0	_
supply	Inrush current	3.2A, 0.43ms or less	2.6A, 3.2ms or less	0	_
	Current consumption	0.27A (at 24VDC)	0.24A (at 24VDC)	0	_
External interface	Communication part, module power supply part	27-point two-piece terminal block (M3.5×7)	7-point two-piece terminal block M3×5.2 screw (tightening torque range: 0.59 to 0.88 N•m) Applicable solderless terminal: 2 or less	×	The screw size different.
	I/O part		18-point two-piece terminal block M3×5.2 screw (tightening torque range: 0.59 to 0.88 N•m) Applicable solderless terminal: 2 or less	×	
Applicable wire s	size	0.75 to 2.0mm (18 to 14 AWG)	0.3 to 2.0mm (22 to 14 AWG)	0	_
Applicable solderless terminal		RAV1.25-3.5 (compliant with JIS C 2805), RAV2-3.5	RAV1.25-3 (compliant with JIS C 2805) [Applicable wire size: 0.3 to 1.25mm² (22 to 16 AWG)] V2-MS3 (JST Mfg. Co., Ltd.), RAP2-3SL (Nippon Tanshi Co., Ltd.), TGV2-3N (NICHIFU Co., Ltd.) [Applicable wire size: 1.25 to 2.0mm² (16 to 14 AWG)]	×	The screw size is different. Change the solderless terminals.
External	Height (H)	65mm	50mm	0	_
dimensions	Width (W)	151.9mm	122mm	0	_
	Depth (D)	63mm	54mm	0	_
Installation hall		φ4.5 (2 places)	4.5×5.1 (2 places)	0	_
Installation hall	Height (H)	56mm	0mm	×	The installation
pitch	Width (W)	143mm	109.5mm	×	hall pitch is different.
Weight		0.4kg	0.25kg	0	_

^{*1} The following table lists the I/O characteristics and maximum resolution of AJ65BT-64DAI.

Analog output range		Digital input value	Maximum resolution	
Current	4 to 20mA	0 to 4000	4μΑ	

^{*2} The following table lists the I/O characteristics and maximum resolution of AJ65SBT2B-64DA.

Analog outp	ut range	Digital input value	Maximum resolution
Voltage	-10 to 10V	-16000 to 16000	0.625mV
	0 to 5V	0 to 12000	0.416mV
	1 to 5V		0.333mV
	User range setting 2 (-10 to 10V)	-12000 to 12000	0.3125mV
Current	0 to 20mA	0 to 12000	1.66μΑ
	4 to 20mA		1.33μΑ
	User range setting 1 (0 to 20mA)		0.95μΑ

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■Functional comparison

○: Available, ×: Not available

Item	Description	Model to be discontinued	Alternative model	Precautions for replacement	
		AJ65BT-64DAI	AJ65SBT2B-64DA		
D/A output enable/ disable function	Specifies a value to be output, D/A conversion value or offset value, for each channel. The conversion speed is constant regardless of the output enable/ disable setting.	0	0	_	
D/A conversion enable/disable function	The D/A conversion can be enabled or disabled for each channel using this function. Setting "Disable" for the unused channel shortens the conversion speed.	0	0	The remote register settings of AJ65BT-64DAI is changed to the remote I/O signal settings of AJ65SBT2B-64DA.	
Output range switching function	Sets the analog output range for each channel to change the I/O conversion characteristics.	×	0	_	
Analog output hold/ clear function when the programmable controller CPU is in the STOP status (HOLD/ CLEAR setting)	Specifies whether to hold or clear an analog value (output an offset value) output from each channel immediately before the programmable controller CPU has entered the STOP status or the module has stopped D/A conversion due to an error.	(All channels)	(Each channel)	The terminal wiring of AJ65BT-64DAI is changed to the remote I/O signal settings of AJ65SBT2B-64DA.	
Offset/gain settings	The offset/gain setting can be set for each channel using this function.	0	0	_	
Transmission speed auto-tracking function	When the module is powered on, the transmission speed is set automatically depending on the setting in the master module.	×	0	_	
Error code	When the write data error occurs, the error code is stored.	0	0	_	
Error flag	The flag is set when an error other than the watchdog timer error occurs.	0	0	_	

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■Comparison of remote I/O signals

The remote I/O signals are different. Change the sequence program.

For details, refer to the user's manuals.

n: Address assigned to the master station by station number setting

Signal direct	ion: Remote module –	Master module	Signal direction: Master module → Remote module			
Remote	Signal name		Remote	Signal name		
input (RX)	Model to be	Alternative model	output (RY)	Model to be	Alternative model	
	discontinued			discontinued		
	AJ65BT-64DAI	AJ65SBT2B-64DA		AJ65BT-64DAI	AJ65SBT2B-64DA	
RXn0	Use prohibited	Use prohibited	RYn0	CH1 analog output enable/d	isable flag	
RXn1			RYn1	CH2 analog output enable/d	isable flag	
RXn2			RYn2	CH3 analog output enable/d	isable flag	
RXn3			RYn3	CH4 analog output enable/d	isable flag	
RXn4			RYn4	Offset/gain value selection	CH1 Input range setting (0th bit)	
RXn5			RYn5	Use prohibited	CH1 Input range setting (1s bit)	
RXn6			RYn6		CH1 Input range setting (2nd bit)	
RXn7			RYn7		CH2 Input range setting (0th bit)	
RXn8			RYn8		CH2 Input range setting (1s bit)	
RXn9			RYn9		CH2 Input range setting (2nd bit)	
RXnA		Flash memory write error flag	RYnA		CH3 Input range setting (0th bit)	
RXnB		User range read error flag	RYnB		CH3 Input range setting (1s bit)	
RXnC		Flash memory write error flag	RYnC		CH3 Input range setting (2nd bit)	
RXnD		Use prohibited	RYnD		CH4 Input range setting (0th bit)	
RXnE			RYnE		CH4 Input range setting (1s bit)	
RXnF		Test mode flag	RYnF		CH4 Input range setting (2nd bit)	
RX(n+1)0	Use prohibited	Use prohibited	RY(n+1)0	Use prohibited	CH1 HOLD/CLEAR setting	
RX(n+1)1			RY(n+1)1		CH2 HOLD/CLEAR setting	
RX(n+1)2			RY(n+1)2		CH3 HOLD/CLEAR setting	
RX(n+1)3			RY(n+1)3		CH4 HOLD/CLEAR setting	
RX(n+1)4			RY(n+1)4		CH1 Conversion enable/ disable setting	
RX(n+1)5			RY(n+1)5		CH2 Conversion enable/ disable setting	
RX(n+1)6			RY(n+1)6		CH3 Conversion enable/ disable setting	
RX(n+1)7			RY(n+1)7		CH4 Conversion enable/ disable setting	
RX(n+1)8	Initial data processing re	equest flag	RY(n+1)8	Initial data processing comp	letion flag	
RX(n+1)9	Initial data setting compl	letion flag	RY(n+1)9	Initial data setting request fla	ag	
RX(n+1)A	Error status flag		RY(n+1)A	Error reset request flag		

Signal direct	Signal direction: Remote module → Master module			Signal direction: Master module → Remote module		
Remote input (RX)	Signal name	Signal name		Signal name		
	Model to be discontinued	Alternative model	output (RY)	Model to be discontinued	Alternative model	
	AJ65BT-64DAI AJ65SBT2B-64DA			AJ65BT-64DAI	AJ65SBT2B-64DA	
RX(n+1)B	Remote READY	Remote READY		Use prohibited	Use prohibited	
RX(n+1)C : RX(n+1)F	Use prohibited	Use prohibited	RY(n+1)C : RY(n+1)F			
RX(n+2)0 : RX(n+3)F	Use prohibited	Unusable (not assigned because the number of occupied stations is 1)	RY(n+2)0 : RY(n+3)F	Use prohibited	Unusable (not assigned because the number of occupied stations is 1)	

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■Comparison of remote register

The assignment of remote resisters is changed. Change the sequence program.

For details, refer to the user's manuals.

m, n: Address assigned to the master station by station number setting

Direction	Address	ddress Model to be discontinued		Alternative model			
		AJ65BT-64DAI		AJ65SBT2B-64DA			
		Description	Default	Description	Default		
Master → Remote	RWwm	CH1 digital value setting	0	CH1 digital input value setting	0		
	RWwm+1	CH2 digital value setting	0	CH2 digital input value setting	0		
	RWwm+2	CH3 digital value setting	0	CH3 digital input value setting	0		
	RWwm+3	CH4 digital value setting	0	CH4 digital input value setting	0		
	RWwm+4	Analog output enable/disable	0	Unusable (not assigned becaus	e the number of		
	RWwm+5	Use prohibited	_	occupied stations is 1)	occupied stations is 1)		
	RWwm+6						
	RWwm+7						
Remote → Master	RWrn	CH1 set value check code	0	CH1, 2 check code	0		
	RWrn+1	CH2 set value check code	0	CH3, 4 check code	0		
	RWrn+2	CH3 set value check code	0	Error code	0		
	RWrn+3	CH4 set value check code	0	Use prohibited	_		
	RWrn+4	Error code	0	Unusable (not assigned becaus	e the number of		
	RWrn+5	Use prohibited	_	occupied stations is 1)			
	RWrn+6						
	RWrn+7						

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Replacement from AJ65BT-64DAI to AJ65SBT-62DA

Model to be discontinued		Alternative model		
Model Specifications		Model	Specifications	
AJ65BT-64DAI	Current output	AJ65SBT-62DA	Voltage/current output	

■Performance specifications comparison

 \bigcirc : Compatible, \triangle : Check required, \times : Not compatible

Item		Model to be discontinued	Compatibility	Precautions	
item			Alternative model	Companionity	for
		AJ65BT-64DAI	AJ65SBT-62DA		replacement
Digital input value	Voltage	_	16-bit signed binary data -4096 to 4095	0	_
	Current	16-bit signed binary data (data: 12 bits) 0 to 4095	16-bit signed binary data 0 to 4095	0	_
Analog output value	Voltage	_	-10 to 10VDC (external load resistance: $1k\Omega$ to $1M\Omega$)	0	_
	Current	4 to 20mADC (external load resistance: 0 to 600Ω)	0 to 20mADC (external load resistance: 0 to 600Ω)	0	_
I/O characteristic resolution	cs, maximum	*1	*2	0	_
Total accuracy (accuracy for the maximum analog output value)	Ambient temperature: 25±5°C Ambient temperature: 0 to 55°C	±1% (±200μA)	$\begin{array}{l} \pm 0.2\% \\ \bullet -10 \text{ to } 10\text{V, user range setting 1:} \\ \pm 20\text{mV} \\ \bullet 0 \text{ to } 5\text{V, 1 to } 5\text{V, user range setting 2:} \\ \pm 10\text{mV} \\ \bullet 0 \text{ to } 20\text{mA, 4 to } 20\text{mA, user range setting 3:} \\ \pm 40\mu\text{A} \\ \\ \pm 0.4\% \\ \bullet -10 \text{ to } 10\text{V, user range setting 1:} \\ \pm 40\text{mV} \\ \bullet 0 \text{ to } 5\text{V, 1 to } 5\text{V, user range setting 2:} \end{array}$	0	_
			±20mV • 0 to 20mA, 4 to 20mA, user range setting 3: ±80μA		
Conversion spec	ed	1ms/channel		0	_
Output short circ	cuit protection	Available	ilable		_
Number of analochannels	og output	4 channels	2 channels	×	When using three channels or more, use two modules.
CC-Link station	type	Remote device station		0	_
Number of occu	pied stations	2 stations	1 station	0	_
Connection cabl	le	CC-Link dedicated cable		0	_
Withstand voltage		Between all power supply and communication system terminals and all analog output terminals 500VAC for 1 minute		0	_
Isolation method		Between power supply system terminals and all analog output terminals: Transformer isolation Between output channels: Non-isolation	utput terminals and all analog output lation terminals: Photocoupler		_
Noise immunity		Noise voltage 500Vp-p, noise width $1\mu s$, noise frequency 25 to 60Hz (noise simulator condition)		0	_
Built-in terminating resistor		None		0	
Offset/gain settings					

Item		Model to be discontinued	Alternative model	Compatibility	Precautions
		AJ65BT-64DAI AJ65SBT-62DA			for replacement
External power	Voltage	24VDC (20.4 to 26.4VDC)	0	_	
supply	Inrush current	3.2A, 0.43ms or less	8.2A, 2.1ms or less	Δ	The inrush current is higher. Check that the current consumption does not affect the system.
	Current consumption	0.27A (at 24VDC)	0.16A (at 24VDC)	0	_
External interface	Communication part, module power supply part	27-point two-piece terminal block (M3.5×7)	7-point two-piece terminal block M3×5.2 screw (tightening torque range: 0.59 to 0.88 N•m) Applicable solderless terminal: 2 or less	×	The screw size is different.
	I/O part		18-point direct-mount terminal block M3×5.2 screw (tightening torque range: 0.59 to 0.88 N•m) Applicable solderless terminal: 2 or less	×	
Applicable wire	size	0.75 to 2.0mm (18 to 14 AWG)	0.3 to 0.75mm (22 to 18 AWG)	×	The screw size is
Applicable solderless terminal		RAV1.25-3.5 (compliant with JIS C 2805), RAV2-3.5	RAV1.25-3 (compliant with JIS C 2805) [Applicable wire size: 0.3 to 1.25mm² (22 to 16 AWG)] V2-MS3 (JST Mfg. Co., Ltd.), RAP2-3SL (Nippon Tanshi Co., Ltd.), TGV2-3N (NICHIFU Co., Ltd.) [Applicable wire size: 1.25 to 2.0mm² (16 to 14 AWG)]	×	different. Change the solderless terminals.
External	Height (H)	65mm	50mm	0	_
dimensions	Width (W)	151.9mm	118mm	0	_
	Depth (D)	63mm	40mm	0	_
Installation hall		φ4.5 (2 places)	4.5×5.1 (2 places)	0	_
Installation hall	Height (H)	56mm	0mm	×	The installation
pitch	Width (W)	143mm	109mm	×	hall pitch is different.
Weight		0.4kg	0.2kg	0	-

^{*1} The following table lists the I/O characteristics and maximum resolution of AJ65BT-64DAI.

Analog output range		Digital input value	Maximum resolution	
Current	4 to 20mA	0 to 4000	4μΑ	

^{*2} The following table lists the I/O characteristics and maximum resolution of AJ65SBT-62DA.

Analog outpu	it range	Digital input value	Maximum resolution	
Voltage	-10 to 10V	-4000 to 4000	2.5mV	
	User range setting 1 (-10 to 10V)			
	0 to 5V	0 to 4000	1.25mV	
	1 to 5V		1mV	
	User range setting 2 (0 to 5V)			
Current	0 to 20mA	0 to 4000	5μΑ	
	4 to 20mA		4μΑ	
	User range setting 3 (0 to 20mA)			

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■Functional comparison

○: Available, ×: Not available

Item	Description	Model to be discontinued	Alternative model	Precautions for replacement
		AJ65BT-64DAI	AJ65SBT-62DA	
D/A output enable/disable function	Specifies a value to be output, D/A conversion value or offset value, for each channel. The conversion speed is constant regardless of the output enable/ disable setting.	0	0	_
D/A conversion enable/ disable function	The D/A conversion can be enabled or disabled for each channel using this function. Setting "Disable" for the unused channel shortens the conversion speed.	0	0	_
Output range switching function	Sets the analog output range for each channel to change the I/O conversion characteristics.	×	0	_
Analog output hold/clear function when the programmable controller CPU is in the STOP status (HOLD/CLEAR setting)	Specifies whether to hold or clear an analog value (output an offset value) output from each channel immediately before the programmable controller CPU has entered the STOP status or the module has stopped D/A conversion due to an error.	(All channels)	(Each channel)	The terminal wiring of AJ65BT-64DAI is changed to the remote I/O signal settings of AJ65SBT-62DA.
Offset/gain settings	The offset/gain setting can be set for each channel using this function.	0	0	_
Error code	When the write data error occurs, the error code is stored.	0	0	_

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■Comparison of remote I/O signals

The remote I/O signals are different. Change the sequence program.

For details, refer to the user's manuals.

n: Address assigned to the master station by station number setting

Remote	Signal name		Remote	Signal name		
input (RX)	Model to be discontinued	Alternative model	output (RY)	Model to be discontinued	Alternative model	
	AJ65BT-64DAI	AJ65SBT-62DA	_	AJ65BT-64DAI	AJ65SBT-62DA	
RXn0	Use prohibited	Use prohibited	RYn0	CH1 analog output enable/d	isable flag	
RXn1			RYn1	CH2 analog output enable/d	isable flag	
RXn2			RYn2	CH3 analog output enable/ disable flag	Use prohibited	
RXn3	_		RYn3	CH4 analog output enable/ disable flag		
RXn4			RYn4	Offset/gain value selection		
RXn5			RYn5	Use prohibited	1	
RXn6			RYn6	1		
RXn7			RYn7			
RXn8			RYn8			
RXn9			RYn9			
RXnA			RYnA			
RXnB			RYnB			
RXnC		E ² PROM write error flag	RYnC			
RXnD		Use prohibited	RYnD			
RXnE			RYnE			
RXnF		Test mode flag	RYnF			
RX(n+1)0	Use prohibited	Use prohibited	RY(n+1)0	Use prohibited	Use prohibited	
RX(n+1)1			RY(n+1)1			
RX(n+1)2			RY(n+1)2			
RX(n+1)3			RY(n+1)3			
RX(n+1)4			RY(n+1)4			
RX(n+1)5			RY(n+1)5			
RX(n+1)6			RY(n+1)6			
RX(n+1)7			RY(n+1)7			
RX(n+1)8	Initial data processing re	equest flag	RY(n+1)8	Initial data processing comp	letion flag	
RX(n+1)9	Initial data setting comp	letion flag	RY(n+1)9	Initial data setting request fla	ag	
RX(n+1)A	Error status flag		RY(n+1)A	Error reset request flag		
RX(n+1)B	Remote READY		RY(n+1)B	Use prohibited	Use prohibited	
RX(n+1)C :	Use prohibited	Use prohibited	RY(n+1)C :			
RX(n+1)F			RY(n+1)F			
RX(n+2)0 :	Use prohibited	Unusable (not assigned because the number of	RY(n+2)0 :	Use prohibited	Unusable (not assigned because the number of	
: RX(n+3)F		occupied stations is 1)	: RY(n+3)F		occupied stations is 1)	

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■Comparison of remote register

The assignment of remote resisters is changed. Change the sequence program.

For details, refer to the user's manuals.

m, n: Address assigned to the master station by station number setting

Direction	Address	Model to be discontinued		Alternative model		
		AJ65BT-64DAI		AJ65SBT-62DA		
		Description	Default	Description	Default	
Master → Remote	RWwm	CH1 digital value setting	0	CH1 digital input value setting	0	
	RWwm+1	CH2 digital value setting	0	CH2 digital input value setting	0	
	RWwm+2	CH3 digital value setting	0	Analog output enable/disable setting	0	
	RWwm+3	CH4 digital value setting	0	Output range, HOLD/CLEAR 0 setting		
	RWwm+4	Analog output enable/disable	0	Unusable (not assigned because the number		
	RWwm+5	Use prohibited	_	occupied stations is 1)		
	RWwm+6					
	RWwm+7]				
Remote → Master	RWrn	CH1 set value check code	0	CH1 check code	0	
	RWrn+1	CH2 set value check code	0	CH2 check code	0	
	RWrn+2	CH3 set value check code	0	Error code	0	
	RWrn+3	CH4 set value check code	0	Use prohibited	_	
	RWrn+4	Error code	0	Unusable (not assigned becaus	e the number of	
	RWrn+5	Use prohibited	_	occupied stations is 1)		
	RWrn+6]				
	RWrn+7	1				

3-wire type platinum temperature-measuring resistor input (AJ65BT-64RD3)

Replacement from AJ65BT-64RD3 to AJ65SBT2B-64RD3

Model to be disconti	nued	Alternative model	
Model Specifications		Model	Specifications
AJ65BT-64RD3	3-wire type platinum temperature-measuring resistor (Pt100, JPt100) input	AJ65SBT2B-64RD3	3-wire type platinum temperature-measuring resistor (Pt100, JPt100, Ni100) input

■Performance specifications comparison

 \bigcirc : Compatible, \triangle : Check required, \times : Not compatible

Item	Model to be discontinued	Alternative model	Compatibility	Precautions	
	AJ65BT-64RD3 AJ65SBT2B-64RD3			for replacement	
Measurement method	3-wire	<u>'</u>	0	_	
Measured temperature value	16-bit signed binary data (-1800 to 6000) 16-bit signed binary data (-2000 to 8500)		Δ	The 32-bit signed binary data is not available.	
	32-bit signed binary data (-180000 to 600000)				
Applicable RDT	Pt100 (JIS C 1604-1997, IEC 751- 1983), JPt100 (JIS C 1604-1981)	Pt100 (JIS C 1604-1997, IEC 751- 1983), JPt100 (JIS C 1604-1981), Ni100 (DIN 43760 1987)	0	_	
Detection current	1mA		0	_	
Measurement range, conversion accuracy, resolution	*1	*2	Δ	The resolution is measured in units of 0.1℃.	
Conversion speed	40ms/channel		0	_	
Number of temperature input points	4 channels		0	_	
Number of flash memory writes	_	10 thousand times maximum	0	_	
CC-Link station type	Remote device station		0	_	
Number of occupied stations	4 stations	1 station	0	_	
Connection cable	CC-Link dedicated cable		0	_	
Withstand voltage	Between all power supply system terminals and ground Between all power supply system terminals and all communication system terminals Between all communication system terminals and all RTD inputs Between all RTD inputs and ground 500VAC for 1 minute	Between all power supply and communication system terminals and all RTD inputs 500VAC for 1 minute	0	_	
Isolation method	Between communication system terminals and all RTD inputs: Photocoupler Between channels: Non-isolation	Between communication system terminals and all RTD inputs: Photocoupler Between power supply system terminals and all RTD inputs: Transformer isolation Between channels: Non-isolation	0		
Noise immunity	Noise voltage 500Vp-p, noise width 1µs simulator condition)	s, noise frequency 25 to 60Hz (noise	0	_	
Built-in terminating resistor	None	Disable/Enable (110Ω): Switchable	0	_	
Offset/gain settings	Available		0	_	
Disconnection detection	Available		0	_	

Item		Model to be discontinued	Alternative model	Compatibility	Precautions
		AJ65BT-64RD3	AJ65SBT2B-64RD3		for replacement
External power supply	Voltage	24VDC (18 to 30VDC)	24VDC (20.4 to 28.8VDC)	Δ	Check the output voltage range and the voltage drop for wire length of power supply device to use.
	Inrush current	_	1.7A, 2.4ms or less	0	_
	Current consumption	0.17A (at 24VDC)	0.14A (at 24VDC)	0	_
Cable between module and RTD		 100Ω or lower (wiring resistance per conductor) Approx. 0.025°C/10mΩ (influence of resistance difference between A and b connecting conductor on measurement resistance) 	• 100Ω or lower (wiring resistance per conductor) • Approx. $0.025^{\circ}\text{C}/10\text{m}\Omega$ (influence of resistance difference between A and b connecting conductor on measurement resistance) • 10Ω or lower (resistance difference between A and b connecting conductor)	0	_
External interface	Communication part, module power supply part	27-point two-piece terminal block (M3.5×7)	7-point two-piece terminal block M3×5.2 screw (tightening torque range: 0.59 to 0.88 N•m) Applicable solderless terminal: 2 or less	×	The screw size is different.
	I/O part		18-point two-piece terminal block M3×5.2 screw (tightening torque range: 0.59 to 0.88 N•m) Applicable solderless terminal: 2 or less	×	
Applicable wire	size	0.75 to 2.0mm (18 to 14 AWG)	0.3 to 2.0mm (22 to 14 AWG)	0	_
Applicable solderless terminal		RAV1.25-3.5, RAV2-3.5 (compliant with JIS C 2805)	RAV1.25-3 (compliant with JIS C 2805) [Applicable wire size: 0.3 to 1.25mm² (22 to 16 AWG)] V2-MS3 (JST Mfg. Co., Ltd.), RAP2-3SL (Nippon Tanshi Co., Ltd.), TGV2-3N (NICHIFU Co., Ltd.) [Applicable wire size: 1.25 to 2.0mm² (16 to 14 AWG)]	×	The screw size is different. Change the solderless terminals.
External	Height (H)	65mm	50mm	0	_
dimensions	Width (W)	151.9mm	122mm	0	_
	Depth (D)	63mm	54mm	0	_
Installation hall		φ4.5 (2 places)	4.5×5.1 (2 places)	0	_
Installation hall pitch	Height (H) Width (W)	56mm 142.9mm	0mm 109.5mm	×	The installation hall pitch is
	. ,				different.
Weight		0.38kg	0.25kg	0	_

^{*1} The following table lists the measurement range, conversion accuracy, and resolution of AJ65BT-64RD3. The accuracy for measured temperature value is the sum of the conversion accuracy and the tolerance of the RTD.

Measurement range		Conversion accuracy		Resolution
		Ambient temperature: 0 to 55°C	Ambient temperature: 25±5°C	
Pt100 -180 to 600°C		±1.5℃	±0.6℃	0.025℃
JPt100				

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*2 The following table lists the measurement range, conversion accuracy, and resolution of AJ65SBT2B-64RD3. The accuracy for measured temperature value is the sum of the conversion accuracy and the tolerance of the RTD.

Measureme	Measurement range		су	Resolution	
		Ambient temperatu 0 to 55℃	re: Ambient temperature: 25±5°C		
Pt100	-200 to 850℃	±1.4℃	±0.5℃	0.1℃	
	-20 to 120℃	±0.6℃	±0.2℃		
	0 to 200℃				
JPt100	-180 to 600℃	±1.0℃	±0.4℃		
	-20 to 120℃	±0.6℃	±0.2℃		
	0 to 200℃				
Ni100	-60 to 180℃	±0.5°C	±0.2℃		

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■Functional comparison

○: Available, ×: Not available

Item	Description	Model to be discontinued	Alternative model	Precautions for replacement
		AJ65BT-64RD3	AJ65SBT2B-64RD3	
Conversion enable/ disable function	The conversion can be enabled or disabled for each channel using this function. By setting conversion disabled for unnecessary channels, the sampling period shortens.	0	0	_
Sampling processing/ averaging processing specification	The sampling processing or average processing can be specified using this function.	Count average: Not available Time average: Not available Moving average: 4 times	Count average: 1 to 255 times Time average: 160 to 2550ms Moving average: 4 times	The remote I/O signal settings of AJ65BT-64RD3 is changed to the remote register settings of AJ65SBT2B-64RD3.
Disconnection detection function	The disconnection of RTD to connect with each channel is detected using this function. The measured temperature value immediately before the disconnection detection of channel is held.	0	0	For AJ65SBT2B-64RD3, the up scale or down scale value is stored in the measured temperature value of the disconnected channel.
Measured temperature value storage	The measured temperature value rounded off to one decimal place and the measured temperature value rounded off to to three decimal place are stored to the remote register.	0	0	For AJ65SBT2B-64RD3, only the measured temperature value rounded off to one decimal point is stored.
RTD type selection	The RTD type to use is selectable.	(All channels)	(Each channel)	The pin setting of AJ65BT-64RD3 is changed to the remote I/O signal settings of AJ65SBT2B-64RD3.
Offset/gain settings	The offset/gain setting can be set for each channel using this function.	0	0	_
Transmission speed auto-tracking function	When the module is powered on, the transmission speed is set automatically depending on the setting in the master module.	×	0	_
Error flag	The flag is set when an error other than the watchdog timer error occurs.	0	0	_

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■Comparison of remote I/O signals

The remote I/O signals are different. Change the sequence program.

For details, refer to the user's manuals.

n: Address assigned to the master station by station number setting

Signal direct	tion: Remote module $ ightarrow$ N	laster module	Signal directi	on: Master module $ ightarrow$ Rer	note module	
Remote	Signal name		Remote	Signal name		
input (RX)	Model to be discontinued	Alternative model	output (RY)	Model to be discontinued	Alternative model	
	AJ65BT-64RD3	AJ65SBT2B-64RD3	1	AJ65BT-64RD3	AJ65SBT2B-64RD3	
RXn0	CH1 conversion completion	ı flag	RYn0	CH1 conversion enable flag		
RXn1	CH2 conversion completion	ı flag	RYn1	CH2 conversion enable flag		
RXn2	CH3 conversion completion	ı flag	RYn2	CH3 conversion enable flag		
RXn3	CH4 conversion completion	ı flag	RYn3	CH4 conversion enable flag		
RXn4	CH1 disconnection detection	n flag	RYn4	CH1 sampling processing/ travel average processing specification flag		
RXn5	CH2 disconnection detection	n flag	RYn5	CH2 sampling processing/ travel average processing specification flag		
RXn6	CH3 disconnection detection flag CH4 disconnection detection flag		RYn6	CH3 sampling processing/ travel average processing specification flag		
RXn7			RYn7	CH4 sampling processing/ travel average processing specification flag		
RXn8	E ² PROM abnormal flag	Use prohibited	RYn8	Use prohibited	CH1 Input range setting (0th bit)	
RXn9	Test mode flag		RYn9		CH1 Input range setting (1s bit)	
RXnA	Use prohibited	Flash memory read error flag	RYnA		CH1 Input range setting (2nd bit)	
RXnB		User range read error flag	RYnB		CH2 Input range setting (0th bit)	
RXnC		Flash memory write error flag	RYnC		CH2 Input range setting (1s bit)	
RXnD		Use prohibited	RYnD		CH2 Input range setting (2nd bit)	
RXnE			RYnE		CH3 Input range setting (0th bit)	
RXnF		Test mode flag	RYnF		CH3 Input range setting (1s bit)	

Remote	Signal name		Remote	Signal name		
input (RX)	Model to be discontinued	Alternative model	output (RY)	Model to be discontinued	Alternative model	
	AJ65BT-64RD3	AJ65SBT2B-64RD3		AJ65BT-64RD3	AJ65SBT2B-64RD3	
RX(n+1)0	Use prohibited	Use prohibited	RY(n+1)0	Use prohibited	CH3 Input range setting (2nd bit)	
RX(n+1)1			RY(n+1)1		CH4 Measurement range setting 0th bit	
RX(n+1)2			RY(n+1)2		CH4 Measurement range setting 1st bit	
RX(n+1)3			RY(n+1)3		CH4 Measurement range setting 2nd bit	
RX(n+1)4			RY(n+1)4		Disconnection detection upper/lower limit selection flag (all channels batch-select)	
RX(n+1)5			RY(n+1)5		Use prohibited	
RX(n+1)6			RY(n+1)6			
RX(n+1)7			RY(n+1)7		Offset/gain value selection flag	
RX(n+1)8		Initial data processing request flag	RY(n+1)8		Initial data processing completion flag	
RX(n+1)9		Initial data setting completion flag	RY(n+1)9		Initial data setting request flag	
RX(n+1)A		Error status flag	RY(n+1)A		Error reset request flag	
RX(n+1)B		Remote READY	RY(n+1)B		Use prohibited	
RX(n+1)C :		Use prohibited	RY(n+1)C :			
RX(n+1)F			RY(n+1)F			
RX(n+2)0	Use prohibited	Unusable (not assigned because the number of	RY(n+2)0	Use prohibited	Unusable (not assigned because the number of	
: RX(n+7)6		occupied stations is 1)	: RY(n+7)6		occupied stations is 1)	
RX(n+7)7	_		RY(n+7)7	Offset/gain value selection flag		
RX(n+7)8	Initial data processing request flag		RY(n+7)8	Initial data processing completion flag		
RX(n+7)9	Initial data setting completion flag		RY(n+7)9	Initial data setting request flag		
RX(n+7)A	Error status flag		RY(n+7)A	Error reset request flag		
RX(n+7)B	Remote READY		RY(n+7)B	Use prohibited		
RX(n+7)C :	Use prohibited		RY(n+7)C :			
RX(n+7)F			RY(n+7)F			

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■Comparison of remote register

The assignment of remote resisters is changed. Change the sequence program.

For details, refer to the user's manuals.

m, n: Address assigned to the master station by station number setting

Direction	Address	Model to be discontinued		Alternative model		
		AJ65BT-64RD3		AJ65SBT2B-64RD3	AJ65SBT2B-64RD3	
		Description	Default	Description	Default	
Master → Remote	RWwm	Use prohibited	_	CH1 Averaging processing selection	0	
	RWwm+1			CH2 Averaging processing selection	0	
	RWwm+2			CH3 Averaging processing selection	0	
	RWwm+3			CH4 Averaging processing selection	0	
	RWwm+4 : RWwm+F			Unusable (not assigned because the number of occupied stations is 1)		
Remote → Master	RWrn	CH1 Measured temperature value (in units of 0.1°C)	0	CH1 Measured temperature value (in units of 0.1°C)	0	
	RWrn+1	CH2 Measured temperature value (in units of 0.1℃)	0	CH2 Measured temperature value (in units of 0.1℃)	0	
	RWrn+2	CH3 Measured temperature value (in units of 0.1°C)	0	CH3 Measured temperature value (in units of 0.1℃)	0	
	RWrn+3	CH4 Measured temperature value (in units of 0.1℃)	0	CH4 Measured temperature value (in units of 0.1℃)	0	
	RWrn+4	CH1 Measured temperature	0	Unusable (not assigned becau	se the number of	
	RWrn+5	value (in units of 0.001°C)		occupied stations is 1)		
	RWrn+6	CH2 Measured temperature	0			
	RWrn+7	value (in units of 0.001°C)				
	RWrn+8	CH3 Measured temperature	0			
	RWrn+9	value (in units of 0.001°C)				
	RWrn+A	CH4 Measured temperature	0			
	RWrn+B	value (in units of 0.001°C)				
	RWrn+C : RWrn+F	Use prohibited	_			

4-wire type platinum temperature-measuring resistor input (AJ65BT-64RD4)

Replacement from AJ65BT-64RD4 to AJ65SBT2B-64RD3

Model to be discontinued		Alternative model		
Model	Specifications	Model	Specifications	
AJ65BT-64RD4	4-wire type platinum temperature-measuring resistor (Pt100, JPt100) input	AJ65SBT2B-64RD3	3-wire type platinum temperature-measuring resistor (Pt100, JPt100, Ni100) input	

■Performance specifications comparison

 \bigcirc : Compatible, \triangle : Check required, \times : Not compatible

Item	Model to be discontinued	Alternative model	Compatibility	Precautions
	AJ65BT-64RD4	AJ65SBT2B-64RD3	_	for replacement
Measurement method	4-wire	3-wire	Δ	The conductor resistance may affect the measurement value.
Measured temperature value	16-bit signed binary data (-1800 to 6000) 32-bit signed binary data (-180000 to	16-bit signed binary data (-2000 to 8500)	Δ	The 32-bit signed binary data is not available.
	600000)			
Applicable RDT	Pt100 (JIS C 1604-1997, IEC 751- 1983), JPt100 (JIS C 1604-1981)	Pt100 (JIS C 1604-1997, IEC 751- 1983), JPt100 (JIS C 1604-1981), Ni100 (DIN 43760 1987)	0	_
Detection current	1mA		0	_
Measurement range, conversion accuracy, resolution	*1	*2	Δ	The resolution is measured in units of 0.1℃.
Conversion speed	40ms/channel		0	_
Number of temperature input points	4 channels		0	_
Number of flash memory writes	_	10 thousand times maximum	0	_
CC-Link station type	Remote device station		0	_
Number of occupied stations	4 stations	1 station	0	_
Connection cable	CC-Link dedicated cable		0	_
Withstand voltage	Between all power supply system terminals and ground Between all power supply system terminals and all communication system terminals Between all communication system terminals and all RTD inputs Between all RTD inputs and ground 500VAC for 1 minute	Between all power supply and communication system terminals and all RTD inputs 500VAC for 1 minute	0	_
Isolation method	Between communication system terminals and all RTD inputs: Photocoupler Between channels: Non-isolation	Between communication system terminals and all RTD inputs: Photocoupler Between power supply system terminals and all RTD inputs: Transformer isolation Between channels: Non-isolation	0	
Noise immunity	Noise voltage 500Vp-p, noise width 1µs simulator condition)	s, noise frequency 25 to 60Hz (noise	0	_
Built-in terminating resistor	None	Disable/Enable (110Ω): Switchable	0	_
Offset/gain settings	Available		0	_
Disconnection detection	Available		0	-

Item		Model to be discontinued		Compatibility	Precautions	
		AJ65BT-64RD4	AJ65SBT2B-64RD3		for replacement	
External power supply	Voltage	24VDC (18 to 30VDC)	24VDC (20.4 to 28.8VDC)	Δ	Check the output voltage range and the voltage drop for wire length of power supply device to use.	
	Inrush current	_	1.7A, 2.4ms or less	0	_	
	Current consumption	0.17A (at 24VDC)	0.14A (at 24VDC)	0	_	
Cable between module and RTD		100Ω or lower (wiring resistance per conductor)	• 100Ω or lower (wiring resistance per conductor) • Approx. $0.025^{\circ}\text{C}/10\text{m}\Omega$ (influence of resistance difference between A and b connecting conductor on measurement resistance) • 10Ω or lower (resistance difference between A and b connecting conductor)	0	_	
External interface	Communication part, module power supply part	27-point two-piece terminal block (M3.5×7)	7-point two-piece terminal block M3×5.2 screw (tightening torque range: 0.59 to 0.88 N•m) Applicable solderless terminal: 2 or less	×	The screw size is different.	
	I/O part		18-point two-piece terminal block M3×5.2 screw (tightening torque range: 0.59 to 0.88 N•m) Applicable solderless terminal: 2 or less	×		
Applicable wire s	ize	0.75 to 2.0mm (18 to 14 AWG)	0.3 to 2.0mm (22 to 14 AWG)	0	_	
Applicable solderless terminal		RAV1.25-3.5, RAV2-3.5 (compliant with JIS C 2805)	RAV1.25-3 (compliant with JIS C 2805) [Applicable wire size: 0.3 to 1.25mm² (22 to 16 AWG)] V2-MS3 (JST Mfg. Co., Ltd.), RAP2-3SL (Nippon Tanshi Co., Ltd.), TGV2-3N (NICHIFU Co., Ltd.) [Applicable wire size: 1.25 to 2.0mm² (16 to 14 AWG)]	×	The screw size is different. Change the solderless terminals.	
External	Height (H)	65mm	50mm	0	_	
dimensions	Width (W)	151.9mm	122mm	0	_	
	Depth (D)	63mm	54mm	0	_	
Installation hall		φ4.5 (2 places)	4.5×5.1 (2 places)	0	_	
Installation hall Installation hall Height (H)		56mm 142.9mm	0mm 109.5mm	×	The installation hall pitch is	
pitch	Width (W)	142.911111	100.0		different.	

^{*1} The following table lists the measurement range, conversion accuracy, and resolution of AJ65BT-64RD4. The accuracy for measured temperature value is the sum of the conversion accuracy and the tolerance of the RTD.

Measurement range		Conversion accuracy	Resolution	
		Ambient temperature: 0 to 55°C	Ambient temperature: 25±5°C	
Pt100	-180 to 600°C	±1.5℃	±0.6℃	0.025℃
JPt100				

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*2 The following table lists the measurement range, conversion accuracy, and resolution of AJ65SBT2B-64RD3. The accuracy for measured temperature value is the sum of the conversion accuracy and the tolerance of the RTD.

Measuremen	Measurement range		су	Resolution
		Ambient temperatu 0 to 55℃	re: Ambient temperature: 25±5°C	
Pt100 -200 to 850°C ±1.4°C		±1.4℃	±0.5℃	0.1℃
	-20 to 120℃	±0.6℃	±0.2℃	
	0 to 200°C			
JPt100	-180 to 600℃	±1.0℃	±0.4℃	
	-20 to 120℃	±0.6℃	±0.2℃	
	0 to 200℃			
Ni100	-60 to 180℃	±0.5℃	±0.2℃	

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■Functional comparison

○: Available, ×: Not available

Item	Description	Model to be discontinued	Alternative model	Precautions for replacement
		AJ65BT-64RD4	AJ65SBT2B-64RD3	
Conversion enable/ disable function	The conversion can be enabled or disabled for each channel using this function. By setting conversion disabled for unnecessary channels, the sampling period shortens.	0	0	_
Sampling processing/ averaging processing specification	The sampling processing or average processing can be specified using this function.	Count average: Not available Time average: Not available Moving average: 4 times	Count average: 1 to 255 times Time average: 160 to 2550ms Moving average: 4 times	The remote I/O signal settings of AJ65BT-64RD4 is changed to the remote register settings of AJ65SBT2B-64RD3.
Disconnection detection function	The disconnection of RTD to connect with each channel is detected using this function. The measured temperature value immediately before the disconnection detection of channel is held.	0	0	For AJ65SBT2B-64RD3, the up scale or down scale value is stored in the measured temperature value of the disconnected channel.
Measured temperature value storage	The measured temperature value rounded off to one decimal place and the measured temperature value rounded off to to three decimal place are stored to the remote register.	0	0	For AJ65SBT2B-64RD3, only the measured temperature value rounded off to one decimal point is stored.
RTD type selection	The RTD type to use is selectable.	(All channels)	(Each channel)	The pin setting of AJ65BT-64RD4 is changed to the remote I/O signal settings of AJ65SBT2B-64RD3.
Offset/gain settings	The offset/gain setting can be set for each channel using this function.	0	0	_
Transmission speed auto-tracking function	When the module is powered on, the transmission speed is set automatically depending on the setting in the master module.	×	0	_
Error flag	The flag is set when an error other than the watchdog timer error occurs.	0	0	_

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■Comparison of remote I/O signals

The remote I/O signals are different. Change the sequence program.

For details, refer to the user's manuals.

n: Address assigned to the master station by station number setting

Signal direct	tion: Remote module $ ightarrow$ M	laster module	Signal directi	on: Master module $ ightarrow$ Rer	note module	
Remote	Signal name		Remote	Signal name		
input (RX)	Model to be discontinued	Alternative model	output (RY)	Model to be discontinued	Alternative model	
	AJ65BT-64RD4	AJ65SBT2B-64RD3		AJ65BT-64RD4	AJ65SBT2B-64RD3	
RXn0	CH1 conversion completion	flag	RYn0	CH1 conversion enable flag		
RXn1	CH2 conversion completion	flag	RYn1	CH2 conversion enable flag		
RXn2	CH3 conversion completion	flag	RYn2	CH3 conversion enable flag		
RXn3	CH4 conversion completion	flag	RYn3	CH4 conversion enable flag		
RXn4	CH1 disconnection detection	n flag	RYn4	CH1 sampling processing/ travel average processing specification flag	Use prohibited	
RXn5	CH2 disconnection detection	n flag	RYn5	CH2 sampling processing/ travel average processing specification flag		
RXn6	CH3 disconnection detection flag CH4 disconnection detection flag		RYn6	CH3 sampling processing/ travel average processing specification flag		
RXn7			RYn7	CH4 sampling processing/ travel average processing specification flag		
RXn8	E ² PROM abnormal flag	Use prohibited	RYn8	Use prohibited	CH1 Input range setting 0 bit	
RXn9	Test mode flag		RYn9		CH1 Input range setting 1 bit	
RXnA	Use prohibited	Flash memory read error flag	RYnA		CH1 Input range setting 2	
RXnB		User range read error flag	RYnB		CH2 Input range setting 0 bit	
RXnC		Flash memory write error flag	RYnC		CH2 Input range setting 1 bit	
RXnD		Use prohibited	RYnD		CH2 Input range setting 2	
RXnE			RYnE		CH3 Input range setting 0 bit	
RXnF		Test mode flag	RYnF		CH3 Input range setting 1 bit	

Signal direct	tion: Remote module $ ightarrow$ l	Master module	Signal direction: Master module → Remote module			
Remote	Signal name		Remote output (RY)	Signal name		
input (RX)	Model to be discontinued	Alternative model		Model to be discontinued	Alternative model	
	AJ65BT-64RD4	AJ65SBT2B-64RD3		AJ65BT-64RD4	AJ65SBT2B-64RD3	
RX(n+1)0	Use prohibited	Use prohibited	RY(n+1)0	Use prohibited	CH3 Input range setting 2nd bit	
RX(n+1)1			RY(n+1)1		CH4 Measurement range setting 0th bit	
RX(n+1)2			RY(n+1)2		CH4 Measurement range setting 1st bit	
RX(n+1)3			RY(n+1)3		CH4 Measurement range setting 2nd bit	
RX(n+1)4			RY(n+1)4		Disconnection detection upper/lower limit selection flag (all channels batch-select)	
RX(n+1)5			RY(n+1)5		Use prohibited	
RX(n+1)6			RY(n+1)6			
RX(n+1)7			RY(n+1)7		Offset/gain value selection flag	
RX(n+1)8		Initial data processing request flag	RY(n+1)8		Initial data processing completion flag	
RX(n+1)9		Initial data setting completion flag	RY(n+1)9		Initial data setting request flag	
RX(n+1)A		Error status flag	RY(n+1)A		Error reset request flag	
RX(n+1)B		Remote READY	RY(n+1)B		Use prohibited	
RX(n+1)C :		Use prohibited	RY(n+1)C			
RX(n+1)F			RY(n+1)F			
RX(n+2)0	Use prohibited	Unusable (not assigned	RY(n+2)0	Use prohibited	Unusable (not assigned because the number of	
: RX(n+7)6		because the number of occupied stations is 1)	: RY(n+7)6		occupied stations is 1)	
RX(n+7)7	_		RY(n+7)7	Offset/gain value selection flag		
RX(n+7)8	Initial data processing request flag		RY(n+7)8	Initial data processing completion flag		
RX(n+7)9	Initial data setting completion flag		RY(n+7)9	Initial data setting request flag		
RX(n+7)A	Error status flag		RY(n+7)A	Error reset request flag		
RX(n+7)B	Remote READY		RY(n+7)B	Use prohibited		
RX(n+7)C	Use prohibited		RY(n+7)C			
: RX(n+7)F			: RY(n+7)F			

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■Comparison of remote register

The assignment of remote resisters is changed. Change the sequence program.

For details, refer to the user's manuals.

m, n: Address assigned to the master station by station number setting

Direction	Address	ress Model to be discontinued		Alternative model		
		AJ65BT-64RD4		AJ65SBT2B-64RD3		
		Description	Default	Description	Default	
Master → Remote	RWwm	Use prohibited	_	CH1 Averaging processing selection	0	
	RWwm+1			CH2 Averaging processing selection	0	
	RWwm+2			CH3 Averaging processing selection	0	
	RWwm+3			CH4 Averaging processing selection	0	
	RWwm+4 : RWwm+F			Unusable (not assigned becau occupied stations is 1)	se the number of	
Remote → Master	RWrn	CH1 Measured temperature value (in units of 0.1℃)	0	CH1 Measured temperature value (in units of 0.1°C)	0	
	RWrn+1	CH2 Measured temperature value (in units of 0.1°C)	0	CH2 Measured temperature value (in units of 0.1℃)	0	
	RWrn+2	CH3 Measured temperature value (in units of 0.1°C)	0	CH3 Measured temperature value (in units of 0.1℃)	0	
	RWrn+3	CH4 Measured temperature value (in units of 0.1°C)	0	CH4 Measured temperature value (in units of 0.1℃)	0	
	RWrn+4	CH1 Measured temperature	0	Unusable (not assigned because th	se the number of	
	RWrn+5	value (in units of 0.001°C)		occupied stations is 1)		
	RWrn+6	CH2 Measured temperature	0			
	RWrn+7	value (in units of 0.001°C)				
	RWrn+8	CH3 Measured temperature	0			
	RWrn+9	value (in units of 0.001℃)				
	RWrn+A	CH4 Measured temperature	0			
	RWrn+B	value (in units of 0.001℃)				
	RWrn+C : RWrn+F	Use prohibited	_			

Thermocouple input (AJ65BT-68TD)

Replacement from AJ65BT-68TD to AJ65SBT2B-64TD

Model to be discontinued		Alternative model		
Model	Specifications	Model	Specifications	
AJ65BT-68TD	Thermocouple (B, R, S, K, E, J, T) input	AJ65SBT2B-64TD	Thermocouple (B, R, S, K, E, J, T, N) input	

■Performance specifications comparison

 $\bigcirc :$ Compatible, $\triangle :$ Check required, $\times :$ Not compatible

Item Temperature sensor input		Model to be discontinued	Alternative model	Compatibility	Precautions for replacement
		AJ65BT-68TD	AJ65SBT2B-64TD		
		-200 to 1700℃	-270 to 1820℃		
Output	Measured temperature value	16-bit signed binary data (-2000 to 17000)	16-bit signed binary data (-2700 to 18200)	0 –	
	Scaling value	16-bit signed binary data (0 to 2000)	_	×	The equivalent function is available by operation in the sequence program
Cold junction compensation accuracy		±1.0°C (0 to 55°C)	±1.0°C (25°C±10°C) ±1.5°C (0 to 15°C, 35 to 55°C)	Δ	Check the functions using the
Applicable thermocouples and temperature measurement range		*2*3		0	calculation formula of *1 and the tables
Overall accuracy*1		†		Δ	of *2 and *3.
Maximum resolution		B, R, S: 0.3℃ K, E, J, T: 0.1℃	B, R, S, N: 0.3℃ K, E, J, T: 0.1℃	0	_
Conversion speed		45ms/channel	640ms/4 channels	×	The conversion speed is different. Check the specifications.
Sampling cycle		45ms/channel	160ms/4 channels	×	
Absolute maximum input		±5V		0	_
Number of temperature input points		8 channels + Pt100 connection channel	4 channels + Pt100 connection channel × 2	×	When using five or more channels, use two modules.
Number of flash memory writes		_	10000 times maximum	0	_
CC-Link station type		Remote device station		0	_
Number of occupied stations		4 stations	1 station	0	_
Connection cable		CC-Link dedicated cable		0	_
Withstand voltage		Between all power supply system terminals and ground Between all power supply system terminals and all communication system terminals Between all communication system terminals and all thermocouple inputs Between all thermocouple inputs and ground 500VAC for 1 minute	Between all power supply system terminals and all communication system terminals and cold junction compensation channels Between all thermocouple inputs and all communication system terminals and cold junction compensation channels Between thermocouple input channels 500VAC for 1 minute	0	_
Isolation method		Between communication system terminal and all thermocouple inputs: Transformer isolation Between channels: Transformer isolation	Between all power supply system terminals and all communication system terminals and cold junction compensation channels: Transformer isolation Between thermocouple input and all communication system terminals and cold junction compensation channels: Transformer isolation Between thermocouple input channels: Transformer isolation	0	_

Item		Model to be discontinued	Alternative model	Compatibility	Precautions for	
		AJ65BT-68TD	AJ65SBT2B-64TD		replacement	
Noise immunity		Noise voltage 500Vp-p, noise width simulator condition)	1μs, noise frequency 25 to 60Hz (noise	0	_	
Built-in terminati	ng resistor	None	Disable/Enable (110Ω): Switchable	0	_	
Offset/gain settir	ngs	Available	-	0	_	
Disconnection de	etection	Available		0	_	
External power supply	, , , , , , , , , , , , , , , , , , , ,		Δ	Check the output voltage range and the voltage drop for wire length of power supply device to use.		
	Inrush current	_	1.5A, 1.3ms or less	0	_	
	Current consumption	0.081A (at 24VDC)	0.12A (at 24VDC)	Δ	The current consumption is higher. Check that the current consumption does not affect the system.	
External interface	Communication part, module power supply part	27-point two-piece terminal block (M3.5×7)	7-point two-piece terminal block M3×5.2 screw (tightening torque range: 0.59 to 0.88 N•m) Applicable solderless terminal: 2 or less	×	The screw size is different.	
	I/O part		18-point two-piece terminal block M3×5.2 screw (tightening torque range: 0.59 to 0.88 N•m) Applicable solderless terminal: 2 or less	×		
Applicable wire s	size	0.75 to 2.0mm (18 to 14 AWG)	0.3 to 2.0mm (22 to 14 AWG)	0	_	
Applicable solderless terminal		RAV1.25-3.5, RAV2-3.5	RAV1.25-3 (compliant with JIS C 2805) [Applicable wire size: 0.3 to 1.25mm² (22 to 16 AWG)] V2-MS3 (JST Mfg. Co., Ltd.), RAP2-3SL (Nippon Tanshi Co., Ltd.), TGV2-3N (NICHIFU Co., Ltd.) [Applicable wire size: 1.25 to 2.0mm² (16 to 14 AWG)]	×	The screw size is different. Change the solderless terminals.	
External	Height (H)	65mm	50mm	0	_	
dimensions	Width (W)	151.9mm	122mm	0	_	
	Depth (D)	63mm	54mm	0	_	
Installation hall	•	φ4.5 (2 places)	4.5×5.1 (2 places)	0	_	
Installation hall	Height (H)	56mm	0mm	×	The installation hall	
pitch	Width (W)	142.9mm	109.5mm	×	pitch is different.	
Weight		0.4kg	0.3kg	0	_	

^{*1 (}Overall accuracy) = (Conversion accuracy) + (Temperature characteristics) × (Operating ambient temperature variation) + (Cold junction compensation accuracy)

Operating ambient temperature variation means the value out of operating ambient temperature range of $25\pm5^{\circ}$ C.

^{*2} The following table lists the applicable thermocouples and measured temperature range accuracy. The thermocouples are compliant with JIS C 1602-1995.

Thermocouple	Measured	Model to be di	scontinued	Alternative model		Compatibility	Precautions
type	temperature	AJ65BT-68TD		AJ65SBT2B-64	TD		for
	range	Conversion accuracy Ambient temperature: 25±5°C	Temperature characteristics Variation per ambient temperature of 1°C	Conversion accuracy Ambient temperature: 25±5℃	Temperature characteristics Variation per ambient temperature of 1°C		replacement
В	600 to 1700℃	±2.5℃	±0.4℃	±2.5℃	±0.4℃	0	_
R	0 to 200℃	±2.0℃	±0.4℃	±2.0℃	±0.4℃	0	_
	200 to 1600°C		±0.3℃			Δ	Check the overall accuracy depending on the temperature characteristics and operating ambient temperature variation.
S	0 to 200°C	±2.0℃	±0.4℃	±2.0℃	±0.4℃	0	_
	200 to 1600°C		±0.3℃			Δ	Check the overall accuracy depending on the temperature characteristics and operating ambient temperature variation.
К	-200 to 0℃	±0.5℃ or ±0.25% of measured temperature, whichever is	±0.06°C or ±0.3% of measured temperature, whichever is greater	±0.5°C or ±0.25% of measured temperature, whichever is greater	±0.06°C or ±0.3% of measured temperature, whichever is greater	0	_
	0 to 1200℃	greater			±0.06°C or ±0.02% of measured temperature, whichever is greater		
E	-200 to 0℃	±0.5℃ or ±0.25% of measured temperature, whichever is	±0.06°C or ±0.3% of measured temperature, whichever is greater	±0.5℃ or ±0.25% of measured temperature, whichever is greater	±0.06°C or ±0.3% of measured temperature, whichever is greater	0	_
	0 to 800°C	greater	±0.06°C or ±0.02% of measured temperature, whichever is greater		±0.06℃ or ±0.02% of measured temperature, whichever is greater		
	800 to 900°C	_	_				
J	-40 to 0℃ 0 to 750℃	±0.5°C or ±0.25% of measured temperature, whichever is greater	±0.06℃ or ±0.02% of measured temperature, whichever is greater	±0.5°C or ±0.25% of measured temperature, whichever is greater	±0.06℃ or ±0.02% of measured temperature, whichever is greater	0	_

Thermocouple	Measured	Model to be di	scontinued	Alternative mod	del	Compatibility	Precautions
type	temperature	AJ65BT-68TD		AJ65SBT2B-64TD			for replacement
	range	Conversion accuracy Ambient temperature: 25±5°C	Temperature characteristics Variation per ambient temperature of 1°C	Conversion accuracy Ambient temperature: 25±5℃	Temperature characteristics Variation per ambient temperature of 1°C		replacement
Т	-200 to 0℃	±0.5°C or ±0.25% of measured temperature, whichever is	±0.06°C or ±0.3% of measured temperature, whichever is greater	±0.5°C or ±0.25% of measured temperature, whichever is greater	±0.06°C or ±0.3% of measured temperature, whichever is greater	0	_
	0 to 350°C	greater	±0.06°C or ±0.02% of measured temperature, whichever is greater		±0.06°C or ±0.02% of measured temperature, whichever is greater		
N	-200 to 0℃	No setting		±0.5°C or ±0.25% of measured temperature, whichever is greater	±0.06°C or ±0.3% of measured temperature, whichever is greater	_	_
	0 to 1250℃				±0.06°C or ±0.02% of measured temperature, whichever is greater		

^{*3} The following table lists the applicable thermocouples and effect by wiring resistance of 1Ω . The thermocouples are compliant with JIS C 1602-1995.

Thermocouple	Measured	Effect by wiring resistance of	Effect by wiring resistance of 1 Ω		
type	temperature	Model to be discontinued	Alternative model		replacement
	range	AJ65BT-68TD	AJ65SBT2B-64TD		
В	600 to 1700℃	0.019℃/Ω	0.042°C/Ω	Δ	The effect by wiring
R	0 to 200°C	0.023℃/Ω	0.050°C/Ω	Δ	resistance is larger. Check the wiring
	200 to 1600℃	0.015℃/Ω			resistance.
S	0 to 200°C	0.023℃/Ω	0.050°C/Ω	Δ	
	200 to 1600℃	0.015℃/Ω			
К	-200 to 0°C	0.008℃/Ω	0.009°C/Ω	Δ	
	0 to 1200℃	0.003℃/Ω	0.007°C/Ω		
E	-200 to 0°C	0.005℃/Ω	0.006℃/Ω	Δ	
	0 to 800°C	0.002°C/Ω	0.005℃/Ω		
	800 to 900℃	_		_	_
J	-40 to 0°C	_	0.006℃/Ω	_	_
	0 to 750°C	0.003℃/Ω		Δ	The effect by wiring
Т	-200 to 0°C	0.008℃/Ω	0.009℃/Ω	Δ	resistance is larger.
	0 to 350°C	0.003℃/Ω	0.007℃/Ω	Check the wiring resistance.	
N	-200 to 0°C	No setting	0.012℃/Ω	_	_
	0 to 1250℃	1	0.010℃/Ω]	

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■Functional comparison

○: Available, ×: Not available

Item	Description	Model to be discontinued	Alternative model	Precautions for replacement	
		AJ65BT-68TD	AJ65SBT2B-64TD		
Conversion enable/ disable function	The conversion can be enabled or disabled for each channel using this function. By setting conversion disabled for unnecessary channels, the sampling period shortens.	0	0	The sampling period of AJ65SBT2B-64TD does not shorten by setting conversion disabled for unnecessary channels.	
Sampling processing/ averaging processing specification	The sampling processing or average processing can be specified using this function.	Count average: Not available Time average: Not available Moving average: 4 times	Count average: 3 to 255 times Time average: 480 to 2550ms Moving average: 4 times	The remote I/O signal settings of AJ65BT-68TD is changed to the remote register settings of AJ65SBT2B-64TD.	
Disconnection detection function	The disconnection of thermocouple to connect with each channel is detected using this function. The measured temperature value immediately before the disconnection detection of channel is held.	0	0	For AJ65SBT2B-64TD, the up scale or down scale value is stored in the measured temperature value of the disconnected channel.	
Measured temperature value storage	The measured temperature value rounded off to one decimal place is stored to the remote register.	0	0	_	
Upper/lower limit value of measured temperature setting function	The upper/lower limit value of measured temperature is set for each channel using this function	0	×	The equivalent function is available by operation in the sequence program.	
Scaling function	The temperature measured value is increase/decrease within the upper/ lower limit value up to 0 to 2000 using this function.	0	×	The equivalent function is available by operation in the sequence program.	
Thermocouple type selection	The thermocouple type to use is set for channel using this function.	0	0	_	
Offset/gain settings	The offset/gain setting can be set for each channel using this function.	0	0	_	
Pt100 cold junction compensation enable/ disable function	The Pt100 cold junction compensation can be enabled or disabled using this function.	0	0	_	
Transmission speed auto-tracking function	When the module is powered on, the transmission speed is set automatically depending on the setting in the master module.	×	0	_	
Error flag	The flag is set when an error other than the watchdog timer error occurs.	0	0	_	

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■Comparison of remote I/O signals

The remote I/O signals are different. Change the sequence program.

For details, refer to the user's manuals.

n: Address assigned to the master station by station number setting

Signal un	rection: Remote module → Mas	ster module	Signal dii	rection: Master module → Rem	ote module
Remote	Signal name		Remote	Signal name	
input	Model to be discontinued	Alternative model	output	Model to be discontinued	Alternative model
(RX)	AJ65BT-68TD	AJ65SBT2B-64TD	(RY)	AJ65BT-68TD	AJ65SBT2B-64TD
RXn0	CH1 conversion completion flag	,	RYn0	CH1 conversion enable flag	'
RXn1	CH2 conversion completion flag		RYn1	CH2 conversion enable flag	
RXn2	CH3 conversion completion flag		RYn2	CH3 conversion enable flag	
RXn3	CH4 conversion completion flag		RYn3	CH4 conversion enable flag	
RXn4	CH5 conversion completion flag	CH1 Disconnection detection flag	RYn4	CH5 conversion enable flag	Use prohibited
RXn5	CH6 conversion completion flag	CH2 Disconnection detection flag	RYn5	CH6 conversion enable flag	
RXn6	CH7 conversion completion flag	CH3 Disconnection detection flag	RYn6	CH7 conversion enable flag	
RXn7	CH8 conversion completion flag	CH4 Disconnection detection flag	RYn7	CH8 conversion enable flag	
RXn8	CH1 Disconnection detection flag	Use prohibited	RYn8	CH1 sampling/travel average processing specification designation flag	CH1 Thermocouple selection bit 0
RXn9	CH2 disconnection detection flag		RYn9	CH2 sampling/travel average processing specification designation flag	CH1 Thermocouple selection bit 1
RXnA	CH3 disconnection detection flag	Flash memory read error flag	RYnA	CH3 sampling/travel average processing specification designation flag	CH1 Thermocouple selection bit 2
RXnB	CH4 disconnection detection flag	User range read error flag	RYnB	CH4 sampling/travel average processing specification designation flag	CH2 Thermocouple selection bit 0
RXnC	CH5 disconnection detection flag	Flash memory write error flag	RYnC	CH5 sampling/travel average processing specification designation flag	CH2 Thermocouple selection bit 1
RXnD	CH6 disconnection detection flag	Use prohibited	RYnD	CH6 sampling/travel average processing specification designation flag	CH2 Thermocouple selection bit 2
RXnE	CH7 disconnection detection flag		RYnE	CH7 sampling/travel average processing specification designation flag	CH3 Thermocouple selection bit 0
RXnF	CH8 disconnection detection flag	Test mode flag	RYnF	CH8 sampling/travel average processing specification designation flag	CH3 Thermocouple selection bit 1

Signal dir	rection: Remote module $ ightarrow$ Ma	ster module	Signal dir	Signal direction: Master module → Remote module			
Remote	Signal name		Remote	Signal name			
input	Model to be discontinued	Alternative model	output	Model to be discontinued	Alternative model		
(RX)	AJ65BT-68TD	AJ65SBT2B-64TD	(RY)	AJ65BT-68TD	AJ65SBT2B-64TD		
RX(n+1)0	CH1 measurement range over flag (lower limit)	Use prohibited	RY(n+1)0	CH1 type "K" thermocouple selection flag	CH3 Thermocouple selection bit 2		
RX(n+1)1	CH1 measurement range over flag (upper limit)		RY(n+1)1	CH1 type "E" thermocouple selection flag	CH4 Thermocouple selection bit 0		
RX(n+1)2	CH2 measurement range over flag (lower limit)		RY(n+1)2	CH1 type "J" thermocouple selection flag	CH4 Thermocouple selection bit 1		
RX(n+1)3	CH2 measurement range over flag (upper limit)		RY(n+1)3	CH1 type "T" thermocouple selection flag	CH4 Thermocouple selection bit 2		
RX(n+1)4	CH3 measurement range over flag (lower limit)		RY(n+1)4	CH1 type "B" thermocouple selection flag	Converted value at disconnection detection selection flag (all channel batch-select)		
RX(n+1)5	CH3 measurement range over flag (upper limit)		RY(n+1)5	CH1 type "R" thermocouple selection flag	Use prohibited		
RX(n+1)6	CH4 measurement range over flag (lower limit)		RY(n+1)6	CH1 type "S" thermocouple selection flag	Pt100 cold junction compensation disable flag		
RX(n+1)7	CH4 measurement range over flag (upper limit)		RY(n+1)7	Use prohibited	Offset/gain value selection flag		
RX(n+1)8	CH5 measurement range over flag (lower limit)	Initial data processing request flag	RY(n+1)8	CH2 type "K" thermocouple selection flag	Initial data processing completion flag		
RX(n+1)9	CH5 measurement range over flag (upper limit)	Initial data setting completion flag	RY(n+1)9	CH2 type "E" thermocouple selection flag	Initial data setting request		
RX(n+1)A	CH6 measurement range over flag (lower limit)	Error status flag	RY(n+1)A	CH2 type "J" thermocouple selection flag	Error reset request flag		
RX(n+1)B	CH6 measurement range over flag (upper limit)	Remote READY	RY(n+1)B	CH2 type "T" thermocouple selection flag	Use prohibited		
RX(n+1)C	CH7 measurement range over flag (lower limit)	Use prohibited	RY(n+1)C	CH2 type "B" thermocouple selection flag			
RX(n+1)D	CH7 measurement range over flag (upper limit)		RY(n+1)D	CH2 type "R" thermocouple selection flag			
RX(n+1)E	CH8 measurement range over flag (lower limit)		RY(n+1)E	CH2 type "S" thermocouple selection flag			
RX(n+1)F	CH8 measurement range over flag (upper limit)		RY(n+1)F	Use prohibited			

Signal dir	rection: Remote module → Ma	ster module	Signal dir	rection: Master module $ ightarrow$ Rem	ote module
Remote	Signal name		Remote	Signal name	
input	Model to be discontinued	Alternative model	output	Model to be discontinued	Alternative model
(RX)	AJ65BT-68TD	AJ65SBT2B-64TD	(RY)	AJ65BT-68TD	AJ65SBT2B-64TD
RX(n+2)0	CH1 write data error flag	Unusable (not assigned because the number of	RY(n+2)0	CH3 type "K" thermocouple selection flag	Unusable (not assigned because the number of
RX(n+2)1	CH2 write data error flag	occupied stations is 1)	RY(n+2)1	CH3 type "E" thermocouple selection flag	occupied stations is 1)
RX(n+2)2	CH3 write data error flag		RY(n+2)2	CH3 type "J" thermocouple selection flag	
RX(n+2)3	CH4 write data error flag		RY(n+2)3	CH3 type "T" thermocouple selection flag	
RX(n+2)4	CH5 write data error flag		RY(n+2)4	CH3 type "B" thermocouple selection flag	
RX(n+2)5	CH6 write data error flag		RY(n+2)5	CH3 type "R" thermocouple selection flag	
RX(n+2)6	CH7 write data error flag		RY(n+2)6	CH3 type "S" thermocouple selection flag	
RX(n+2)7	CH8 write data error flag		RY(n+2)7	Use prohibited	
RX(n+2)8	E ² PROM abnormal flag		RY(n+2)8	CH4 type "K" thermocouple selection flag	
RX(n+2)9	Test mode flag		RY(n+2)9	CH4 type "E" thermocouple selection flag	
RX(n+2)A	Use prohibited		RY(n+2)A	CH4 type "J" thermocouple selection flag	
RX(n+2)B			RY(n+2)B	CH4 type "T" thermocouple selection flag	
RX(n+2)C			RY(n+2)C	CH4 type "B" thermocouple selection flag	
RX(n+2)D			RY(n+2)D	CH4 type "R" thermocouple selection flag	
RX(n+2)E			RY(n+2)E	CH4 type "S" thermocouple selection flag	
RX(n+2)F			RY(n+2)F	Use prohibited	
RX(n+3)0			RY(n+3)0	CH5 type "K" thermocouple selection flag	
RX(n+3)1			RY(n+3)1	CH5 type "E" thermocouple selection flag	
RX(n+3)2			RY(n+3)2	CH5 type "J" thermocouple selection flag	
RX(n+3)3			RY(n+3)3	CH5 type "T" thermocouple selection flag	
RX(n+3)4			RY(n+3)4	CH5 type "B" thermocouple selection flag	
RX(n+3)5			RY(n+3)5	CH5 type "R" thermocouple selection flag	
RX(n+3)6			RY(n+3)6	CH5 type "S" thermocouple selection flag	
RX(n+3)7	1		RY(n+3)7	Use prohibited	

Signal dir	ection: Remote module $ ightarrow$ Ma	ster module	Signal dir	ection: Master module $ ightarrow$ Rem	ote module
Remote	Signal name		Remote	Signal name	
input	Model to be discontinued	Alternative model	output	Model to be discontinued	Alternative model
(RX)	AJ65BT-68TD	AJ65SBT2B-64TD	(RY)	AJ65BT-68TD	AJ65SBT2B-64TD
RX(n+3)8	Use prohibited	Unusable (not assigned because the number of	RY(n+3)8	CH6 type "K" thermocouple selection flag	Unusable (not assigned because the number of
RX(n+3)9		occupied stations is 1)	RY(n+3)9	CH6 type "E" thermocouple selection flag	occupied stations is 1)
RX(n+3)A			RY(n+3)A	CH6 type "J" thermocouple selection flag	
RX(n+3)B			RY(n+3)B	CH6 type "T" thermocouple selection flag	
RX(n+3)C			RY(n+3)C	CH6 type "B" thermocouple selection flag	
RX(n+3)D			RY(n+3)D	CH6 type "R" thermocouple selection flag	
RX(n+3)E			RY(n+3)E	CH6 type "S" thermocouple selection flag	
RX(n+3)F			RY(n+3)F	Use prohibited	
RX(n+4)0			RY(n+4)0	CH7 type "K" thermocouple selection flag	
RX(n+4)1			RY(n+4)1	CH7 type "E" thermocouple selection flag	
RX(n+4)2			RY(n+4)2	CH7 type "J" thermocouple selection flag	
RX(n+4)3			RY(n+4)3	CH7 type "T" thermocouple selection flag	
RX(n+4)4			RY(n+4)4	CH7 type "B" thermocouple selection flag	
RX(n+4)5			RY(n+4)5	CH7 type "R" thermocouple selection flag	
RX(n+4)6			RY(n+4)6	CH7 type "S" thermocouple selection flag	
RX(n+4)7			RY(n+4)7	Use prohibited	
RX(n+4)8			RY(n+4)8	CH8 type "K" thermocouple selection flag	
RX(n+4)9			RY(n+4)9	CH8 type "E" thermocouple selection flag	
RX(n+4)A			RY(n+4)A	CH8 type "J" thermocouple selection flag	
RX(n+4)B			RY(n+4)B	CH8 type "T" thermocouple selection flag	
RX(n+4)C			RY(n+4)C	CH8 type "B" thermocouple selection flag	
RX(n+4)D			RY(n+4)D	CH8 type "R" thermocouple selection flag	
RX(n+4)E			RY(n+4)E	CH8 type "S" thermocouple selection flag	
RX(n+4)F			RY(n+4)F	Use prohibited	

Signal dir	Signal direction: Remote module → Master module		Signal direction: Master module → Remote module			
Remote	Signal name		Remote	Signal name		
input	Model to be discontinued	Alternative model	output	Model to be discontinued	Alternative model	
(RX)	AJ65BT-68TD	AJ65SBT2B-64TD	(RY)	AJ65BT-68TD	AJ65SBT2B-64TD	
RX(n+5)0	Use prohibited	Unusable (not assigned because the number of	RY(n+5)0	All CH batch type "K" thermocouple selection flag	Unusable (not assigned because the number of	
RX(n+5)1		occupied stations is 1)	RY(n+5)1	All CH batch type "E" thermocouple selection flag	occupied stations is 1)	
RX(n+5)2			RY(n+5)2	All CH batch type "J" thermocouple selection flag		
RX(n+5)3			RY(n+5)3	All CH batch type "T" thermocouple selection flag		
RX(n+5)4			RY(n+5)4	All CH batch type "B" thermocouple selection flag		
RX(n+5)5			RY(n+5)5	All CH batch type "R" thermocouple selection flag		
RX(n+5)6			RY(n+5)6	All CH batch type "S" thermocouple selection flag		
RX(n+5)7			RY(n+5)7	Pt100 cold junction compensation disable flag		
RX(n+5)8			RY(n+5)8	Use prohibited		
: RX(n+7)6			: RY(n+7)6			
RX(n+7)7	1		RY(n+7)7	Offset/gain value selection flag	1	
RX(n+7)8	Initial data processing request flag		RY(n+7)8	Initial data processing completion flag		
RX(n+7)9	Initial data setting completion flag		RY(n+7)9	Initial data setting request flag		
RX(n+7)A	Error status flag		RY(n+7)A	Error reset request flag		
RX(n+7)B	Remote READY		RY(n+7)B	Use prohibited	1	
RX(n+7)C :	Use prohibited		RY(n+7)C :			
RX(n+7)F			RY(n+7)F			

■Comparison of remote register

The assignment of remote resisters is changed. Change the sequence program.

For details, refer to the user's manuals.

m, n: Address assigned to the master station by station number setting

Direction	Address	Model to be discontinued		Alternative model		
		AJ65BT-68TD		AJ65SBT2B-64TD		
		Description	Default	Description	Default	
Master → Remote	RWwm	CH1 lower limit value (0.1°C)	Measured temperature	CH1 Averaging processing setting	0	
	RWwm+1	CH1 upper limit value (0.1°C)	range of selected thermocouple	CH2 Averaging processing setting	0	
	RWwm+2	CH2 lower limit value (0.1℃)		CH3 Averaging processing setting	0	
	RWwm+3	CH2 upper limit value (0.1°C)		CH4 Averaging processing setting	0	
	RWwm+4	CH3 lower limit value (0.1°C)		Unusable (not assigned becau	se the number of	
	RWwm+5	CH3 upper limit value (0.1℃)		occupied stations is 1)		
	RWwm+6	CH4 lower limit value (0.1°C)				
	RWwm+7	CH4 upper limit value (0.1℃)				
	RWwm+8	CH5 lower limit value (0.1°C)				
	RWwm+9	CH5 upper limit value (0.1℃)				
	RWwm+A	CH6 lower limit value (0.1°C)				
	RWwm+B	CH6 upper limit value (0.1℃)	-			
	RWwm+C	CH7 lower limit value (0.1°C)				
	RWwm+D	CH7 upper limit value (0.1℃)				
	RWwm+E	CH8 lower limit value (0.1°C)				
	RWwm+F	CH8 upper limit value (0.1℃)				
Remote → Master	RWrn	CH1 Measured temperature value (in units of 0.1℃)	0	CH1 Measured temperature value (in units of 0.1°C)	0	
	RWrn+1	CH2 Measured temperature value (in units of 0.1℃)	0	CH2 Measured temperature value (in units of 0.1°C)	0	
	RWrn+2	CH3 Measured temperature value (in units of 0.1°C)	0	CH3 Measured temperature value (in units of 0.1°C)	0	
	RWrn+3	CH4 Measured temperature value (in units of 0.1℃)	0	CH4 Measured temperature value (in units of 0.1°C)	0	
	RWrn+4	CH5 Measured temperature value (in units of 0.1℃)	0	Unusable (not assigned becau occupied stations is 1)	se the number of	
	RWrn+5	CH6 Measured temperature value (in units of 0.1℃)	0			
	RWrn+6	CH7 Measured temperature value (in units of 0.1℃)	0			
	RWrn+7	CH8 Measured temperature value (in units of 0.1℃)	0			
	RWrn+8	CH1 scaling value	0			
	RWrn+9	CH2 scaling value	0			
	RWrn+A	CH3 scaling value	0			
	RWrn+B	CH4 scaling value	0	1		
	RWrn+C	CH5 scaling value	0	1		
	RWm+D	CH6 scaling value	0	1		
	RWm+E	CH7 scaling value	0	1		
	RWrn+F	CH8 scaling value	0	-		

6.5 High-speed Counter Module

AJ65BT-D62D-S1

Model to be discontinued		Alternative model		
Model Specifications		Model	Specifications	
AJ65BT-D62D-S1	Differential input/sink output (preset: differential input)	AJ65BT-D62D	Differential input/sink output (preset: DC input)	

■Performance specifications comparison

 $\bigcirc :$ Compatible, $\triangle :$ Check required, $\times :$ Not compatible

Item			Model to be discontinued	Alternative model	Compatibility	Precautions	
			AJ65BT-D62D-S1		for replacement		
Number of cha	nnole		2 channels	0	_		
Count input Phase		1-phase input, 2-phase input	0	_			
signal	Signal level		EIA Standard RS-422-A differential	type line driver level (equivalent to	0		
	(φA, φB)		AM26LS31 (Japan Texas Instrume	*	O		
Counter	Counting speed	1-phase input	Counting speed setting switch of HIGH position: 400kpps Counting speed setting switch of LOW position: 10kpps		0	_	
	(max.)	2-phase input	Counting speed setting switch of H Counting speed setting switch of Lo				
	Counting ra	nge	24-bit signed binary data (0 to 1677	77215)			
	Туре		UP/DOWN preset counter and ring	counter functions			
Coincidence	Comparison	range	24-bit signed binary data		0	_	
output	Comparison	ı result	Set value < count value, set value : value	= count value, set value > count			
External input	Preset		EIA Standard RS-422-A differential type line driver level (equivalent to AM26LS31 (Japan Texas Instruments make))		×	The input type is different.*1	
	Function start		5/12/24VDC, 2 to 5mA	0	_		
	Response	$OFF \rightarrow ON$	0.5ms or less		0	_	
	time	$ON \rightarrow OFF$	3ms or less				
External	Coincidence	e output	2A/common		0	_	
output	Response time		0.1ms or less				
CC-Link station	n type		Remote device station	0	_		
Number of occ	upied stations	;	4 stations	0	_		
Connection cal	ble		CC-Link dedicated cable	0	_		
Withstand volta	age		Between all DC external terminals 500VAC for 1 minute	0	_		
insulation resis	tance		Between all DC external terminals a $10M\Omega$ or higher (500VDC insulation	0	_		
Noise immunity	/		Noise voltage 500Vp-p, noise width (noise simulator condition)	0	_		
Terminal block			27-point two-piece terminal block (I	0	_		
Applicable wire size			0.75 to 2.0mm (18 to 14 AWG)	0	_		
Applicable solderless terminal			RAV1.25-3, RAV2-3.5 (compliant w	0	_		
Module mounting screws			Screws of M4×0.7mm×16mm or lar 1.18N•m)	0	_		
Applicable DIN	rail		TH35-7.5Fe, TH35-7.5Al, TH35-15 60715)	0	_		
External	Voltage		18 to 28.8VDC		0	_	
power supply	Current consumption		120mA (at 24VDC)	C) 100mA (at 24VDC)		_	
Permissible ins	tantaneous p	ower failure time	1ms		0	_	

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Item		Model to be discontinued AJ65BT-D62D-S1			Precautions for replacement
External	Height (H)	63mm	63mm		_
dimensions	Width (W)	151.9mm	151.9mm		
	Depth (D)	65mm	65mm		
Weight		0.42kg	0.42kg		_

^{*1} The input type of external input (preset) is different. Check the specifications of sensors to connect. Take measures such as converting differential signals to DC signals with a signal converter.

The transmission time between the differential input and DC input may be different. Verify the operation in the system.

■Functional comparison

 \bigcirc : Available, \times : Not available

Item		Description	Model to be discontinued	Alternative model	Precautions for
			AJ65BT-D62D-S1	AJ65BT-D62D	replacement
Coincidence outpu	t function	Outputs an ON/OFF signal in a specified output status, comparing it with the present value.	0	0	_
Preset function		Counting alternates between the preset value and the ring counter value. The preset operation can be done either by a sequence program or by an external preset input.	0	0	_
Ring counter function		Counting alternates between the preset value and the ring counter.	0	0	_
Counter function selection	Count disable function	Stops counting pulses while the count disable command is ON.	0	0	_
	Latch counter function	Stores the present value of the counter into the remote register when the signal of the counter function selection start command is input.	0	0	_
	Sampling counter function	Stores the present value of the counter into the remote register when the signal of the counter function selection start command is input.	0	0	_
	Periodical pulse counter function	Stores the number of input pulses into the remote register every preset cycle time when the signal of the counter function selection start command is input.	0	0	_

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■Comparison of remote I/O signals

All the remote I/O signals are the same.

For details, refer to the user's manuals.

n: Address assigned to the master station by station number setting

Signal direct	ote module → Master	module	Signal direction: Master module → Remote module					
Remote	Signal	Signal name			Signal	name		
input (RX)	СН	Model to be discontinued	Alternative model	output (RY)	СН	Model to be discontinued	Alternative model	
		AJ65BT-D62D-S1	AJ65BT-D62D			AJ65BT-D62D-S1	AJ65BT-D62D	
RXn0	CH1	Counter value greater (p	point No. 1)	RYn0	<u> </u>	Use prohibited		
RXn1		Counter value coincider	nce (point No. 1)	RYn1	1			
RXn2		Counter value less (poir	nt No. 1)	RYn2	1			
RXn3		External preset commar	nd detection	RYn3	1			
RXn4	CH2	Counter value greater (p	RYn4	†				
RXn5		Counter value coincider	nce (point No. 1)	RYn5	1			
RXn6		Counter value less (poir	nt No. 1)	RYn6	1			
RXn7		External preset commar	nd detection	RYn7	1			
RXn8	CH1	Counter value greater (p	point No. 2)	RYn8	†			
RXn9		Counter value coincider	nce (point No. 2)	RYn9	1			
RXnA		Counter value less (poir	nt No. 2)	RYnA	1			
RXnB	CH2	Counter value greater (p	point No. 2)	RYnB	†			
RXnC		Counter value coincider	nce (point No. 2)	RYnC	1			
RXnD		Counter value less (point No. 2)		RYnD	1			
RXnE	_	Use prohibited		RYnE	1			
RXnF				RYnF	1			
RX(n+1)0	CH1	Preset completion	RY(n+1)0	CH1	Point No. 1 coincidence	signal reset command		
RX(n+1)1		Counter function detecti	RY(n+1)1	1	Preset command			
RX(n+1)2	CH2	Preset completion	RY(n+1)2	7	Coincidence signal enable			
RX(n+1)3		Counter function detecti	RY(n+1)3	1	Down count command			
RX(n+1)4	_	Use prohibited	RY(n+1)4	1	Count enable			
RX(n+1)5				RY(n+1)5	1	Use prohibited		
RX(n+1)6				RY(n+1)6	1	Counter function selection start command		
RX(n+1)7				RY(n+1)7	CH2	Point No. 1 coincidence signal reset command		
RX(n+1)8				RY(n+1)8	1	Preset command		
RX(n+1)9				RY(n+1)9	1	Coincidence signal ena	ble	
RX(n+1)A				RY(n+1)A	1	Down count command		
RX(n+1)B				RY(n+1)B	1	Count enable		
RX(n+1)C				RY(n+1)C	1	Use prohibited		
RX(n+1)D				RY(n+1)D	1	Counter function selecti	on start command	
RX(n+1)E				RY(n+1)E	_	Use prohibited		
RX(n+1)F				RY(n+1)F	1			
RX(n+2)0				RY(n+2)0	CH1	External preset detection	n reset command	
RX(n+2)1				RY(n+2)1		Point No. 2 coincidence	signal reset command	
RX(n+2)2			RY(n+2)2	CH2	External preset detection	n reset command		
RX(n+2)3				RY(n+2)3		Point No. 2 coincidence	signal reset command	
RX(n+2)4				RY(n+2)4	_	Use prohibited		
: RX(n+7)7				: RY(n+7)7				
RX(n+7)8	_	Initial data processing re	equest flag	RY(n+7)8	<u> </u>	Initial data processing of	ompletion flag	

Signal directi	Signal direction: Remote module \rightarrow Master module					Signal direction: Master module → Remote module			
Remote	Remote Signal name		Remote Signal name						
input (RX)	СН	Model to be discontinued	Alternative model output (RY)		СН	Model to be discontinued	Alternative model		
		AJ65BT-D62D-S1	AJ65BT-D62D			AJ65BT-D62D-S1	AJ65BT-D62D		
RX(n+7)9	_	Use prohibited	RY(n+7)9	_	Use prohibited				
RX(n+7)A	1-	Use prohibited		RY(n+7)A				1	
RX(n+7)B	<u> </u>	Remote READY		RY(n+7)B				1	
RX(n+7)C	_	Use prohibited		RY(n+7)C	1				
: RX(n+7)F				: RY(n+7)F					

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■Comparison of remote register

The assignment of remote register is the same.

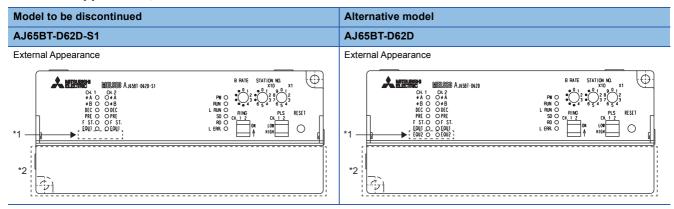
For details, refer to the user's manuals.

m, n: Address assigned to the master station by station number setting

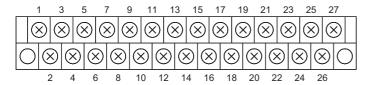
Direction	Address	Model to be discontinued			Alternative model			
		AJ65BT-D62D-S1			AJ65BT-D62D			
		СН	Description	Default	СН	Description	Default	
Master → Remote	RWwm	CH1	Preset value setting area	0	CH1	Preset value setting area	0	
	RWwm+1	7						
	RWwm+2	_	Pulse input mode/function selection register/external output hold or	0		Pulse input mode/function selection register/external output hold or	0	
		_	clear setting area			clear setting area		
	RWwm+3		Coincidence output point No. 1	0		Coincidence output point No. 1	0	
	RWwm+4	_	setting area			setting area		
	RWwm+5		Sampling/cycle time setting area	0		Sampling/cycle time setting area	0	
	RWwm+6		Coincidence output point No. 2	0		Coincidence output point No. 2	0	
	RWwm+7		setting area			setting area		
	RWwm+8	CH2	Preset value setting area	0	CH2	Preset value setting area	0	
	RWwm+9							
	RWwm+A		Pulse input mode/function selection register/external output hold or clear setting area	0		Pulse input mode/function selection register/external output hold or clear setting area	0	
	RWwm+B	7	Coincidence output point No. 1	0		Coincidence output point No. 1	0	
	RWwm+C	7	setting area			setting area		
	RWwm+D		Sampling/cycle time setting area	0		Sampling/cycle time setting area	0	
	RWwm+E		Coincidence output point No. 2	0		Coincidence output point No. 2	0	
	RWwm+F	1	setting area			setting area		
Remote → Master	RWrn	CH1	Present value storage area	0	CH1	Present value storage area	0	
	RWrn+1	7			_			
	RWrn+2		Latch count value/sampling count	0		Latch count value/sampling count	0	
	RWrn+3		value/periodic pulse count previous value storage area			value/periodic pulse count previous value storage area		
	RWrn+4		Periodic pulse count present value	0		Periodic pulse count present value	0	
	RWrn+5		storage area			storage area		
	RWrn+6	CH1, CH2	Sampling/periodic counter flag storage area (for both CH1 and CH2)	0	CH1, CH2	Sampling/periodic counter flag storage area (for both CH1 and CH2)	0	
	RWrn+7	_	Use prohibited	_	_	Use prohibited	_	
	RWrn+8	CH2	Present value storage area	0	CH2	Present value storage area	0	
	RWrn+9	7						
	RWrn+A	7	Latch count value/sampling count	0	1	Latch count value/sampling count	0	
	RWm+B		value/periodic pulse count previous value storage area			value/periodic pulse count previous value storage area		
	RWrn+C		Periodic pulse count present value	0		Periodic pulse count present value	0	
	RWrn+D	7	storage area			storage area		
	RWrn+E	_	Use prohibited	_	_	Use prohibited	_	
	RWrn+F							

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■External appearance, terminal block



- *1 AJ65BT-D62D has EQU2 of LED indication.
- *2 The terminal layout is different. Change the wiring. The following figure shows the pin numbers of terminal block and the terminal layout of AJ65BT-D62D-S1 and AJ65BT-D62D.



Pin number	Signal name							
	Model to be dis	continued		Alternative	model			
	AJ65BT-D62D-S	S1		AJ65BT-D6	AJ65BT-D62D			
1	DA			DA				
2	DB			DB				
3	DG			DG				
4	SLD			SLD				
5	24V			24V				
6	F.G.			F.G.				
7	24G			24G				
8	CH1	φА	А	CH1	φА	А		
9	- -		Ā			Ā		
10		φВ	В		φВ	В		
11			B			B		
12		PRESET	'		PRESET			
13		PRESET			COM			
14		F.START			F.START			
15				CH2	φА	А		
16	CH2	φА	А			Ā		
17			Ā		φВ	В		
18		φВ	В			B		
19			B		PRESET			
20		PRESET			СОМ			
21		PRESET			F.START			
22	F.START			CH1	EQU1	EQU1		
23					EQU2			
24	CH1	EQU1		CH2	EQU1			
25	CH2	EQU1			EQU2			
26	12/24V			12/24V	12/24V			
27	COM			СОМ				

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REVISIONS

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