

TECHNICAL BULLETIN

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FA-A-0315-A

Production Discontinuation of MELSEC-Q Series Redundant CPU and Peripherals

■Date of Issue

October 2020

■Relevant Models

Q12PRHCPU, Q25PRHCPU, Q65WRB, QC10TR, QC30TR

Thank you for your continued support of Mitsubishi Electric programmable controllers, MELSEC-Q series. Production of the following MELSEC-Q series models will be discontinued.

1 LIST OF MODELS TO BE DISCONTINUED

Product	Model
Redundant CPU	Q12PRHCPU, Q25PRHCPU
Redundant type extension base unit	Q65WRB
Tracking cable for the Redundant CPU	QC10TR, QC30TR

2 SCHEDULE

• Transition to made-to-order: September 30, 2021

· Order acceptance: Until August 31, 2022

• Production discontinuation: September 30, 2022

3 REASON FOR DISCONTINUATION

Some parts of the above products are now obsolete, and we will have difficulty to maintain our production system.

4 REPAIR SUPPORT

Repair support period: Until September 28, 2029 (for seven years after the discontinuation of production)

5 LIST OF ALTERNATIVE MODELS

Please replace the models to be discontinued with the alternative models as follows.

Model to be discontinued		Alternative model	
Product	Model	Product	Model
Redundant CPU	Q12PRHCPU	Process CPU + redundant function module	R16PCPU + R6RFM*1
	Q25PRHCPU		R32PCPU + R6RFM*1
Redundant type extension base unit	Q65WRB	Redundant extension base unit	R68WRB ^{*2}
Tracking cable for the Redundant CPU	QC10TR	Optical fiber cable for CC-Link IE	QG series*3(Mitsubishi Electric System
	QC30TR	Controller Network	& Service Co., Ltd.)

^{*1} To use the redundant system with the RnPCPU, a separate redundant function module R6RFM is required.

- IEEE802.3 (1000BASE-SX)
- IEC60793-2-10 Types A1a.1

^{*2} When using two extension cables, connect the redundant extension base units to the extension level 2 to 7. When using an extension cable, connect the extension base units other than the redundant extension base units to the extension level 2 to 7. RQ extension base units, however, cannot be used.

^{*3} In addition, optical fiber cables compliant with the following standards (multimode optical fiber (GI)) can be used.

6 RECOMMENDED ALTERNATIVE MODELS FOR THE MODELS TO BE DISCONTINUED

Consider using the MELSEC iQ-R series redundant system with the models listed below when replacing the MELSEC-Q series redundant system.

Model to be discontinued	Alternative model	Alternative model		
Model	Model	Performance specifications		
Q12PRHCPU	R16PCPU + R6RFM	 Program capacity: 124K steps → 160K steps Standard RAM capacity: 256K bytes → Device/label memory capacity: 1720K bytes Standard ROM capacity: 496K bytes → Data memory: 10M bytes Communication interface: USB (connector type B)/RS-232 → USB (connector type miniB)*1/Ethernet Memory card: SRAM/Flash/ATA card → SD memory card/Extended SRAM cassette*2 		
Q25PRHCPU	R32PCPU + R6RFM	 Program capacity: 252K steps → 320K steps Standard RAM capacity: 256K bytes → Device/label memory capacity: 2316K bytes Standard ROM capacity: 1008K bytes → Data memory: 20M bytes Communication interface: USB (connector type B)/RS-232 → USB (connector type miniB)*1/Ethernet Memory card: SRAM/Flash/ATA card → SD memory card/extended SRAM cassette*2 		
Q65WRB	R68WRB	 Number of slots: 5 → 8 Number of extension connectors: IN side: 2 slots, OUT side: 1 slot → IN side: 2 slots, OUT side: 2 slots 		
QC10TR	QG series	Cable specifications: Dedicated cable → Optical fiber cable compliant with the following standards (multimode optical fiber (GI))		
QC30TR	IEEE802.3 (1000BASE-SX) IEC60793-2-10 Types A1a.1 Maximum cable length: 3m → 550m			

^{*1} Since the connector type differs, replacement of the cable or a conversion adapter is required. For cables and conversion adapters, refer to the following.

List of cables and converters for connection with peripheral devices (recommended product) (FA-A-0036)

^{*2} If the file register file is stored in an SRAM/Flash card, an extended SRAM cassette is required for the MELSEC iQ-R series. (It is not required if the device/label memory has enough capacity for the number of file register points.)

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REVISIONS

Version	Date of Issue	Revision
A	October 2020	First edition