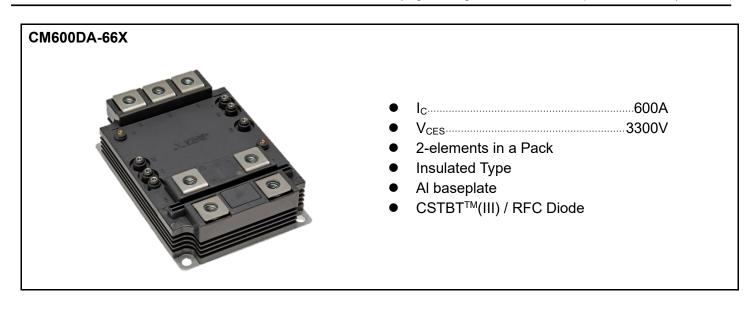


< High Voltage Insulated Gate Bipolar Transistor: HVIGBT >

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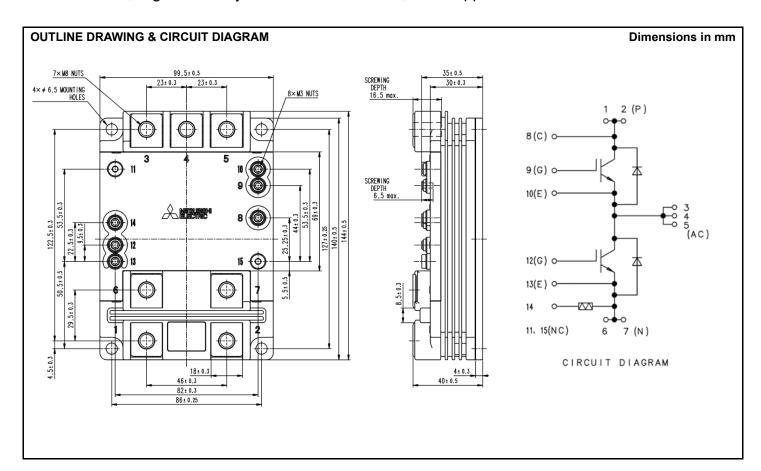
HIGH POWER SWITCHING USE INSULATED TYPE

5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules



APPLICATION

Traction drives, High Reliability Converters / Inverters, DC choppers



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HIGH POWER SWITCHING USE INSULATED TYPE

5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

MAXIMUM RATINGS

Symbol	Item	Conditions	Ratings	Unit
V	Collector-emitter voltage	$V_{GE} = 0V, T_j = -40+150$ °C	3300	V
V _{CES}		$V_{GE} = 0V, T_j = -50^{\circ}C$	3200	V
V_{GES}	Gate-emitter voltage	$V_{CE} = 0V, T_j = 25^{\circ}C$	± 20	V
Ic	Collector ourrent	DC, T _c = 109°C	600	Α
I _{CRM}	Collector current	Pulse (Note 1)	1200	Α
I _E	Emittor current (Note 2)	DC, T _c = 90°C	600	Α
I _{ERM}	Emitter current (Note 2)	Pulse (Note 1)	1200	Α
P _{tot}	Maximum power dissipation (Note 3)	T _c = 25°C, IGBT part	6000	W
V _{iso}	Isolation voltage	RMS, sinusoidal, f = 60Hz, t = 1 min., T _C = 25°C	6000	V
Q_{PD}	Partial discharge	Charged part to the baseplate V1 = 3500 Vrms, V2 = 2600 Vrms AC 60 Hz, T _c = 25 °C (acc. to IEC 61287)	10	pC
V _e	Partial discharge extinction voltage	RMS, sinusoidal, f = 60Hz, Q _{PD} ≤ 10 pC., T _C = 25°C	2600	V
Tj	Junction temperature		− 50 ~ + 150	°C
T _{jop}	Operating junction temperature		−50 ~ +150	°C
T_{stg}	Storage temperature		−55 ~ +150	°C
t _{psc}	Short circuit pulse width	$V_{CC} = 2400V, V_{CE} \le V_{CES}, V_{GE} = 15V, T_j = 150^{\circ}C$ $R_{g(on)} = 2.2\Omega, R_{g(off)} = 51\Omega, C_{GE} = 33nF$	10	μS

ELECTRICAL CHARACTERISTICS

Symbol	Itom	Conditions			Unit		
Symbol	Item			Min	Тур	Max	Unit
			T _i = 25°C	_	_	2.0	
I _{CES}	Collector cutoff current	$V_{CE} = V_{CES}, V_{GE} = 0V$	T _i = 125°C	_	2.0	_	mA
			T _i = 150°C		20.0	_	
$V_{GE(th)}$	Gate-emitter threshold voltage	$V_{CE} = 10 \text{ V}, I_{C} = 60 \text{ mA}, T_{j} = 25^{\circ}\text{C}$		6.5	7.0	7.5	V
I_{GES}	Gate leakage current	$V_{GE} = V_{GES}, V_{CE} = 0V, T_{j} = 25^{\circ}C$		-0.5	-	0.5	μΑ
C _{ies}	Input capacitance	// - 10 // // - 0 // f - 100 kHz			53.4	_	nF
C _{oes}	Output capacitance	$V_{CE} = 10 \text{ V}, V_{GE} = 0 \text{ V}, f = 100 \text{ kHz}$		_	3.8	_	nF
C _{res}	Reverse transfer capacitance	T _j = 25°C		_	0.5	_	nF
Q_{G}	Total gate charge	$V_{CC} = 1800V$, $I_{C} = 600A$, $V_{GE} = \pm 15V$		_	3.6	_	μC
		L COO A (Note 4)	T _i = 25°C	_	2.30	_	
V_{CEsat}	Collector-emitter saturation voltage	$I_C = 600 \text{ A}^{\text{(Note 4)}}$	T _i = 125°C		2.80	3.20	V
		V _{GE} = 15 V	T _i = 150°C	_	2.90	3.30	
	Turn-on delay time		T _i = 125°C	_	_	1.25	μs
$t_{d(on)}$			T _i = 150°C	_	_	1.25	
	Rise time	$V_{CC} = 1800 \text{ V}$ $I_{C} = 600 \text{ A}$ $V_{GE} = \pm 15 \text{ V}$ $R_{G(on)} = 2.2 \Omega$	T _i = 125°C		_	0.50	μs
t _r			T _i = 150°C		_	0.50	
	Turn-on switching energy (Note 5) per pulse		T _i = 25°C		0.76	_	J
E _{on(10%)}			T _i = 125°C		0.92	_	
(- ,		$L_s = 65 \text{nH}$	T _i = 150°C		0.93	_	
	Turn-on switching energy (Note 6) per pulse	Inductive load	T _i = 25°C		0.82	_	J
Eon		C _{GE} = 33 nF	T _i = 125°C		0.99	_	
			T _i = 150°C		1.00	_	
			T _i = 25°C		3.40	_	
$t_{d(off)}$	Turn-off delay time		T _i = 125°C		3.60	5.00	μs
			T _i = 150°C	_	3.65	5.00	
		V _{CC} = 1800 V	T _i = 25°C		0.23	_	
t _f	Fall time	$I_{\rm C} = 600 {\rm A}$	T _i = 125°C		0.33	1.00	μs
		$V_{GE} = \pm 15 \text{ V}$	T _i = 150°C		0.35	1.00	
	T (No. 1)	$R_{G(off)} = 51 \Omega$	T _i = 25°C	_	0.67	_	
E _{off(10%)}	Turn-off switching energy (Note 5)	$L_s = 65 \text{nH}$	T _i = 125°C	_	0.91	_	J
,	per pulse	Inductive load	T _i = 150°C	_	0.92	_	1
	- C	C _{GE} = 33 nF	T _i = 25°C	_	0.76	_	
E _{off}	Turn-off switching energy (Note 6)		T _i = 125°C	_	1.03	_	J
OII	per pulse		T _i = 150°C	_	1.04	_	

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HIGH POWER SWITCHING USE INSULATED TYPE

5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

ELECTRICAL CHARACTERISTICS (continuation)

Cumbal	Item		Conditions		Limits			Unit
Symbol					Min	Тур	Max	Offic
			(Note 4)	T _j = 25°C	_	2.10	_	
V_{EC}	Emitter-collector voltage	(Note 2)	$I_E = 600 \text{ A}^{\text{(Note 4)}}$	T _j = 125°C	_	2.30	2.80	V
			$V_{GE} = 0 V$	T _j = 150°C	_	2.40	2.90	
				T _i = 25°C	_	0.55	_	
t _{rr}	Reverse recovery time	(Note 2)		T _i = 125°C	_	0.65	_	μs
				T _j = 150°C	1	0.70	_	
				T _i = 25°C	_	1170	_	Α
Irr	Reverse recovery current (Note 2)	(Note 2)	V 4000 V	T _j = 125°C	-	1120	_	
				T _j = 150°C	1	1100	_	
	Reverse recovery charge (Note 2,7)			T _j = 25°C	_	620	_	μC
Q _{rr(10%)}		(Note 2,7)		T _j = 125°C	_	740	_	
			$V_{GE} = \pm 15 \text{ V}$	T _i = 150°C	_	770		
	Reverse recovery charge (Note 2,6)		$R_{G(on)} = 2.2 \Omega$ $L_s = 65 \text{ nH}$ Inductive load	$T_j = 25^{\circ}C$	_	650		
Q_{rr}		(Note 2,6)		T _j = 125°C	_	805		
		C _{GE} = 33 nF	T _i = 150°C	_	845			
	Poverse receivery energy	(Note 2,5)	OGE - 33 III	$T_j = 25^{\circ}C$	_	0.66	_	
E _{rec(10%)}	Reverse recovery energy		T _j = 125°C	_	0.88		J	
	per pulse			T _j = 150°C	_	0.91		
	Poverse recovery energy	(Note 2,6)		T _j = 25°C	_	0.75	_	
E _{rec}	reverse recovery energy	(=,0)		T _j = 125°C	_	1.01	_	J
	per pulse			T _j = 150°C	_	1.03	_	

THERMAL CHARACTERISTICS

Coursels al	ltem	Conditions		Limits		
Symbol				Тур	Max	Unit
$R_{th(j-c)Q}$	Thermal registeres	Junction to Case, IGBT part , 1/2 module		_	20.5	K/kW
$R_{th(j-c)D}$	Thermal resistance	Junction to Case, FWDi part, per 1/2 module		_	34.0	K/kW
R _{th(c-s)}	Contact thermal resistance	Case to heat sink, 1/2 module λ_{grease} = 1W/m*k, D _(c-s) = 70 μ m		16.0	_	K/kW

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HIGH POWER SWITCHING USE INSULATED TYPE

5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

NTC THERMISTOR PART

Comple al	lkana	Conditions	Limits			1.124
Symbol	Item		Min	Тур	Max	Unit
R ₂₅	Zero-power resistance	T _c =25°C	-	5.00	1	kΩ
B _(25/50)	B-constant (Note 8)	Approximate by equation		3375	•	K

MECHANICAL CHARACTERISTICS

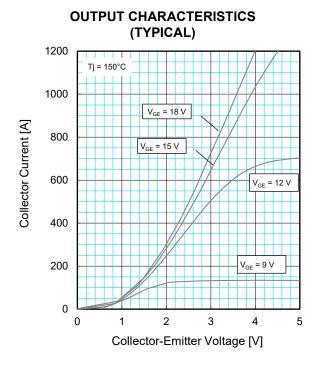
Commando and	Item	Conditions	Limits			1.124
Symbol		Conditions	Min	Тур	Max	Unit
Mt		Main terminals screw M8	7.0	_	14.0	N·m
Ms	Mounting torque	Mounting screw M6	3.0	_	6.0	N·m
M_t		Auxiliary terminals screw M3	0.4	_	0.8	N·m
m	Mass		_	0.75	_	kg
CTI	Comparative tracking index		600	_	_	-
d _a	Clearance	Between terminals and baseplate	19.5	_	_	mm
ds	Creepage distance	Between terminals and baseplate	32.0	_	_	mm
L _{P P-N}	Parasitic stray inductance	Between terminal 1, 2 and terminal 6, 7	_	10.0	_	nΗ
R _{CC'+EE'}	Internal lead resistance	T _C = 25 °C, 1/2 module	_	0.41	_	mΩ
r_g	Internal gate resistance	T _C = 25 °C	_	0.83	_	Ω

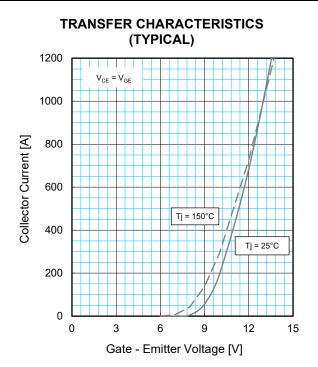
Note1. Pulse width and repetition rate should be such that junction temperature (Tj) does not exceed Tjopmax rating.

- 2. The symbols represent characteristics of the anti-parallel, emitter to collector free-wheel diode (FWD_i).
- 3. Junction temperature (T_j) should not exceed T_{jmax} rating (150°C).
- 4. Pulse width and repetition rate should be such as to cause negligible temperature rise.
- 5. The integration range of switching energies is from $10\% V_{CE}$ to $10\% I_{C}(10\% I_{E}).$
- 6. Definition of all items is according to IEC 60747, unless otherwise specified.
- 7. The integration range of reverse recovery charge is from I_E = 0A to 10% I_E .

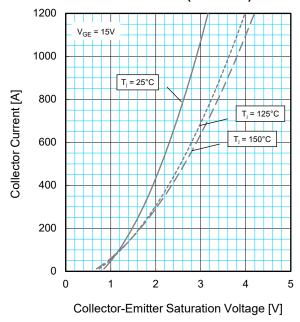
8.
$$B_{(25/50)} = \ln\left(\frac{R_{25}}{R_{50}}\right) / \left(\frac{1}{T_{25}} - \frac{1}{T_{50}}\right)$$

 $R_{25}\text{: resistance at absolute temperature } T_{25}\text{ [K]; } T_{25}\text{ = }25\text{[°C]} \text{ + }273.15\text{ = }298.15\text{[K]} \\ R_{50}\text{: resistance at absolute temperature } T_{25}\text{ [K]; } T_{50}\text{ = }50\text{[°C]} \text{ + }273.15\text{ = }323.15\text{[K]} \\$

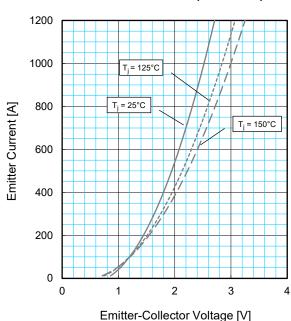




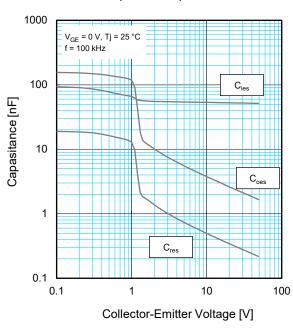
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



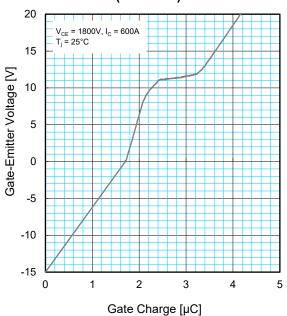
FREE-WHEEL DIODE FORWARD CHARACTERISTICS (TYPICAL)



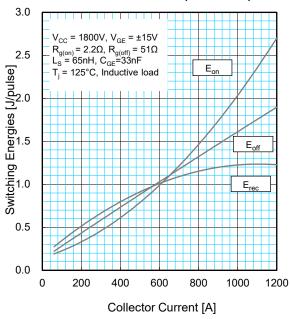
CAPACITANCE CHARACTERISTICS (TYPICAL)



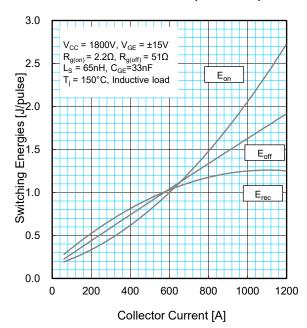
GATE CHARGE CHARACTERISTICS (TYPICAL)



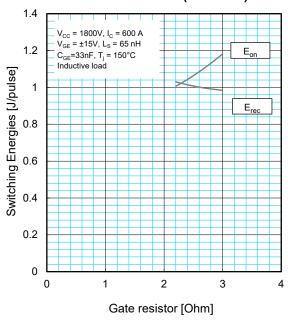
HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



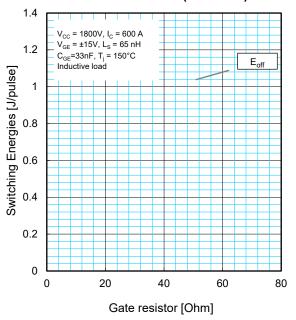
HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



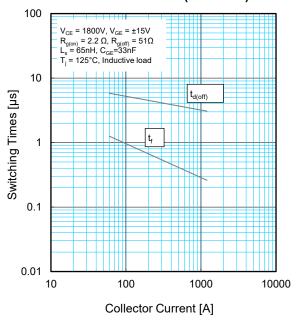
HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



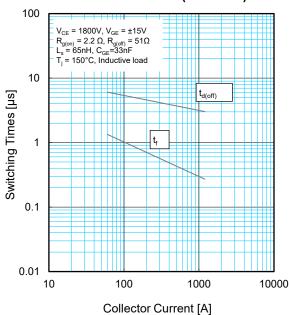
HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



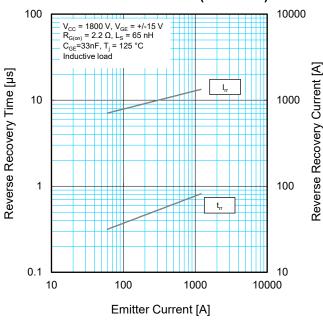
HALF-BRIDGE SWITCHING TIME CHARACTERISTICS (TYPICAL)



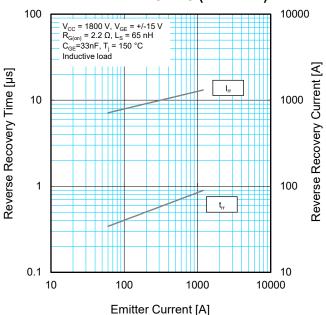
HALF-BRIDGE SWITCHING TIME CHARACTERISTICS (TYPICAL)



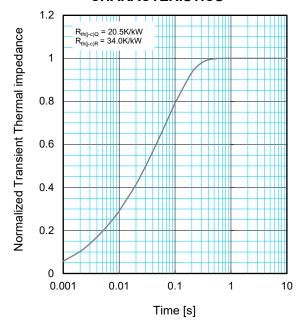
FREE-WHEEL DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



FREE-WHEEL DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



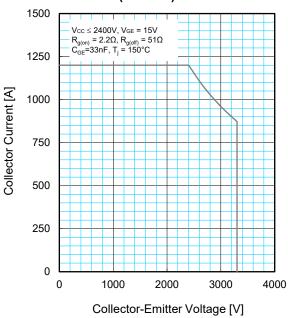
TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS



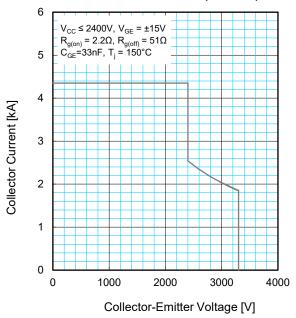
$$Z_{th(j-c)}(t) = \sum_{i=1}^{n} R_{i} \left\{ 1 - exp^{\left(-\frac{t}{\tau_{i}}\right)} \right\}$$

	1	2	3	4
R_i/R_{th} :	0.0292	0.0832	0.2277	0.6599
τ _i [sec.] :	0.0025	0.0027	0.0155	0.0865

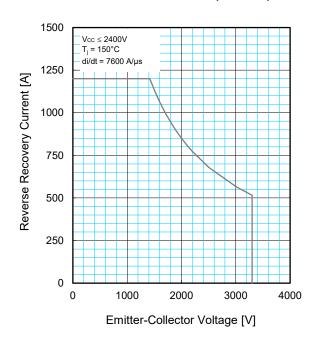
REVERSE BIAS SAFE OPERATING AREA (RBSOA)



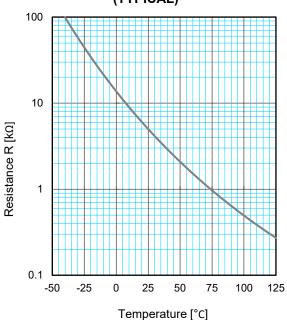
SHORT CIRCUIT SAFE OPERATING AREA (SCSOA)



FREE-WHEEL DIODE REVERSE RECOVERY SAFE OPERATING AREA (RRSOA)



NTC THERMISTOR TEMPERATURE CHARACTERISTICS (TYPICAL)



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HIGH POWER SWITCHING USE INSULATED TYPE

5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

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5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

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